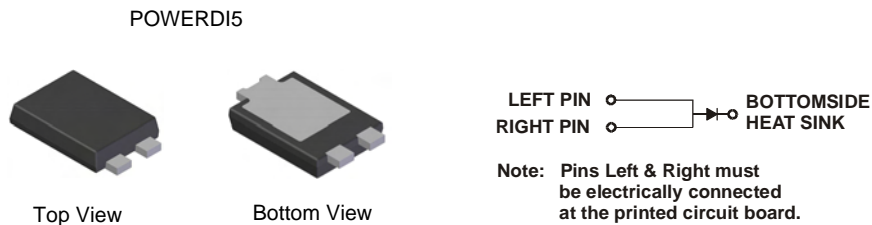


Features

- Guard Ring Die Construction for Transient Protection
- Low Forward Voltage Drop
- For Use in Low Voltage, High Frequency Inverters, Free Wheeling, and Polarity Protection Applications
- High Forward Surge Current Capability
- **Lead-Free Finish; RoHS Compliant (Notes 1 & 2)**
- **Halogen and Antimony Free. "Green" Device (Note 3)**
- **Qualified to AEC-Q101 Standards for High Reliability**

Mechanical Data

- Case: POWERDI5
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish – Matte Tin annealed over Copper leadframe. Solderable per MIL-STD-202, Method 208
- Polarity: See Diagram
- Weight: 0.094 grams (approximate)

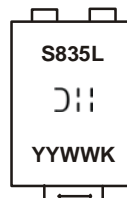


Ordering Information (Note 4)

| Part Number | Case | Packaging |
|-------------|----------|------------------|
| PDS835L-13 | POWERDI5 | 5000/Tape & Reel |
| PDS835L-7 | POWERDI5 | 1500/Tape & Reel |

- Notes:
1. EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant. All applicable RoHS exemptions applied.
 2. See <http://www.diodes.com> for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
 4. For packaging details, go to our website at <http://www.diodes.com>.

Marking Information



- S835L = Product type marking code
- DII = Manufacturers' code marking
- YYWW = Date code marking
- YY = Last two digits of year (ex: 04 for 2004)
- WW = Week code (01 - 53)
- K = Factory Designator

Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Single phase, half wave, 60Hz, resistive or inductive load.
For capacitance load, derate current by 20%.

| Characteristic | Symbol | Value | Unit |
|---|---------------------|-------|------|
| Peak Repetitive Reverse Voltage | V _{RRM} | 35 | V |
| Working Peak Reverse Voltage | V _{RWM} | | |
| DC Blocking Voltage | V _R | | |
| RMS Reverse Voltage | V _{R(RMS)} | 25 | V |
| Average Rectified Output Current (See also figure 5) | I _O | 8 | A |
| Non-Repetitive Peak Forward Surge Current 8.3ms Single Half Sine-Wave Superimposed on Rated Load | I _{FSM} | 120 | A |

Thermal Characteristics

| Characteristic | Symbol | Typ | Max | Unit |
|--|------------------|-------------|-----|------|
| Thermal Resistance Junction to Soldering Point | R _{θJS} | — | 3.0 | °C/W |
| Thermal Resistance Junction to Ambient Air (Note 5) T _A = +25°C | R _{θJA} | 100 | — | °C/W |
| Thermal Resistance Junction to Ambient Air (Note 6) T _A = +25°C | R _{θJA} | 65 | — | °C/W |
| Thermal Resistance Junction to Ambient Air (Note 7) T _A = +25°C | R _{θJA} | 45 | — | °C/W |
| Operating Temperature Range | T _J | -65 to +125 | | °C |
| Storage Temperature Range | T _{STG} | -65 to +150 | | °C |

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

| Characteristic | Symbol | Min | Typ | Max | Unit | Test Condition |
|------------------------------------|--------------------|-----|------|------|------|---|
| Reverse Breakdown Voltage (Note 8) | V _{(BR)R} | 35 | — | — | V | I _R = 1mA |
| Forward Voltage | V _F | — | 0.46 | 0.51 | V | I _F = 8A, T _S = +25°C I _F = 8A, T _S = +125°C |
| Reverse Leakage Current (Note 8) | I _R | — | 0.05 | 1.4 | mA | T _S = +25°C, V _R = 35V T _S = +100°C, V _R = 35V |

- Notes:
5. FR-4 PCB, 2 oz. Copper, minimum recommended pad layout per <http://www.diodes.com>
 6. Polyimide PCB, 2 oz. Copper, minimum recommended pad layout per <http://www.diodes.com>.
 7. Polyimide PCB, 2 oz. Copper. Cathode pad dimensions 9.4mm x 7.2mm. Anode pad dimensions 2.7mm x 1.6mm.
 8. Short duration pulse test used to minimize self-heating effect.

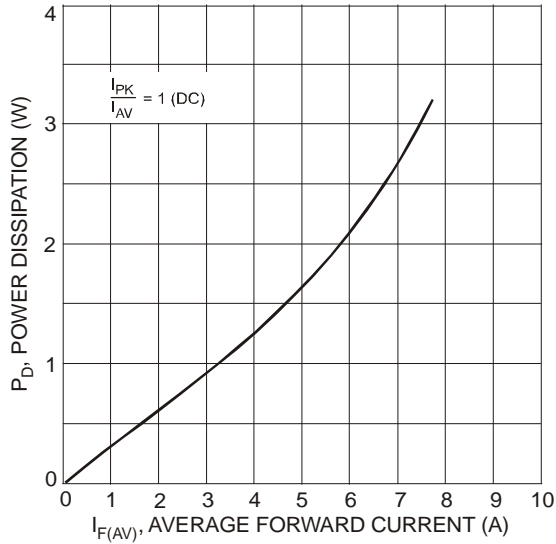


Fig. 1 Forward Power Dissipation

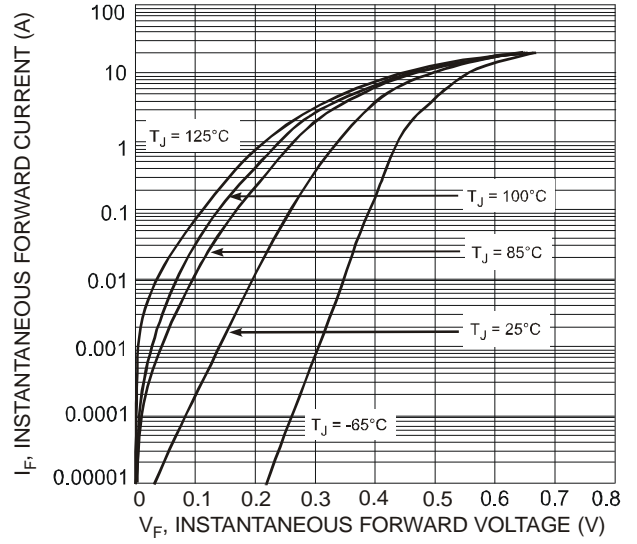


Fig. 2 Typical Forward Characteristics

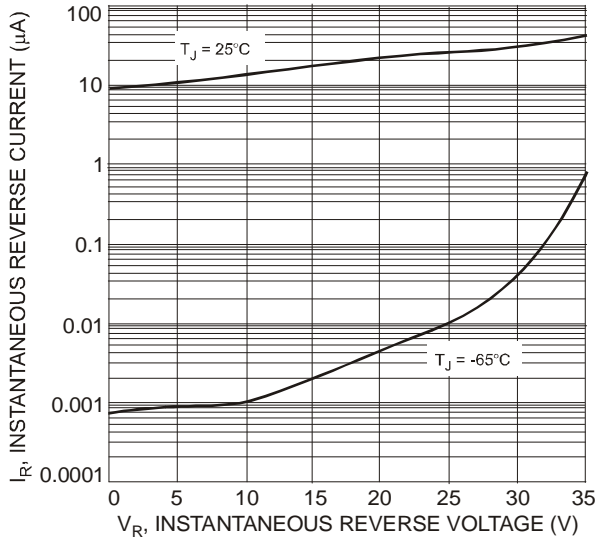


Fig. 3 Typical Reverse Characteristics

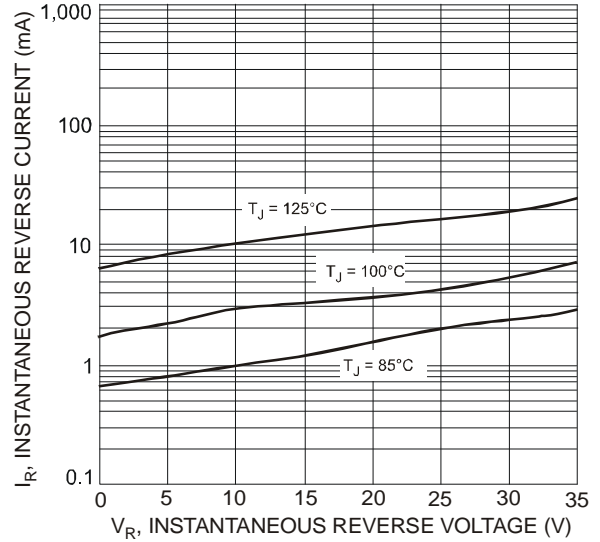


Fig. 4 Typical Reverse Characteristics

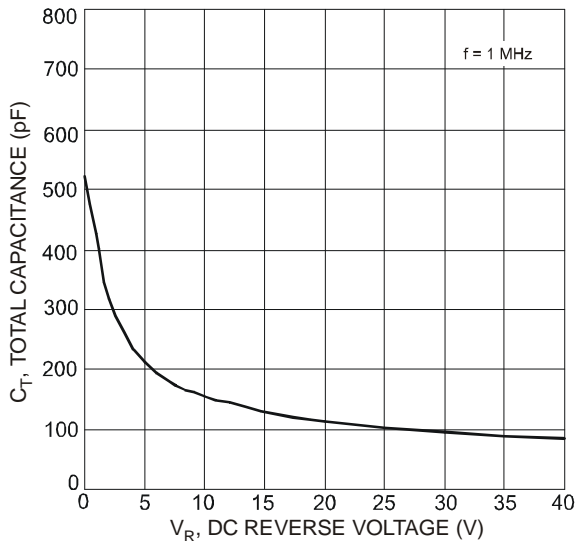


Fig. 5 Total Capacitance vs. Reverse Voltage

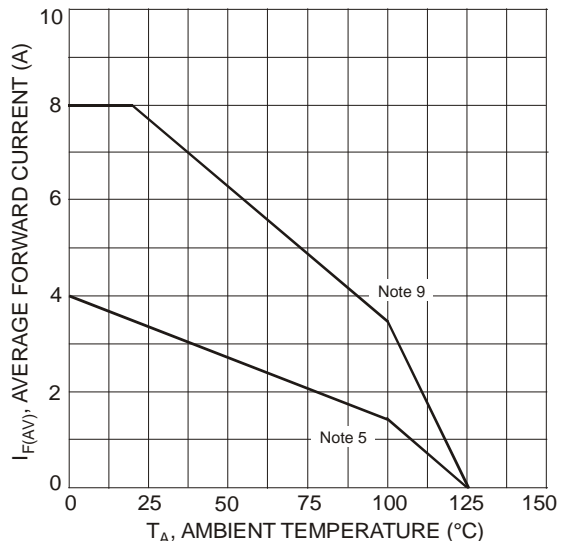


Fig. 6 Forward Current Derating Curve

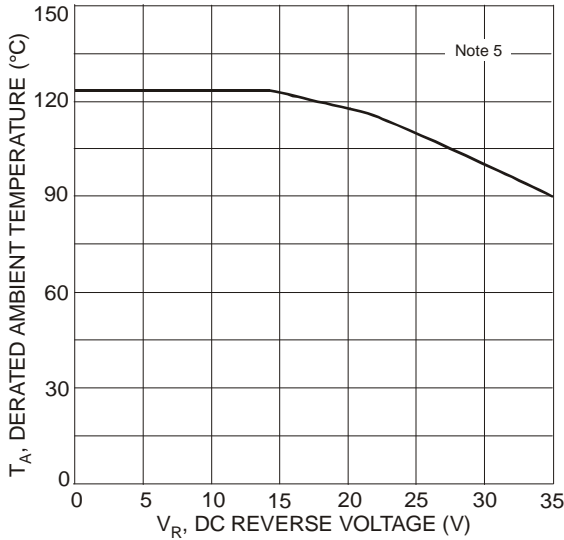
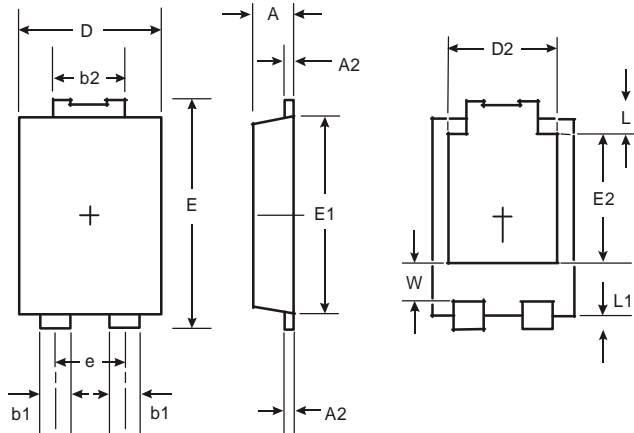


Fig. 7 Operating Temperature Derating

Notes: 9. Polyimide PCB, 2 oz. Copper. Cathode pad dimensions 18.8mm x 14.4mm. Anode pad dimensions 5.6mm x 3.0mm.

Package Outline Dimensions

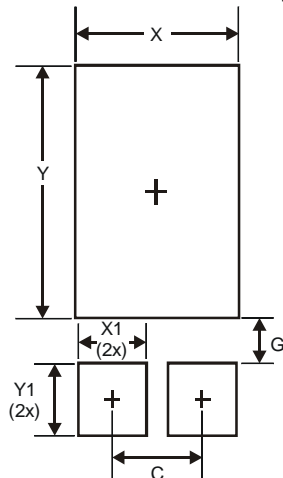
Please see AP02002 at <http://www.diodes.com/datasheets/ap02002.pdf> for latest version.



| POWERDI5 | | |
|----------------------|-----------|------|
| Dim | Min | Max |
| A | 1.05 | 1.15 |
| A2 | 0.33 | 0.43 |
| b1 | 0.80 | 0.99 |
| b2 | 1.70 | 1.88 |
| D | 3.90 | 4.05 |
| D2 | 3.054 Typ | |
| E | 6.40 | 6.60 |
| e | 1.84 Typ | |
| E1 | 5.30 | 5.45 |
| E2 | 3.549 Typ | |
| L | 0.75 | 0.95 |
| L1 | 0.50 | 0.65 |
| W | 1.10 | 1.41 |
| All Dimensions in mm | | |

Suggested Pad Layout

Please see AP02001 at <http://www.diodes.com/datasheets/ap02001.pdf> for the latest version.



| Dimensions | Value (in mm) |
|------------|---------------|
| C | 1.840 |
| G | 0.852 |
| X | 3.360 |
| X1 | 1.390 |
| Y | 4.860 |
| Y1 | 1.400 |

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