

Product Summary

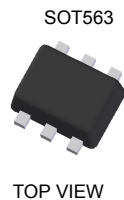
$V_{(BR)DSS}$	$R_{DS(on) max}$	I_D $T_A = 25^\circ C$
-50V	10 Ω @ $V_{GS} = -5V$	-130mA

Description

This MOSFET has been designed to minimize the on-state resistance ($R_{DS(on)}$) and yet maintain superior switching performance, making it ideal for high efficiency power management applications.

Applications

- General Purpose Interfacing Switch
- Power Management Functions
- Analog Switch

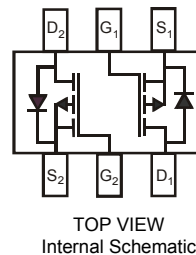


Features and Benefits

- Low On-Resistance
- Low Gate Threshold Voltage
- Low Input Capacitance
- Fast Switching Speed
- **Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)**
- **Halogen and Antimony Free. "Green" Device (Note 3)**
- **Qualified to AEC-Q101 Standards for High Reliability**

Mechanical Data

- Case: SOT563
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020C
- Terminals: Finish — Matte Tin annealed over Copper leadframe. Solderable per MIL-STD-202, Method 208 $\text{\textcircled{e3}}$
- Terminal Connections: See Diagram
- Weight: 0.006 grams (approximate)

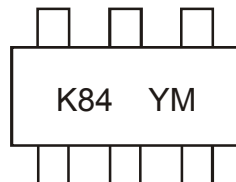


Ordering Information (Note 4)

Part Number	Case	Packaging
BSS84V-7	SOT563	3000/Tape & Reel

- Notes:
1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
 2. See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
 4. For packaging details, go to our website at <http://www.diodes.com/products/packages.html>.

Marking Information



K84 = Product Type Marking Code
 YM = Date Code Marking
 Y = Year ex: S = 2005
 M = Month ex: 9 = September

Date Code Key

Year	2005	2006	2007	2008	2009	2010	2011	2012
Code	S	T	U	V	W	X	Y	Z

Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	O	N	D

Maximum Ratings (@ $T_A = +25^\circ\text{C}$, unless otherwise specified.)

Characteristic	Symbol	Value	Units
Drain-Source Voltage	V_{DSS}	-50	V
Drain-Gate Voltage (Note 5)	V_{DGR}	-50	V
Gate-Source Voltage	V_{GSS}	± 20	V
Drain Current (Note 6)	I_D	-130	mA

Thermal Characteristics (@ $T_A = +25^\circ\text{C}$, unless otherwise specified.)

Characteristic	Symbol	Value	Units
Total Power Dissipation	P_D	150	mW
Thermal Resistance, Junction to Ambient	$R_{\theta JA}$	833	$^\circ\text{C}/\text{W}$
Operating and Storage Temperature Range	T_j, T_{STG}	-55 to +150	$^\circ\text{C}$

Electrical Characteristics (@ $T_A = +25^\circ\text{C}$, unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 7)						
Drain-Source Breakdown Voltage	BV_{DSS}	-50	-75	—	V	$V_{GS} = 0\text{V}, I_D = -250\mu\text{A}$
Zero Gate Voltage Drain Current	I_{DSS}	—	—	-1	μA	$V_{DS} = -50\text{V}, V_{GS} = 0\text{V}, T_J = +25^\circ\text{C}$
		—	—	-2	μA	$V_{DS} = -50\text{V}, V_{GS} = 0\text{V}, T_J = +125^\circ\text{C}$
		—	—	-100	nA	$V_{DS} = -25\text{V}, V_{GS} = 0\text{V}, T_J = +25^\circ\text{C}$
Gate-Body Leakage	I_{GSS}	—	—	± 50	nA	$V_{GS} = \pm 20\text{V}, V_{DS} = 0\text{V}$
ON CHARACTERISTICS (Note 7)						
Gate Threshold Voltage	$V_{GS(th)}$	-0.8	-1.6	-2.0	V	$V_{DS} = V_{GS}, I_D = -1\text{mA}$
Static Drain-Source On-Resistance	$R_{DS(ON)}$	—	2	10	Ω	$V_{GS} = -5\text{V}, I_D = -0.100\text{A}$
Forward Transconductance	g_{FS}	0.05	—	—	S	$V_{DS} = -25\text{V}, I_D = -0.1\text{A}$
DYNAMIC CHARACTERISTICS						
Input Capacitance	C_{iss}	—	—	45	pF	$V_{DS} = -25\text{V}, V_{GS} = 0\text{V}, f = 1.0\text{MHz}$
Output Capacitance	C_{oss}	—	—	25	pF	
Reverse Transfer Capacitance	C_{rss}	—	—	12	pF	
SWITCHING CHARACTERISTICS						
Turn-On Delay Time	$t_{D(ON)}$	—	10	—	ns	$V_{DD} = -30\text{V}, I_D = -0.27\text{A}$,
Turn-Off Delay Time	$t_{D(OFF)}$	—	18	—	ns	$R_{GEN} = 50\Omega, V_{GS} = -10\text{V}$

- Notes:
- $R_{GS} \leq 20\text{K}\Omega$.
 - Device mounted on FR-4 PCB, 1 inch x 0.85 inch x 0.062 inch; pad layout as shown on Diodes Inc. suggested pad layout document AP02001, which can be found on our website at <http://www.diodes.com/datasheets/ap02001.pdf>.
 - Short duration pulse test used to minimize self-heating effect.

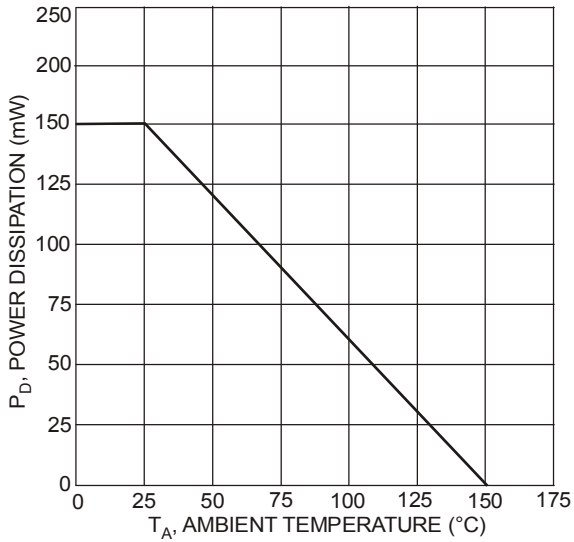


Fig. 1 Max Power Dissipation vs. Ambient Temperature

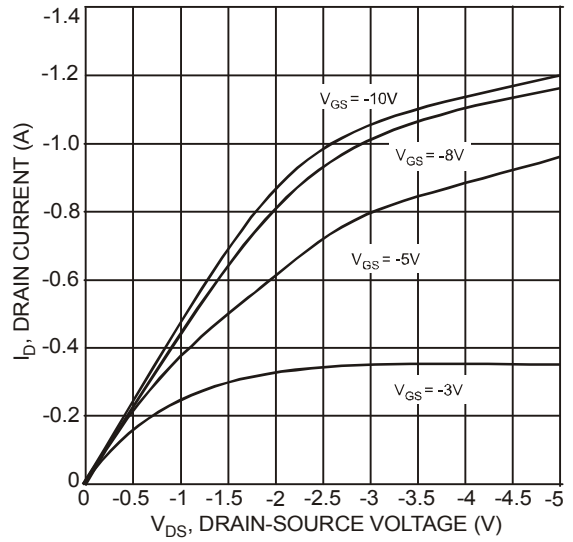


Fig. 2 Typical Output Characteristics

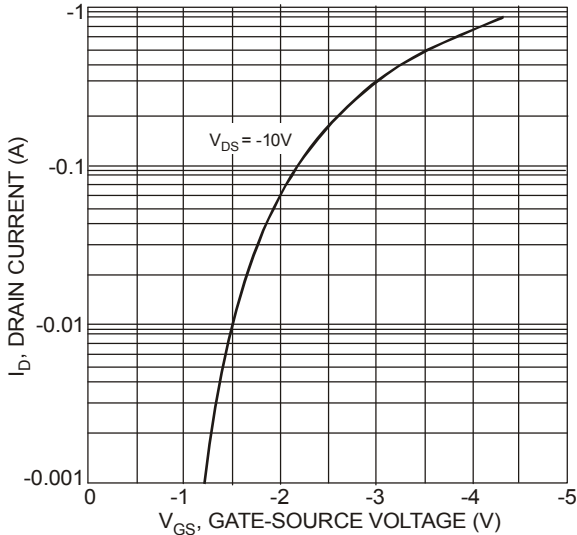


Fig. 3 Typical Transfer Characteristics

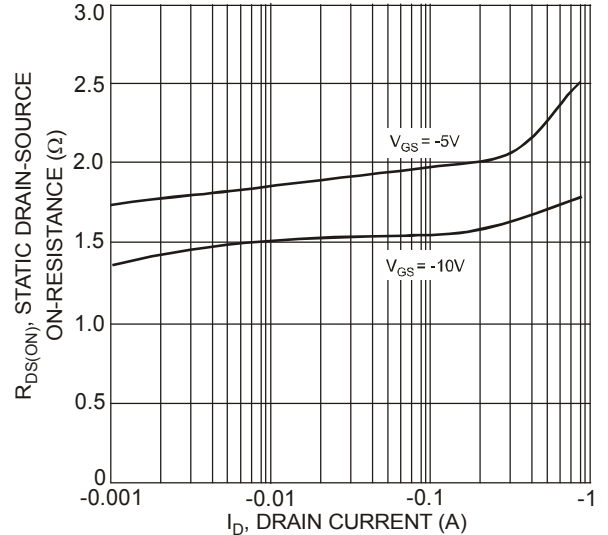


Fig. 4 Static Drain-Source On-Resistance vs. Drain Current

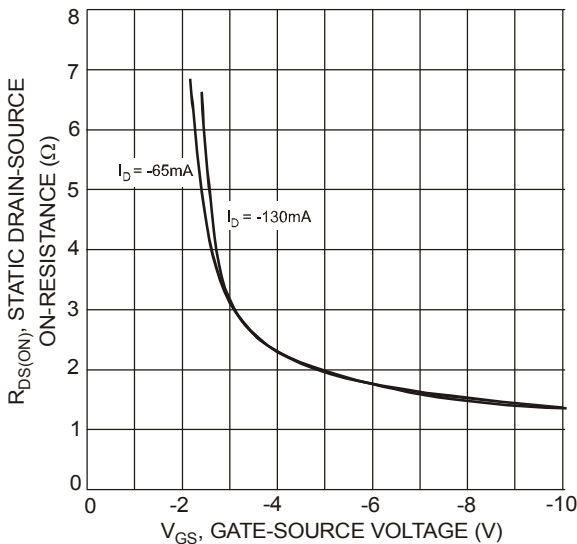


Fig. 5 Static Drain-Source On-Resistance vs. Gate-Source Voltage

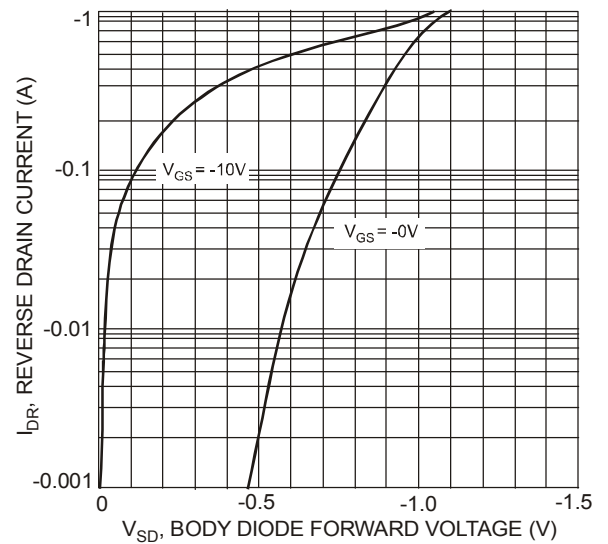
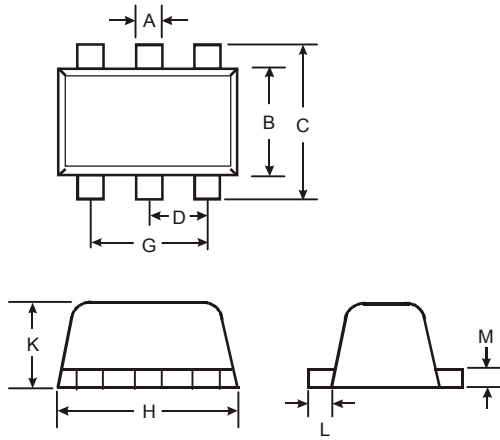


Fig. 6 Reverse Drain Current vs. Body Diode Forward Voltage

Package Outline Dimensions

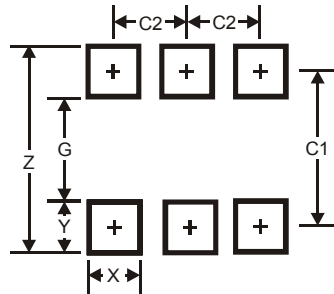
Please see AP02002 at <http://www.diodes.com/datasheets/ap02002.pdf> for latest version.



SOT563			
Dim	Min	Max	Typ
A	0.15	0.30	0.20
B	1.10	1.25	1.20
C	1.55	1.70	1.60
D	-	-	0.50
G	0.90	1.10	1.00
H	1.50	1.70	1.60
K	0.55	0.60	0.60
L	0.10	0.30	0.20
M	0.10	0.18	0.11
All Dimensions in mm			

Suggested Pad Layout

Please see AP02001 at <http://www.diodes.com/datasheets/ap02001.pdf> for the latest version.



Dimensions	Value (in mm)
Z	2.2
G	1.2
X	0.375
Y	0.5
C1	1.7
C2	0.5

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