### FAIRCHILD

SEMICONDUCTOR

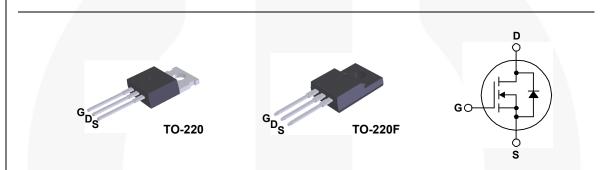
## FQP4N90C / FQPF4N90C **N-Channel QFET® MOSFET** 900 V, 4.0 A, 4.2 Ω

#### Description

This N-Channel enhancement mode power MOSFET is • 4.0 A, 900 V, R<sub>DS(on)</sub> = 4.2 Ω (Max.) @ V<sub>GS</sub> = 10 V, produced using Fairchild Semiconductor's proprietary planar stripe and DMOS technology. This advanced MOSFET technology has been especially tailored to reduce on-state • Low Gate Charge (Typ. 17 nC) resistance, and to provide superior switching performance • Low Crss (Typ. 5.6 pF) and high avalanche energy strength. These devices are suitable for switched mode power supplies, active power • 100% Avalanche Tested factor correction (PFC), and electronic lamp ballasts.

#### Features

- $I_{D} = 2.0 \text{ A}$



#### Absolute Maximum Ratings T<sub>c</sub> = 25°C unless otherwise noted.

Symbol	Parameter	FQP4N90C	FQPF4N90C	Unit	
V <sub>DSS</sub>	Drain-Source Voltage		9	V	
I <sub>D</sub>	Drain Current - Continuous ( $T_C = 25^{\circ}C$ )		4	4 *	А
	- Continuous (T <sub>C</sub> = 100°C)	_	2.3	2.3 *	А
I <sub>DM</sub>	Drain Current - Pulsed	(Note 1)	16	16 *	А
V <sub>GSS</sub>	Gate-Source Voltage		±	V	
E <sub>AS</sub>	Single Pulsed Avalanche Energy	(Note 2)	570		mJ
I <sub>AR</sub>	Avalanche Current	(Note 1)	4		Α
E <sub>AR</sub>	Repetitive Avalanche Energy	(Note 1)	14		mJ
dv/dt	Peak Diode Recovery dv/dt	(Note 3)	4.5		V/ns
P <sub>D</sub> Power Dissipation ( $T_C = 25^{\circ}C$ )			140	47	W
	- Derate above 25°C		1.12	0.38	W/°C
T <sub>J</sub> , T <sub>STG</sub>	Operating and Storage Temperature Range	-55 to +150		°C	
TL	Maximum Lead Temperature for Soldering, 1/8" from Case for 5 Seconds.		3	°C	

\* Drain current limited by maximum junction temperature.

#### **Thermal Characteristics**

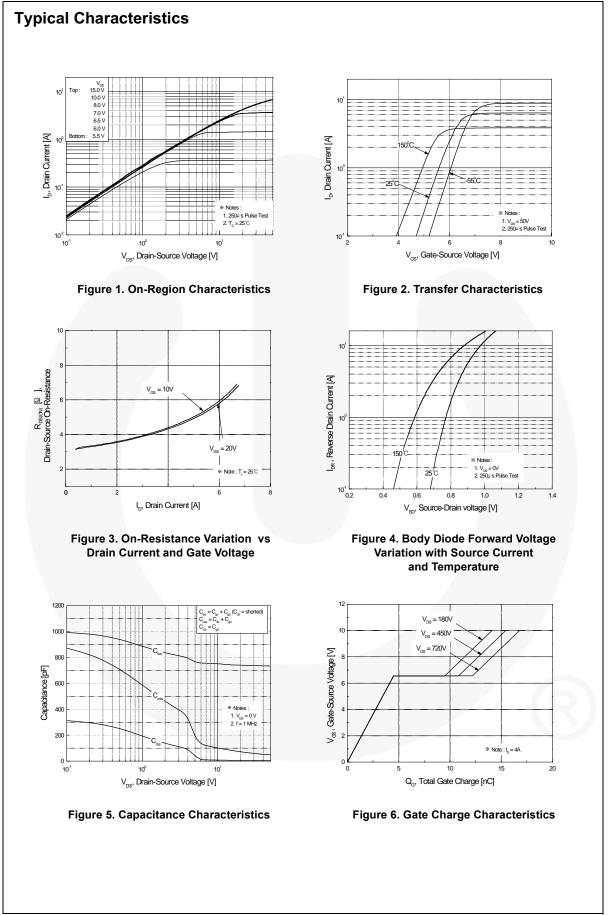
Symbol	Parameter	FQP9N90C	FQPF9N90CT	Unit	
$R_{\theta JC}$	Thermal Resistance, Junction-to-Case, Max.	0.89	2.66	°C/W	
$R_{\theta CS}$	Thermal Resistance, Case-to-Sink Typ, Max.	0.5		°C/W	
$R_{\thetaJA}$	Thermal Resistance, Junction-to-Ambient, Max.	62.5	62.5	°C/W	

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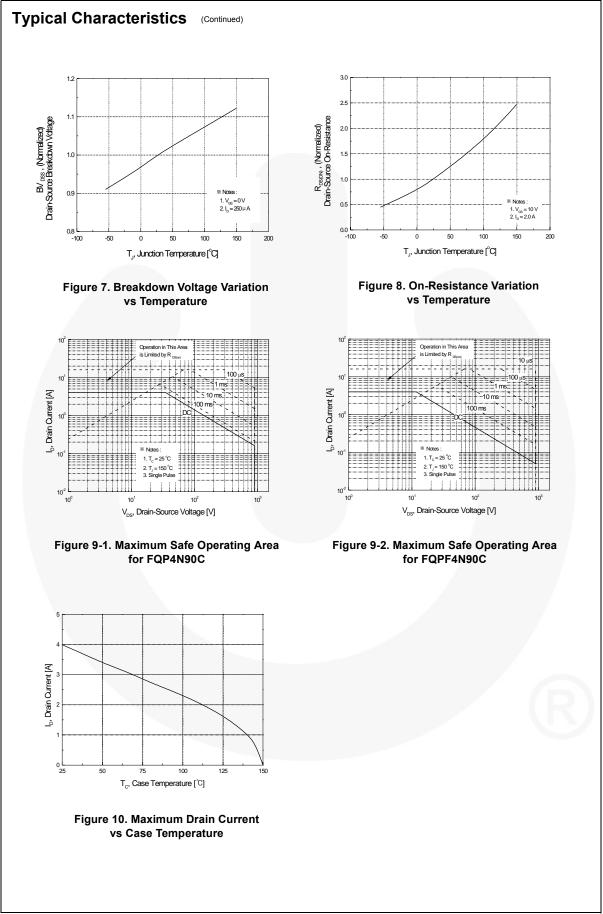
FQP4N90C FQP4N90C TO		Top Mark	rk Pac		kage Packing Method Ree		Size	Tape Width		Quantity	
		FQP4N90C	TO-	220	Tube	N/.	A	N/A		50 units	
		TO-2	220F Tube N/		/A N//			50 units			
Electric	cal Cha	racteristics	T <sub>C</sub> = 25°0	C unless ot	herwise noted.						
Symbol		Parameter			Test Conditions		Min.	Тур.	Max.	Unit	
Off Cha	racterist	ics									
BV <sub>DSS</sub>	Drain-Sou	rce Breakdown Volta	ige	V <sub>GS</sub> =	0 V, I <sub>D</sub> = 250 μA		900			V	
$\Delta BV_{DSS}$ / $\Delta T_{J}$	Breakdow Coefficient	n Voltage Temperatu t	turo		50 μA, Referenced to 25°C			1.05		V/°C	
I <sub>DSS</sub>	Zoro Coto	Voltago Droin Curro	$V_{DS} = 900 \text{ V}, \text{ V}_{GS} = 0 \text{ V}$				10	μA			
	Zeio Gale	Voltage Drain Curre	ant -	$V_{DS} = 720 \text{ V}, \text{ T}_{C} = 125^{\circ}\text{C}$					100	μA	
I <sub>GSSF</sub>	Gate-Body	/ Leakage Current, F	orward	$V_{GS} = 30 \text{ V}, V_{DS} = 0 \text{ V}$					100	nA	
I <sub>GSSR</sub>	Gate-Body	/ Leakage Current, F	Reverse	$V_{GS} = -30 \text{ V}, \text{ V}_{DS} = 0 \text{ V}$				-100	nA		
On Cha	racterist	ics									
V <sub>GS(th)</sub>	Gate Thre	shold Voltage		$V_{DS} = V_{GS}, I_{D} = 250 \ \mu A$		3.0		5.0	V		
R <sub>DS(on)</sub>	Static Drai On-Resist			V <sub>GS</sub> =	10 V, I <sub>D</sub> = 2 A			3.5	4.2	Ω	
9 <sub>FS</sub>	Forward T	ransconductance		$V_{DS} =$	50 V, I <sub>D</sub> = 2 A			5		S	
Dynami	ic Charac	teristics									
C <sub>iss</sub>	Input Capa	acitance		V <sub>DS</sub> = 25 V, V <sub>GS</sub> = 0 V, f = 1.0 MHz			740	960	pF		
C <sub>oss</sub>	Output Ca	pacitance					65	85	pF		
C <sub>rss</sub>	Reverse T	ransfer Capacitance					5.6	7.3	pF		
Switchi	ng Chara	acteristics									
t <sub>d(on)</sub>	Turn-On D		_		450.1/1 4.4			25	60	ns	
t <sub>r</sub>	Turn-On R		-	$V_{DD} = 450 \text{ V}, \text{ I}_{D} = 4 \text{ A},$			50	110	ns		
t <sub>d(off)</sub>	Turn-Off D	elay Time			R <sub>G</sub> = 25 Ω			40	90	ns	
t <sub>f</sub>	Turn-Off F	all Time				(Note 4)		35	80	ns	
Qg	Total Gate	Charge		Vne =	720 V, I <sub>D</sub> = 4 A,			17	22	nC	
Q <sub>gs</sub>	Gate-Sour	ce Charge		$V_{\rm GS} = 120$ V, $I_{\rm D} = 4$ A, $V_{\rm GS} = 10$ V		/	4.5		nC		
Q <sub>gd</sub>	Gate-Drain Charge			(Note 4)				7.5		nC	
	I		stice c	ad Mar	vimum Potinco						
I <sub>S</sub>	1	ode Characteris Continuous Drain-S							4	Α	
I <sub>SM</sub>	Maximum	Pulsed Drain-Source	e Diode F	orward	Current				16	Α	
V <sub>SD</sub>		rce Diode Forward V			0 V, I <sub>S</sub> = 4 A				1.4	V	
t <sub>rr</sub>		ecovery Time	5		0 V, I <sub>S</sub> = 4 A,			450		ns	
Q <sub>rr</sub>	Reverse Recovery Charge			$dI_{\rm F} / dt = 100 \text{ A}/\mu \text{s}$				3.5		μC	

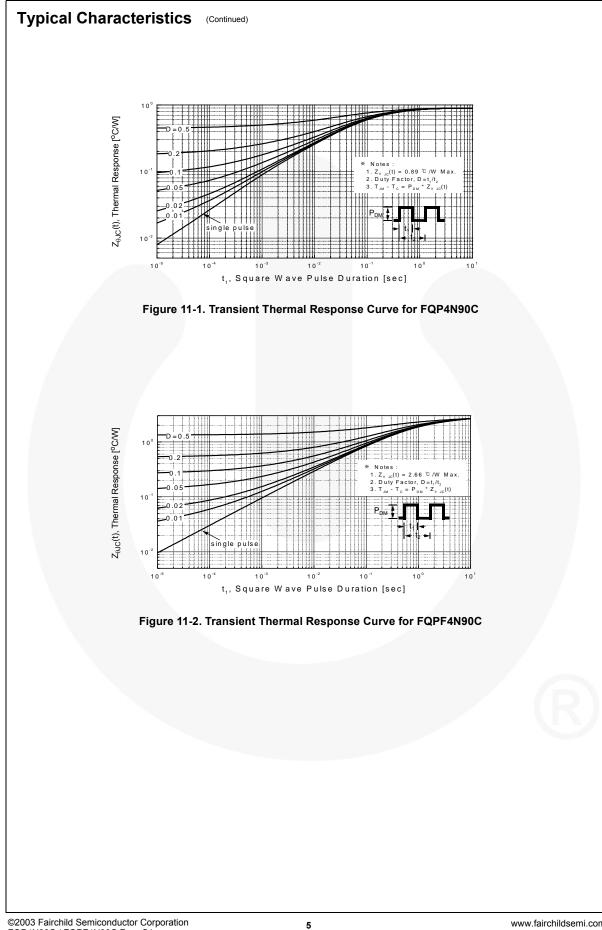
Notes: 1. Repetitive rating : pulse-width limited by maximum junction temperature. 2. L = 67 mH, I<sub>AS</sub> = 4 A, V<sub>DD</sub> = 50 V, R<sub>G</sub> = 25  $\Omega$ , starting T<sub>J</sub> = 25°C. 3. I<sub>SD</sub> ≤ 4 A, di/dt ≤ 200 A/µs, V<sub>DD</sub> ≤ BV<sub>DSS</sub>, starting T<sub>J</sub> = 25°C. 4. Essentially independent of operating temperature.

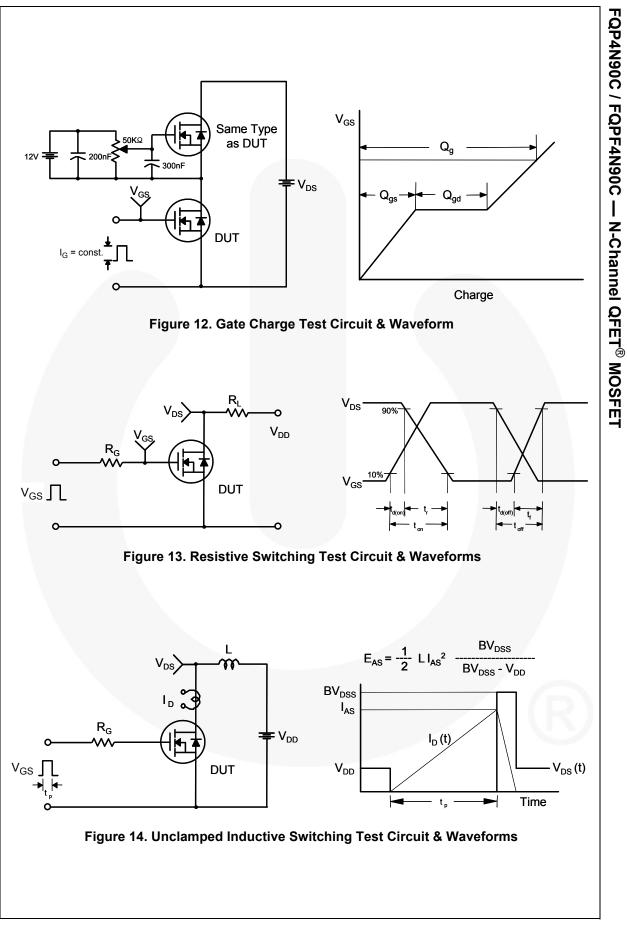
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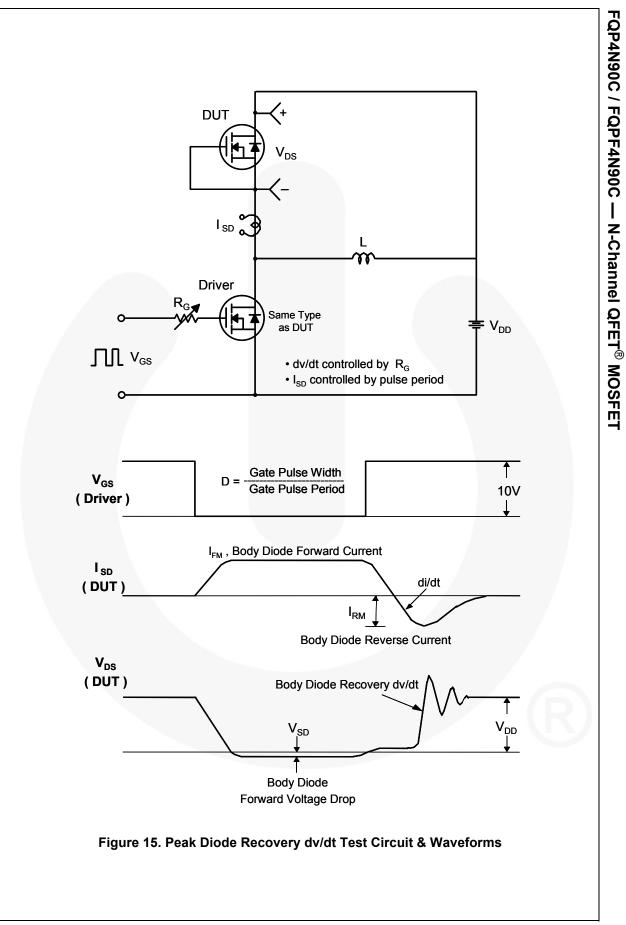


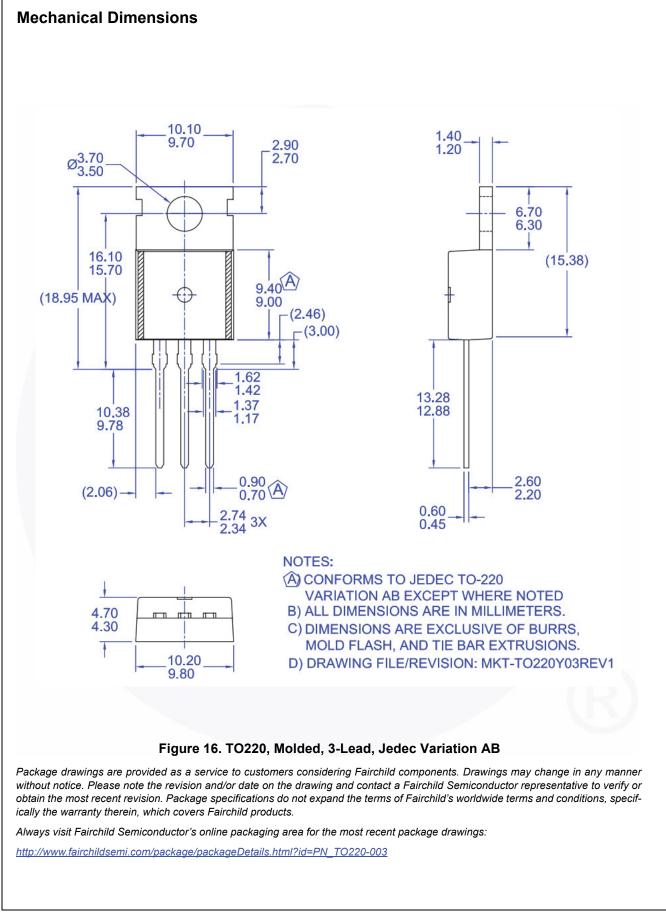
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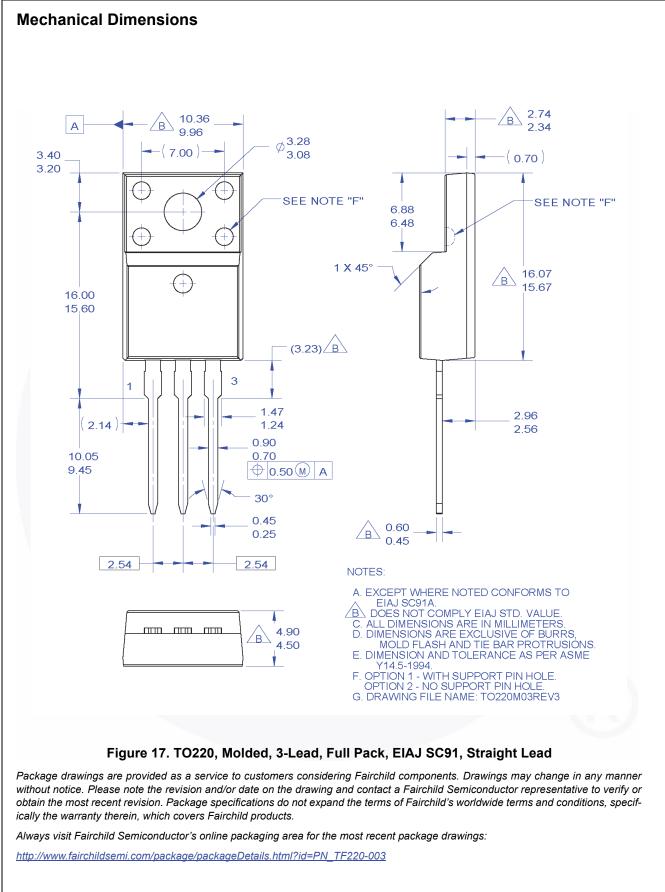












FQP4N90C / FQPF4N90C

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		Rev. 166

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