

SEMICONDUCTOR®

November 2013

FQPF5N90

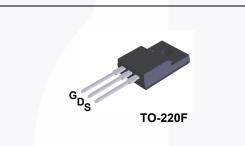
N-Channel QFET[®] MOSFET 900 V, 3 A, 2.3 Ω

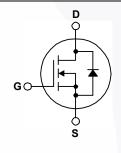
Description

This N-Channel enhancement mode power MOSFET is produced using Fairchild Semiconductor's proprietary planar stripe and DMOS technology. This advanced MOSFET technology has been especially tailored to reduce on-state resistance, and to provide superior switching performance and high avalanche energy strength. These devices are suitable for switched mode power supplies, active power factor correction (PFC), and electronic lamp ballasts.

Features

- 3 A, 900 V, $R_{DS(on)}$ = 2.3 Ω (Max.) @ V_{GS} = 10 V, I_D = 1.5 A
- Low Gate Charge (Typ. 31 nC)
- Low Crss (Typ. 13 pF)
- 100% Avalanche Tested
- LoHS Compliant





Absolute Maximum Ratings T_c = 25°C unless otherwise noted

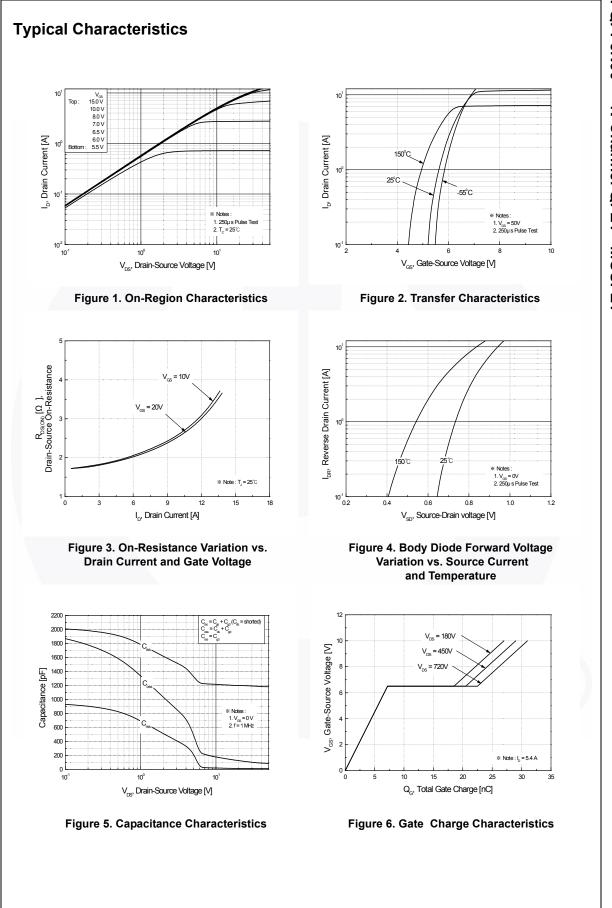
Symbol	Parameter	FQPF5N90	Unit
V _{DSS}	Drain-Source Voltage	900	V
I _D	Drain Current - Continuous (T _C = 25°C)	3.0	А
	- Continuous (T _C = 100°C)	1.9	Α
I _{DM}	Drain Current - Pulsed (Note 1)	12	Α
V _{GSS}	Gate-Source Voltage	± 30	V
E _{AS}	Single Pulsed Avalanche Energy (Note 2)	660	mJ
I _{AR}	Avalanche Current (Note 1)	3.0	А
E _{AR}	Repetitive Avalanche Energy (Note 1)	5.1	mJ
dv/dt	Peak Diode Recovery dv/dt (Note 3)	4.0	V/ns
PD	Power Dissipation $(T_C = 25^{\circ}C)$	51	W
	- Derate Above 25°C	0.41	W/°C
T _J , T _{STG}	Operating and Storage Temperature Range	-55 to +150	°C
Τ _L	Maximum Lead Temperature for Soldering, 1/8" from Case for 5 seconds	300	°C

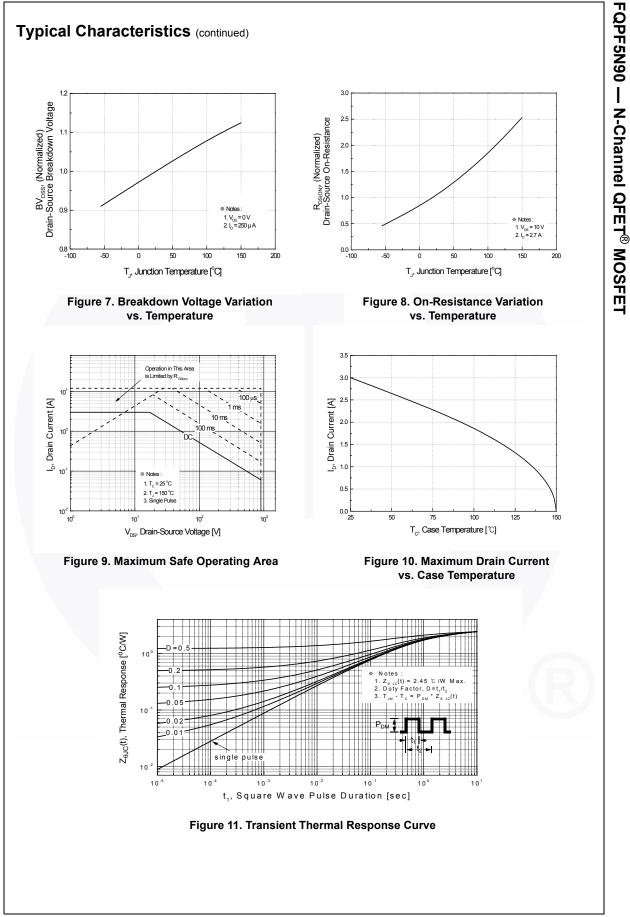
Thermal Characteristics

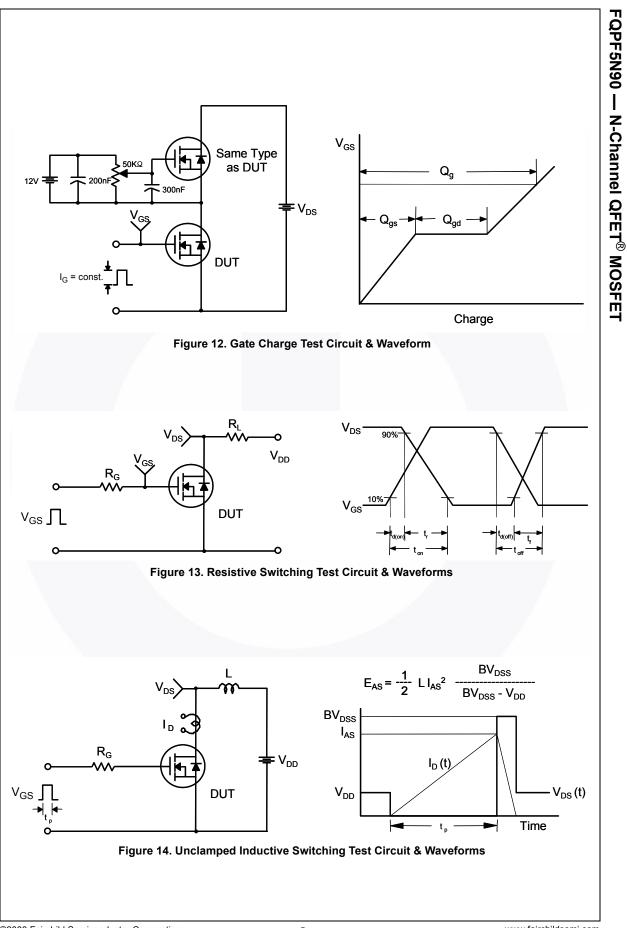
Symbol	Parameter	FQPF5N90	Unit
$R_{ extsf{ heta}JC}$	Thermal Resistance, Junction-to-Case, Max.	2.45	°C/W
$R_{ extsf{ heta}JA}$	Thermal Resistance, Junction-to-Ambient, Max.	62.5	C/ VV

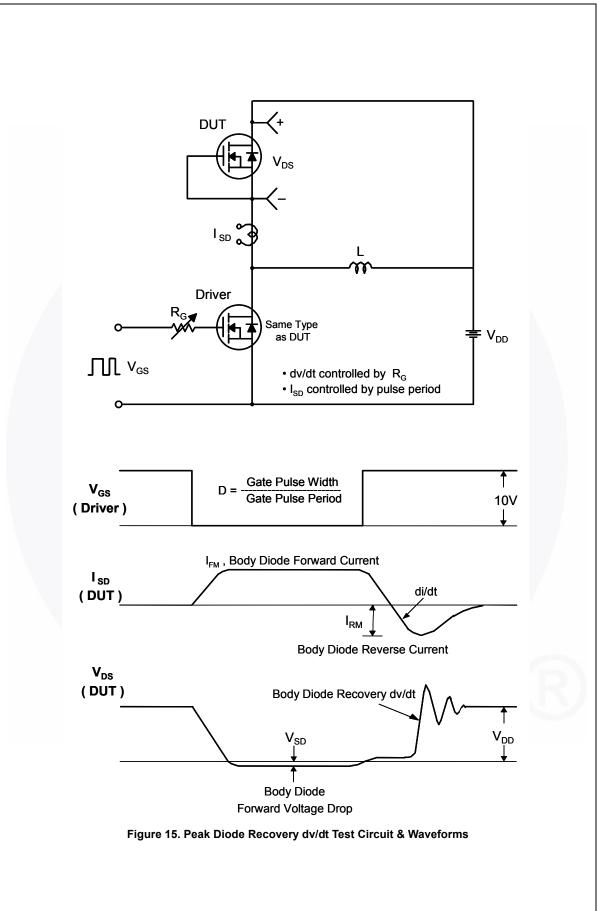
Part NumberTop MarkPackageFQPF5N90FQPF5N90TO-220F		•		e Tape Width N/A		Qu	Quantity 50 units		
						50			
lectri	cal C	haracteristics	T _C = 25°C	unless otherwise noted.					
Symbol		Parameter		Test Conditi	ions	Min.	Тур.	Max.	Unit
Off Cha	aracte	ristics							
BV _{DSS}	1	Source Breakdown V	oltage	V _{GS} = 0 V, I _D = 250 μA		900			V
ABV _{DSS}	Breakdown Voltage Temperature Coefficient		•	$I_D = 250 \ \mu$ A, Referenced to 25°C					
ΔT_{J}							1.0		V/°C
DSS	Zana Oaka Malkana Dasia Ormank		rrant	V_{DS} = 900 V, V_{GS} = 0	V C			10	μA
	Zero	Gate Voltage Drain Co	Inent	V _{DS} = 720 V, T _C = 125°C				100	μA
GSSF	Gate-I	Body Leakage Currer	t, Forward	V_{GS} = 30 V, V_{DS} = 0	V			100	nA
GSSR	Gate-I	Body Leakage Currer	it, Reverse	V_{GS} = -30 V, V_{DS} = 0	V			-100	nA
On Cha	aracto	ristics							
/ _{GS(th)}	1	Threshold Voltage		$V_{DS} = V_{GS}, I_{D} = 250$	μA	3.0		5.0	V
R _{DS(on)}	Static	Drain-Source		V _{GS} = 10 V, I _D = 1.5			1.8	2.3	Ω
FS		rd Transconductance		V _{DS} = 50 V, I _D = 1.5	A		4.0		S
				20 2					1
	1	racteristics				_			_
Ciss		Capacitance		V_{DS} = 25 V, V_{GS} = 0	V,		1200	1550	pF
C _{oss}		t Capacitance		f = 1 MHz	-		110	145	pF
S _{rss}	Rever	se Transfer Capacita	nce				13	17	pF
Switch	ing Ch	aracteristics							
d(on)	Turn-0	On Delay Time		V _{DD} = 450 V, I _D = 5.4)		28	65	ns
r	Turn-0	On Rise Time		$R_{\rm D} = 450$ V, $r_{\rm D} = 5.4$ $R_{\rm G} = 25 \ \Omega$	+ A,		65	140	ns
d(off)	Turn-0	Off Delay Time		NG - 20 32	-		65	140	ns
f	Turn-0	Off Fall Time			(Note 4)		50	110	ns
ζ _g	Total C	Gate Charge		V _{DS} = 720 V, I _D = 5.4 A,			31	40	nC
2 _{gs}	Gate-	Source Charge		$V_{GS} = 10 V$,		7.2		nC
2 _{gd}	Gate-I	Drain Charge		66	(Note 4)		15		nC
			• .•						1
	1			d Maximum Rati	ngs			2.0	•
S	Maximum Continuous Drain-Source Dic Maximum Pulsed Drain-Source Diode F							3.0	A
SM	-							12	A
/ _{SD}		Source Diode Forwar	u voltage	$V_{GS} = 0 V, I_S = 3.0 A$ $V_{GS} = 0 V, I_S = 5.4 A,$				1.4	V
n N		se Recovery Time					610		ns
ל ^{ער}	Rever	se Recovery Charge		dI _F / dt = 100 A/µs			5.26		μC

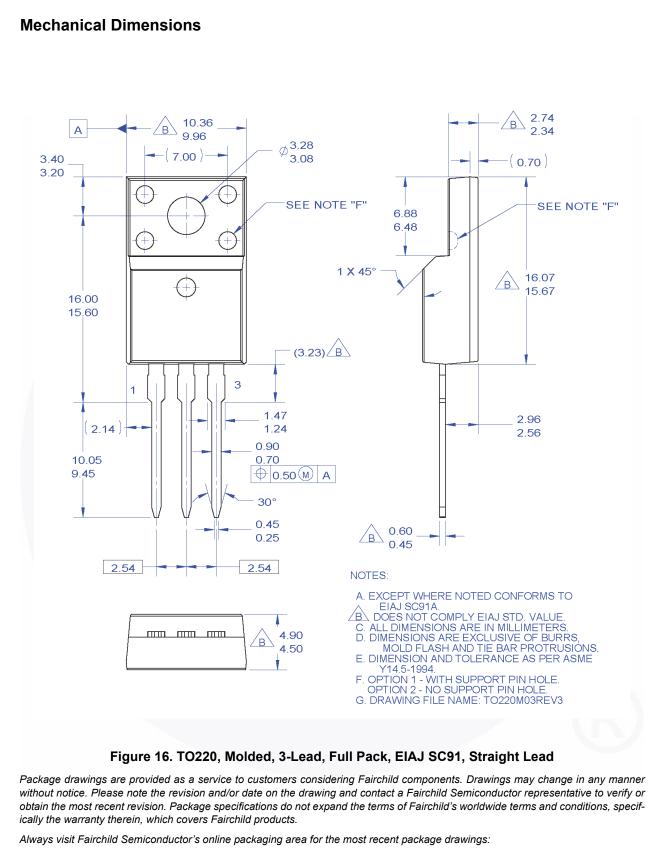
3. I_{SD} \leq 5.4 A, di/dt \leq 200 A/µs, V_{DD} \leq BV_{DSS} starting T_J = 25°C. 4. Essentially independent of operating temperature. FQPF5N90 — N-Channel QFET[®] MOSFET











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FQPF5N90 — N-Channel QFET[®] MOSFET



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