## FAIRCHILD
### SEMICONDUCTOR®

**March 2013**

# FQT7N10
## N-Channel QFET® MOSFET
### 100 V, 1.7 A, 350 mΩ

### Description

This N-Channel enhancement mode power MOSFET is produced using Fairchild Semiconductor®'s proprietary planar stripe and DMOS technology. This advanced MOSFET technology has been especially tailored to reduce on-state resistance, and to provide superior switching performance and high avalanche energy strength. These devices are suitable for switched mode power supplies, audio amplifier, DC motor control, and variable switching power applications.

### Features

- 1.7 A, 100 V, $R_{DS(on)}$=350 mΩ(Max.) @$V_{GS}$=10 V, $I_D$=0.85 A
- Low Gate Charge (Typ. 5.8 nC)
- Low Crss (Typ. 10 pF)
- 100% Avalanche Tested

**D**

**G**

**S** **SOT-223**

**D**

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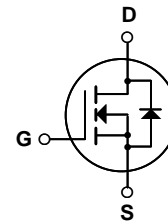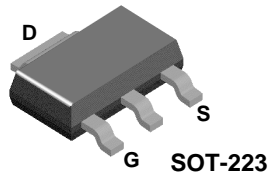
### Absolute Maximum Ratings  $T_C$ = 25°C unless otherwise noted

| Symbol | Parameter | | | FQT7N10 | Unit |
|--------|-----------|--|--|---------|------|
| $V_{DSS}$ | Drain-Source Voltage | | | 100 | V |
| $I_D$ | Drain Current | - Continuous ($T_C$ = 25°C) | | 1.7 | A |
| | | - Continuous ($T_C$ = 70°C) | | 1.36 | A |
| $I_{DM}$ | Drain Current | - Pulsed | (Note 1) | 6.8 | A |
| $V_{GSS}$ | Gate-Source Voltage | | | ± 25 | V |
| $E_{AS}$ | Single Pulsed Avalanche Energy | | (Note 2) | 50 | mJ |
| $I_{AR}$ | Avalanche Current | | (Note 1) | 1.7 | A |
| $E_{AR}$ | Repetitive Avalanche Energy | | (Note 1) | 0.2 | mJ |
| dv/dt | Peak Diode Recovery dv/dt | | (Note 3) | 6.0 | V/ns |
| $P_D$ | Power Dissipation ($T_C$ = 25°C) | | | 2.0 | W |
| | - Derate above 25°C | | | 0.016 | W/°C |
| $T_J$, $T_{STG}$ | Operating and Storage Temperature Range | | | -55 to +150 | °C |
| $T_L$ | Maximum lead temperature for soldering purposes, 1/8" from case for 5 seconds | | | 300 | °C |

### Thermal Characteristics

| Symbol | Parameter | Typ | Max | Unit |
|--------|-----------|-----|-----|------|
| $R_{\theta JA}$ | Thermal Resistance, Junction-to-Ambient * | -- | 62.5 | °C/W |

* When mounted on the minimum pad size recommended (PCB Mount)

## Electrical Characteristics $T_C = 25°C$ unless otherwise noted

| Symbol | Parameter | Test Conditions | Min | Typ | Max | Unit |
|---|---|---|---|---|---|---|
| **Off Characteristics** | | | | | | |
| $BV_{DSS}$ | Drain-Source Breakdown Voltage | $V_{GS} = 0$ V, $I_D = 250$ µA | 100 | -- | -- | V |
| $\Delta BV_{DSS} / \Delta T_J$ | Breakdown Voltage Temperature Coefficient | $I_D = 250$ µA, Referenced to 25°C | -- | 0.1 | -- | V/°C |
| $I_{DSS}$ | Zero Gate Voltage Drain Current | $V_{DS} = 100$ V, $V_{GS} = 0$ V | -- | -- | 1 | µA |
| | | $V_{DS} = 80$ V, $T_C = 125°C$ | -- | -- | 10 | µA |
| $I_{GSSF}$ | Gate-Body Leakage Current, Forward | $V_{GS} = 25$ V, $V_{DS} = 0$ V | -- | -- | 100 | nA |
| $I_{GSSR}$ | Gate-Body Leakage Current, Reverse | $V_{GS} = -25$ V, $V_{DS} = 0$ V | -- | -- | -100 | nA |
| **On Characteristics** | | | | | | |
| $V_{GS(th)}$ | Gate Threshold Voltage | $V_{DS} = V_{GS}$, $I_D = 250$ µA | 2.0 | -- | 4.0 | V |
| $R_{DS(on)}$ | Static Drain-Source On-Resistance | $V_{GS} = 10$ V, $I_D = 0.85$ A | -- | 0.28 | 0.35 | Ω |
| $g_{FS}$ | Forward Transconductance | $V_{DS} = 40$ V, $I_D = 0.85$ A (Note 4) | -- | 1.85 | -- | S |
| **Dynamic Characteristics** | | | | | | |
| $C_{iss}$ | Input Capacitance | $V_{DS} = 25$ V, $V_{GS} = 0$ V, f = 1.0 MHz | -- | 190 | 250 | pF |
| $C_{oss}$ | Output Capacitance | | -- | 60 | 75 | pF |
| $C_{rss}$ | Reverse Transfer Capacitance | | -- | 10 | 13 | pF |
| **Switching Characteristics** | | | | | | |
| $t_{d(on)}$ | Turn-On Delay Time | $V_{DD} = 50$ V, $I_D = 7.3$ A, $R_G = 25$ Ω | -- | 7 | 25 | ns |
| $t_r$ | Turn-On Rise Time | | -- | 24 | 60 | ns |
| $t_{d(off)}$ | Turn-Off Delay Time | | -- | 13 | 35 | ns |
| $t_f$ | Turn-Off Fall Time | (Note 4, 5) | -- | 19 | 50 | ns |
| $Q_g$ | Total Gate Charge | $V_{DS} = 80$ V, $I_D = 7.3$ A, $V_{GS} = 10$ V | -- | 5.8 | 7.5 | nC |
| $Q_{gs}$ | Gate-Source Charge | | -- | 1.4 | -- | nC |
| $Q_{gd}$ | Gate-Drain Charge | (Note 4, 5) | -- | 2.5 | -- | nC |
| **Drain-Source Diode Characteristics and Maximum Ratings** | | | | | | |
| $I_S$ | Maximum Continuous Drain-Source Diode Forward Current | | -- | -- | 1.7 | A |
| $I_{SM}$ | Maximum Pulsed Drain-Source Diode Forward Current | | -- | -- | 6.8 | A |
| $V_{SD}$ | Drain-Source Diode Forward Voltage | $V_{GS} = 0$ V, $I_S = 1.7$ A | -- | -- | 1.5 | V |
| $t_{rr}$ | Reverse Recovery Time | $V_{GS} = 0$ V, $I_S = 7.3$ A, | -- | 70 | -- | ns |
| $Q_{rr}$ | Reverse Recovery Charge | $dI_F / dt = 100$ A/µs (Note 4) | -- | 150 | -- | nC |

**Notes:**
1. Repetitive Rating : Pulse width limited by maximum junction temperature
2. L = 26mH, $I_{AS}$ = 1.7A, $V_{DD}$ = 25V, $R_G$ = 25 Ω, Starting $T_J$ = 25°C
3. $I_{SD} \leq$ 7.3A, di/dt $\leq$ 300A/µs, $V_{DD} \leq BV_{DSS}$, Starting $T_J$ = 25°C
4. Pulse Test : Pulse width $\leq$ 300µs, Duty cycle $\leq$ 2%
5. Essentially independent of operating temperature
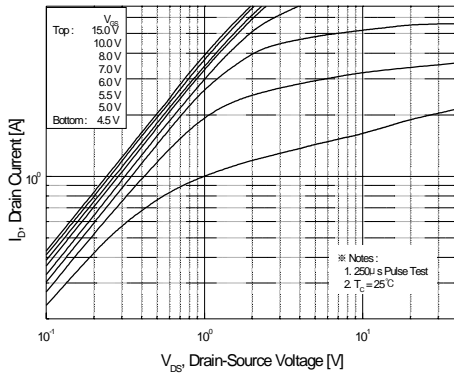
# Typical Characteristics
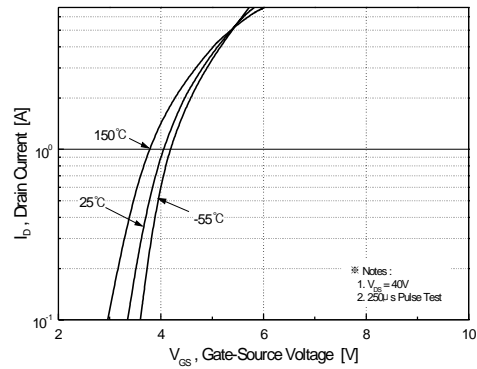


**Figure 1. On-Region Characteristics**



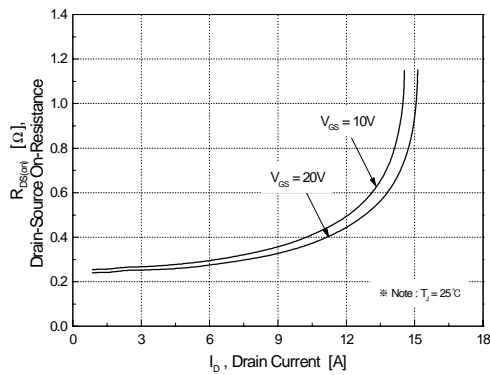**Figure 2. Transfer Characteristics**



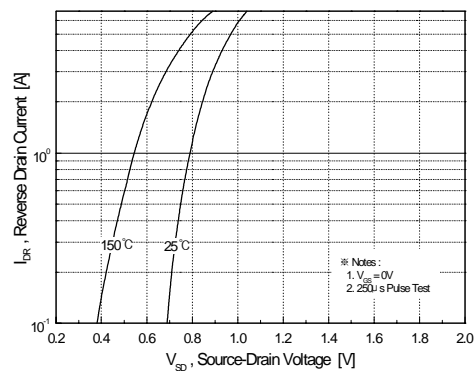**Figure 3. On-Resistance Variation vs. Drain Current and Gate Voltage**



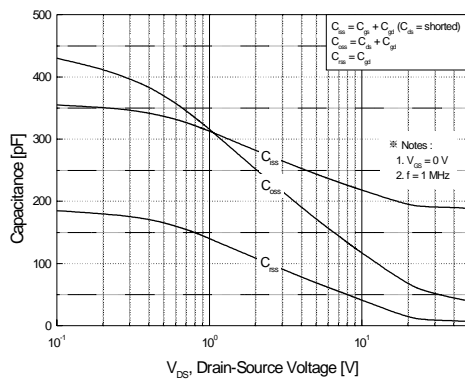**Figure 4. Body Diode Forward Voltage Variation vs. Source Current and Temperature**
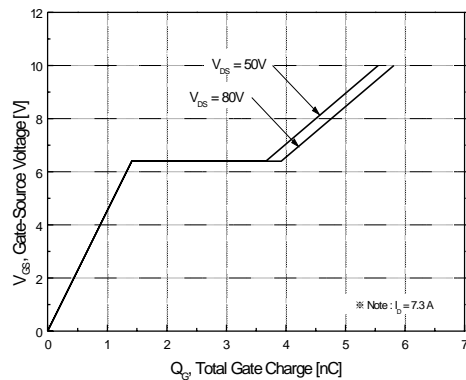


**Figure 5. Capacitance Characteristics**



**Figure 6. Gate Charge Characteristics**
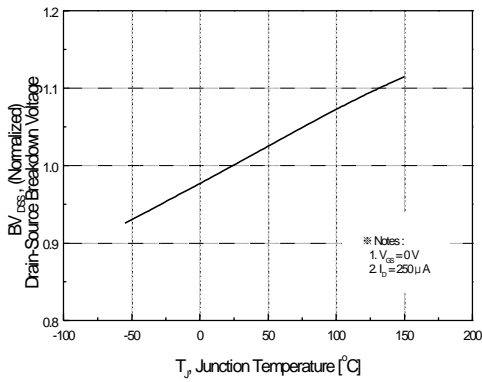
## Typical Characteristics (Continued)



**Figure 7. Breakdown Voltage Variation vs. Temperature**
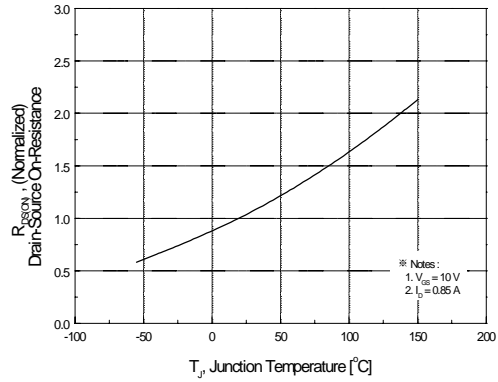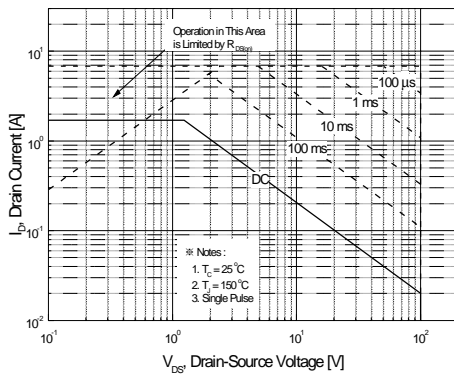


**Figure 8. On-Resistance Variation vs. Temperature**
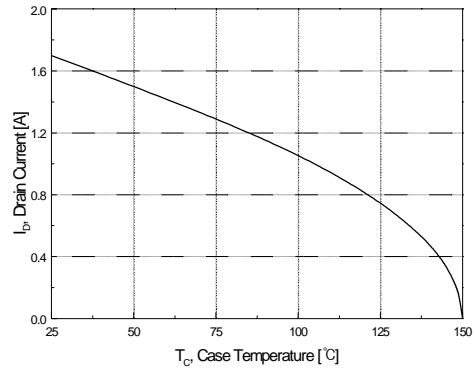


**Figure 9. Maximum Safe Operating Area**
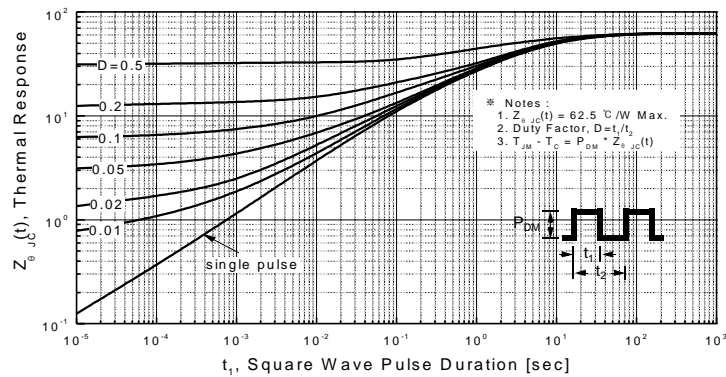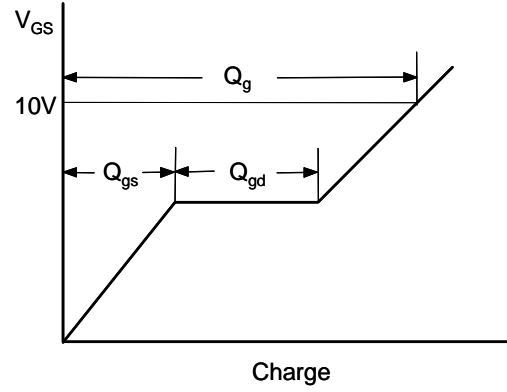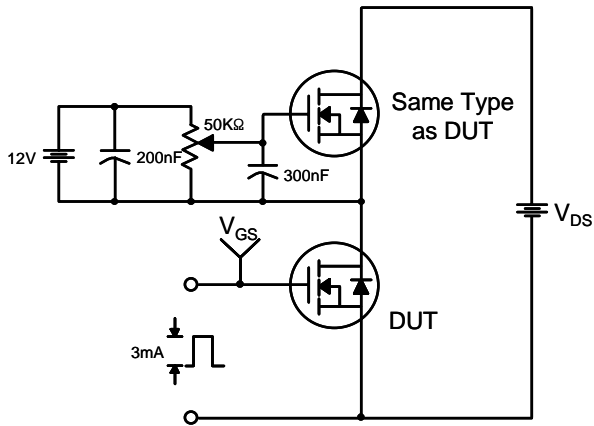


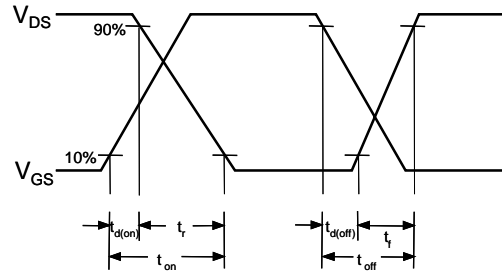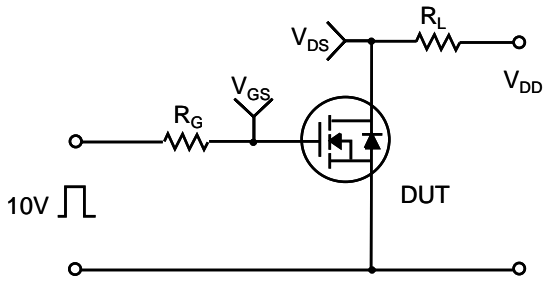**Figure 10. Maximum Drain Current vs. Case Temperature**



**Figure 11. Transient Thermal Response Curve**

**Gate Charge Test Circuit & Waveform**

12V
200nF
50KΩ
300nF
V$_{GS}$

Same Type
as DUT

V$_{DS}$

DUT

3mA

V$_{GS}$

Q$_g$

10V

Q$_{gs}$

Q$_{gd}$

Charge

**Resistive Switching Test Circuit & Waveforms**

V$_{DS}$
R$_L$
V$_{GS}$
V$_{DD}$
R$_G$
10V
DUT

V$_{DS}$
90%

V$_{GS}$
10%

t$_{d(on)}$  t$_r$   t$_{d(off)}$  t$_f$

t$_{on}$   t$_{off}$

**Unclamped Inductive Switching Test Circuit & Waveforms**

V$_{DS}$
L
I$_D$
R$_G$
V$_{DD}$
10V
DUT
t$_p$

$$E_{AS} = \frac{1}{2} L I_{AS}^2 \ \frac{BV_{DSS}}{BV_{DSS} - V_{DD}}$$

BV$_{DSS}$
I$_{AS}$

I$_D$(t)

V$_{DD}$

V$_{DS}$(t)

t$_p$   Time
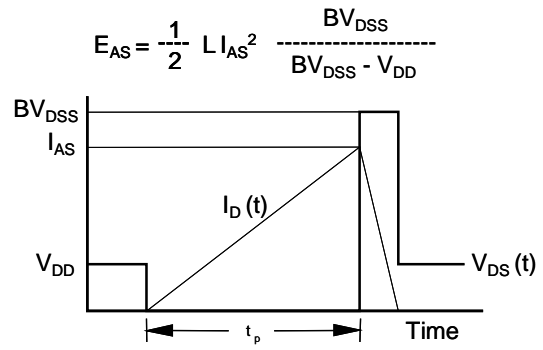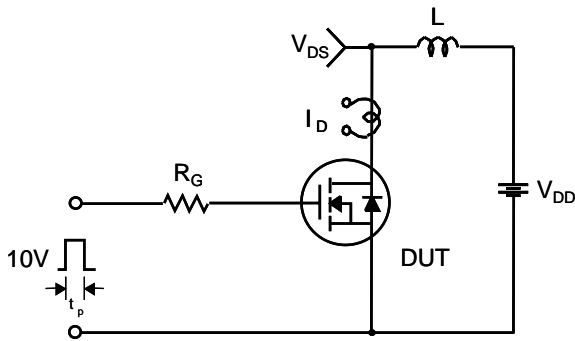
**Peak Diode Recovery dv/dt Test Circuit & Waveforms**

DUT

$V_{DS}$

+

−

$I_{SD}$

L

Driver

$R_G$

Same Type
as DUT

$V_{DD}$

$V_{GS}$

• dv/dt controlled by $R_G$
• $I_{SD}$ controlled by pulse period

$V_{GS}$
( Driver )

$$D = \frac{\text{Gate Pulse Width}}{\text{Gate Pulse Period}}$$

10V

$I_{SD}$
( DUT )

$I_{FM}$ , Body Diode Forward Current

di/dt

$I_{RM}$

Body Diode Reverse Current

$V_{DS}$
( DUT )

Body Diode Recovery dv/dt

$V_{SD}$

$V_{DD}$

Body Diode
Forward Voltage Drop
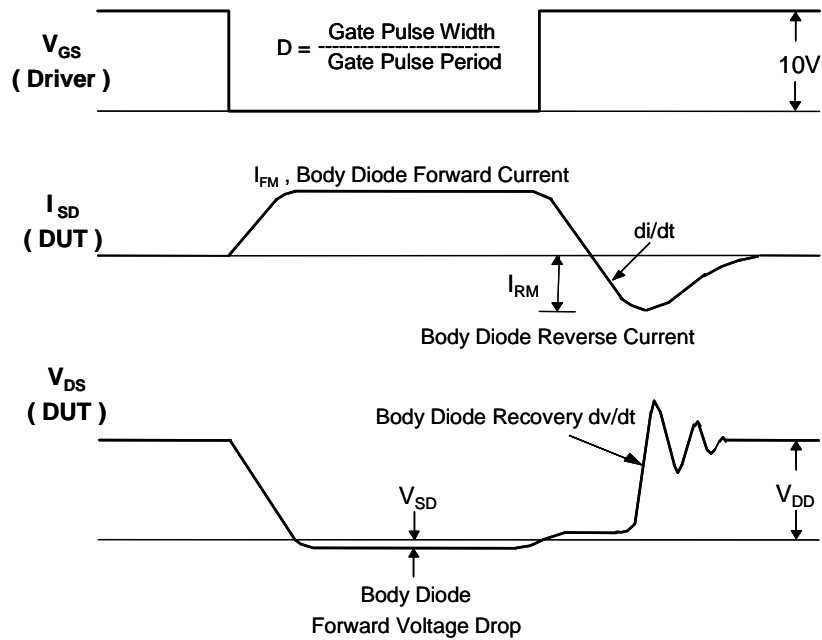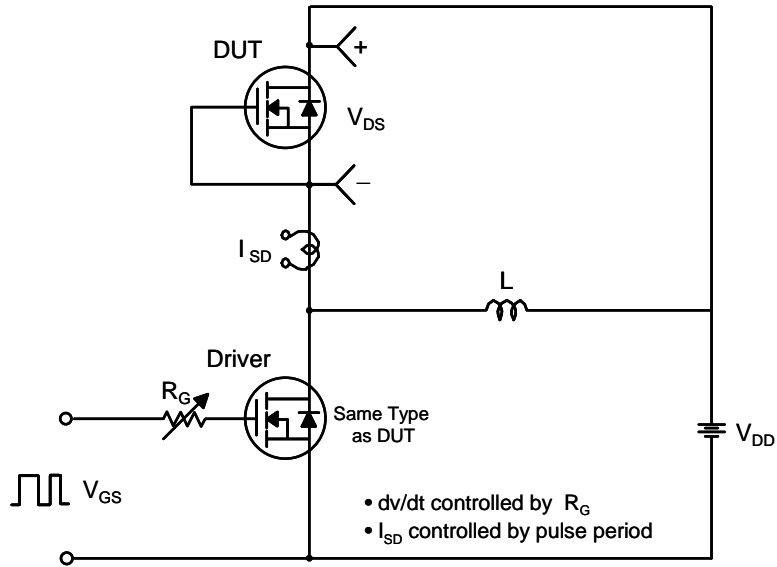
**Package Dimensions**

## SOT-223

3.00 ±0.10

2.30 TYP

0.70 ±0.10

(0.95)

4.60 ±0.25

(0.95)

MAX1.80

0.08MAX

0.65 ±0.20

1.75 ±0.20

(0.60)

3.50 ±0.20

(0.60)

0.06 +0.04 −0.02

7.00 ±0.30

0.25 +0.10 −0.05

0°~10°

1.60 ±0.20

(0.46)

(0.89)

6.50 ±0.20
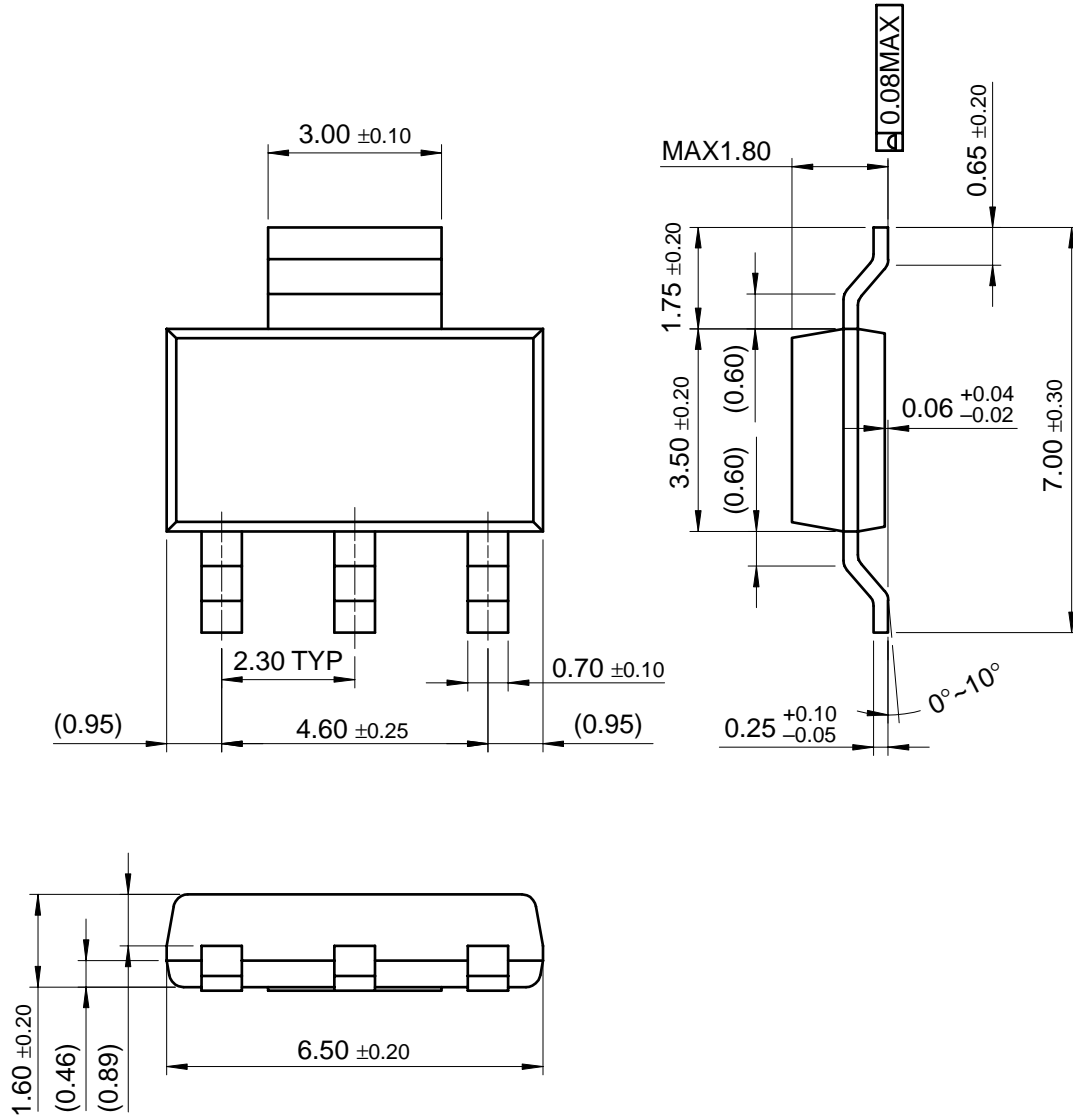
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