International Rectifier

AUIRFP2907Z

AUTOMOTIVE GRADE

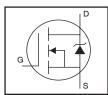
HEXFET® Power MOSFET

Features

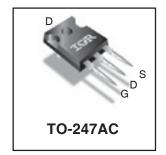
- Advanced Process Technology
- Ultra Low On-Resistance
- 175°C Operating Temperature
- Fast Switching
- Repetitive Avalanche Allowed up to Tjmax
- Lead-Free, RoHS Compliant
- Automotive Qualified *

Description

Specifically designed for Automotive applications, this HEXFET® Power MOSFET utilizes the latest processing techniques to achieve extremely low onresistance per silicon area. Additional features of this design are a 175°C junction operating temperature, fast switching speed and improved repetitive avalanche rating. These features combine to make this design an extremely efficient and reliable device for use in Automotive applications and a wide variety of other applications.



$V_{(BR)DSS}$		75V
R _{DS(on)}	max.	4.5m $Ω$
I _D		170A



G	D	S
Gate	Drain	Source

Absolute Maximum Ratings

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only; and functional operation of the device at these or any other condition beyond those indicated in the specifications is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability. The thermal resistance and power dissipation ratings are measured under board mounted and still air conditions. Ambient temperature (T_A) is 25°C, unless otherwise specified.

	Parameter	Max.	Units
I _D @ T _C = 25°C	Continuous Drain Current, V _{GS} @ 10V	170	Α
I _D @ T _C = 100°C	Continuous Drain Current, V _{GS} @ 10V	120	
I _{DM}	Pulsed Drain Current ①	680	
$P_D @ T_C = 25^{\circ}C$	Maximum Power Dissipation	310	W
	Linear Derating Factor	2.0	W/°C
V_{GS}	Gate-to-Source Voltage	± 20	V
E _{AS}	Single Pulse Avalanche Energy (Thermally Limited) ②	520	mJ
E _{AS} (tested)	Single Pulse Avalanche Energy Tested Value ⑦	690	
I _{AR}	Avalanche Current ①	See Fig.12a,12b,15,16	Α
E _{AR}	Repetitive Avalanche Energy ©		mJ
T_{J}	Operating Junction and	-55 to + 175	°C
T _{STG}	Storage Temperature Range		
	Soldering Temperature, for 10 seconds (1.6mm from case)	300	
	Mounting torque, 6-32 or M3 screw	10 lbf•in (1.1N•m)	

Thermal Resistance

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	Parameter	Тур.	Max.	Units				
$R_{\theta JC}$	Junction-to-Case ®		0.49	°C/W				
R _{0CS}	Case-to-Sink, Flat, Greased Surface	0.24						
$R_{\theta JA}$	Junction-to-Ambient		40					

HEXFET® is a registered trademark of International Rectifier.

^{*}Qualification standards can be found at http://www.irf.com/

Static Electrical Characteristics @ T_J = 25°C (unless otherwise specified)

	Parameter	Min.	Тур.	Max.	Units	Conditions
$V_{(BR)DSS}$	Drain-to-Source Breakdown Voltage	75			V	$V_{GS} = 0V, I_D = 250\mu A$
$\Delta BV_{DSS}/\Delta T_{J}$	Breakdown Voltage Temp. Coefficient		0.069		V/°C	Reference to 25°C, I _D = 1mA
R _{DS(on)}	Static Drain-to-Source On-Resistance		3.5	4.5	mΩ	V _{GS} = 10V, I _D = 90A ④
$V_{GS(th)}$	Gate Threshold Voltage	2.0		4.0	V	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$
gfs	Forward Transconductance	180			S	$V_{DS} = 25V, I_D = 90A$
I _{DSS}	Drain-to-Source Leakage Current			20	μΑ	$V_{DS} = 75V, V_{GS} = 0V$
				250		$V_{DS} = 75V, V_{GS} = 0V, T_{J} = 125^{\circ}C$
I _{GSS}	Gate-to-Source Forward Leakage			200	nA	$V_{GS} = 20V$
	Gate-to-Source Reverse Leakage			-200		V _{GS} = -20V

Dynamic Electrical Characteristics @ T_J = 25°C (unless otherwise specified)

	Parameter	Min.	Тур.	Max.	Units	Conditions
Q_g	Total Gate Charge	—	180	270		$I_D = 90A$
Q _{gs}	Gate-to-Source Charge		46		nC	$V_{DS} = 60V$
Q_{gd}	Gate-to-Drain ("Miller") Charge		65		Ī	V _{GS} = 10V ④
t _{d(on)}	Turn-On Delay Time	T	19		ns	$V_{DD} = 38V$
t _r	Rise Time		140		Ī	$I_D = 90A$
t _{d(off)}	Turn-Off Delay Time	—	97		1	$R_G = 2.5\Omega$
t _f	Fall Time		100		Ī	V _{GS} = 10V ④
L _D	Internal Drain Inductance	l —	5.0		nΗ	Between lead,
						6mm (0.25in.)
L _S	Internal Source Inductance		13		1	from package
						and center of die contact
C _{iss}	Input Capacitance		7500		pF	$V_{GS} = 0V$
C _{oss}	Output Capacitance		970		ĺ	$V_{DS} = 25V$
C _{rss}	Reverse Transfer Capacitance		510		ĺ	f = 1.0MHz, See Fig. 5
Coss	Output Capacitance	l —	3640	_	1	$V_{GS} = 0V, V_{DS} = 1.0V, f = 1.0MHz$
Coss	Output Capacitance	1 —	650		1	$V_{GS} = 0V, V_{DS} = 60V, f = 1.0MHz$
C _{oss} eff.	Effective Output Capacitance	l —	1020		1	$V_{GS} = 0V$, $V_{DS} = 0V$ to $60V$

Diode Characteristics

	Parameter	Min.	Тур.	Max.	Units	Conditions
Is	Continuous Source Current			90		MOSFET symbol
	(Body Diode)				Α	showing the
I _{SM}	Pulsed Source Current			680		integral reverse
	(Body Diode) ①					p-n junction diode.
V_{SD}	Diode Forward Voltage			1.3	V	$T_J = 25^{\circ}C, I_S = 90A, V_{GS} = 0V \oplus$
t _{rr}	Reverse Recovery Time		41	61		$T_J = 25$ °C, $I_F = 90A$, $V_{DD} = 38V$
Q _{rr}	Reverse Recovery Charge		59	89	nC	di/dt = 100A/μs ④
t _{on}	Forward Turn-On Time	Intrinsio	turn-or	time is	negligib	le (turn-on is dominated by LS+LD)

Notes:

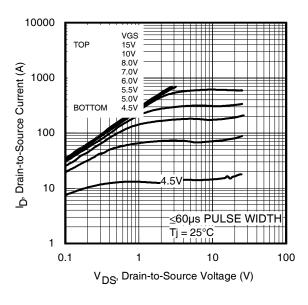
- ① Repetitive rating; pulse width limited by max. junction temperature. (See fig. 11).
- ② Limited by T_{Jmax} , starting $T_J = 25^{\circ}C$, L=0.13mH, $R_G = 25\Omega$, $I_{AS} = 90A$, $V_{GS} = 10V$. Part not recommended for use above this value.
- $\label{eq:loss_def} \begin{tabular}{ll} $I_{SD} \leq 90A, \ di/dt \leq 340A/\mu s, \ V_{DD} \leq V_{(BR)DSS}, \\ $T_J \leq 175^{\circ}C. \end{tabular}$
- 4 Pulse width \leq 1.0ms; duty cycle \leq 2%.
- $\mbox{\@ifnextcoloredge}$ Limited by $\mbox{\/T}_{\mbox{\/Jmax}}$, see Fig.12a, 12b, 15, 16 for typical repetitive avalanche performance.
- $\ \ \,$ This value determined from sample failure population, starting T_J = 25°C, L=0.13mH, R_G = 25 Ω , I_{AS} = 90A, V_{GS} =10V.

Qualification Information[†]

			Automotive			
		(per AEC-Q101) ††				
Qualificatio	n Level	qualification. IR's In	part number(s) passed Automotive dustrial and Consumer qualification leve ion of the higher Automotive level.			
Moisture Se	ensitivity Level	TO-247	MSL1			
	Machine Model	Class M4 (425V)				
			AEC-Q101-002			
FOD	Human Body Model	Class H2 (4000V)				
ESD			AEC-Q101-001			
	Charged Device		Class C5 (1125V)			
	Model		AEC-Q101-005			
RoHS Comp	pliant	Yes				

[†] Qualification standards can be found at International Rectifier's web site: http://www.irf.com/

^{††} Exceptions to AEC-Q101 requirements are noted in the qualification report.



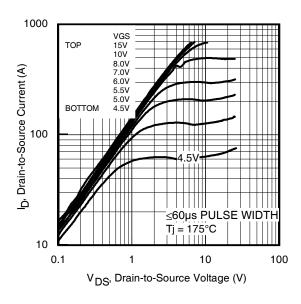
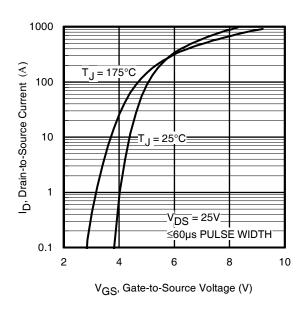


Fig 1. Typical Output Characteristics

Fig 2. Typical Output Characteristics



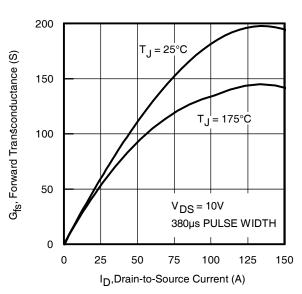
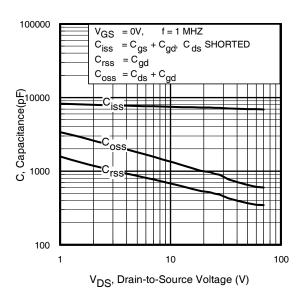


Fig 3. Typical Transfer Characteristics

Fig 4. Typical Forward Transconductance vs. Drain Current



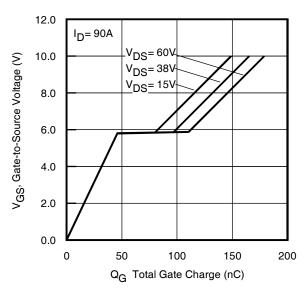
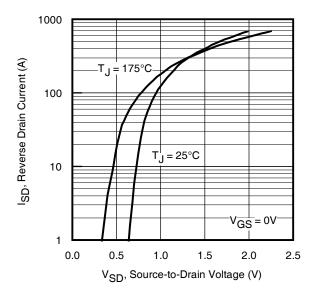


Fig 5. Typical Capacitance vs. Drain-to-Source Voltage

Fig 6. Typical Gate Charge vs. Gate-to-Source Voltage





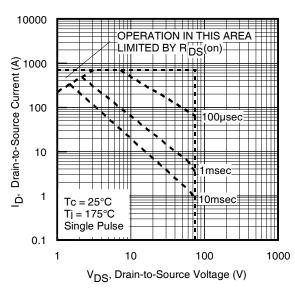
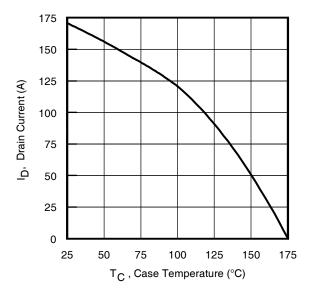


Fig 8. Maximum Safe Operating Area

nce



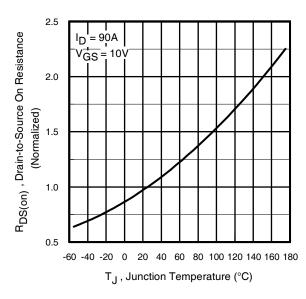


Fig 9. Maximum Drain Current vs. Case Temperature

Fig 10. Normalized On-Resistance vs. Temperature

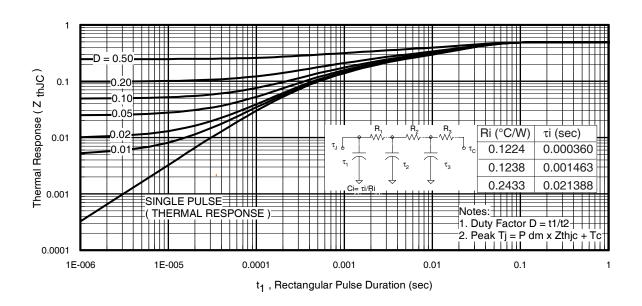


Fig 11. Maximum Effective Transient Thermal Impedance, Junction-to-Case

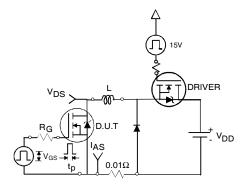


Fig 12a. Unclamped Inductive Test Circuit

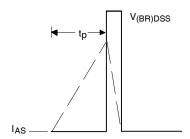


Fig 12b. Unclamped Inductive Waveforms

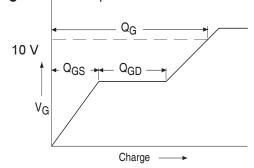


Fig 13a. Basic Gate Charge Waveform

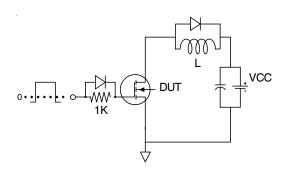


Fig 13b. Gate Charge Test Circuit www.irf.com

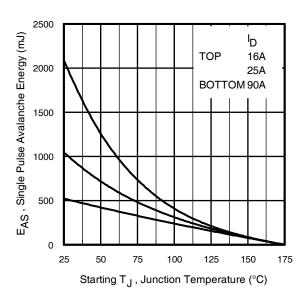


Fig 12c. Maximum Avalanche Energy vs. Drain Current

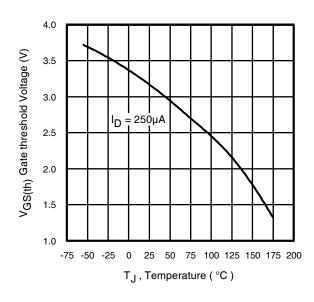


Fig 14. Threshold Voltage vs. Temperature

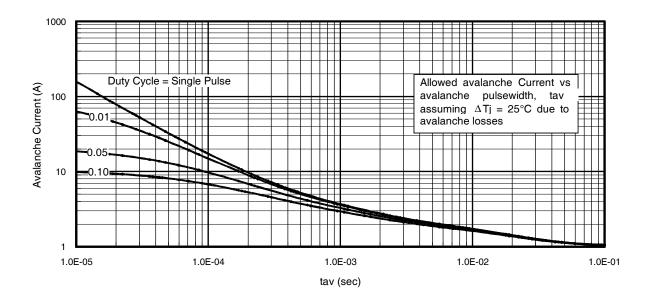


Fig 15. Typical Avalanche Current Vs. Pulsewidth

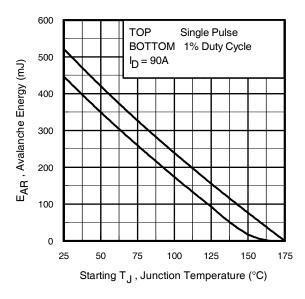


Fig 16. Maximum Avalanche Energy vs. Temperature

Notes on Repetitive Avalanche Curves , Figures 15, 16: (For further info, see AN-1005 at www.irf.com)

- Avalanche failures assumption:
 Purely a thermal phenomenon and failure occurs at a temperature far in excess of T_{jmax}. This is validated for every part type.
- Safe operation in Avalanche is allowed as long asT_{jmax} is not exceeded.
- 3. Equation below based on circuit and waveforms shown in Figures 12a, 12b.
- 4. P_{D (ave)} = Average power dissipation per single avalanche pulse.
- 5. BV = Rated breakdown voltage (1.3 factor accounts for voltage increase during avalanche).
- 6. I_{av} = Allowable avalanche current.
- 7. ΔT = Allowable rise in junction temperature, not to exceed T_{jmax} (assumed as 25°C in Figure 15, 16). t_{av} = Average time in avalanche.

 $D = Duty cycle in avalanche = t_{av} \cdot f$

 $Z_{th,JC}(D, t_{av})$ = Transient thermal resistance, see figure 11)

$$\begin{split} P_{D\;(ave)} &= 1/2\;(\;1.3 \cdot BV \cdot I_{aV}) = \triangle T/\;Z_{thJC} \\ I_{av} &= 2\triangle T/\;[1.3 \cdot BV \cdot Z_{th}] \\ E_{AS\;(AR)} &= P_{D\;(ave)} \cdot t_{av} \end{split}$$

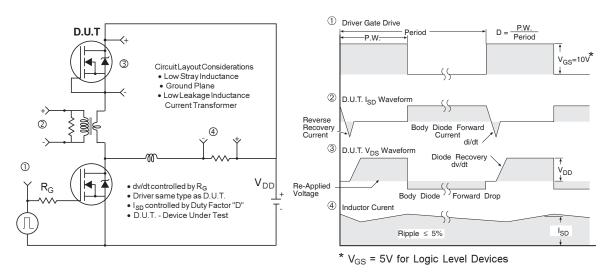


Fig 17. Peak Diode Recovery dv/dt Test Circuit for N-Channel HEXFET® Power MOSFETs

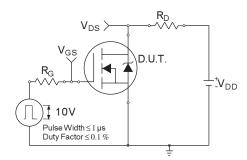


Fig 18a. Switching Time Test Circuit

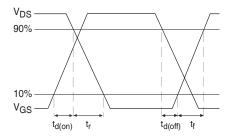
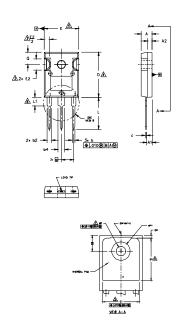


Fig 18b. Switching Time Waveforms

TO-247AC Package Outline

Dimensions are shown in millimeters (inches)



TES					

DIMENSIONING AND TOLERANCING AS PER ASME Y14.5M 1994

DIMENSIONS ARE SHOWN IN INCHES.

CONTOUR OF SLOT OPTIONAL.

DIMENSON D & E DO NOT INCLUDE MOLD FLASH, MOLD FLASH SHALL NOT EXCEED .005" (0.127)

PER SIDE. THESE DIMENSIONS ARE MEASURED AT THE OUTERWOST EXTREMES OF THE PLASTIC BODY.

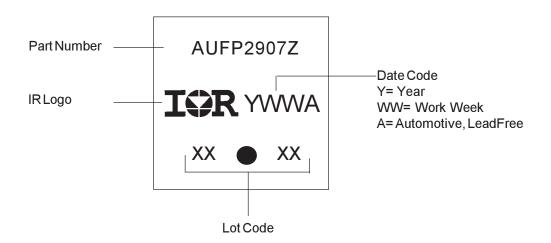
LEAD FINISH UNCONTROLLED IN L1.

PP TO HAVE A MAXIMUM DRAFT ANGLE OF 1.5 * TO THE TOP OF THE PART WITH A MAXIMUM HOLE DIAMETER OF .154 INCH.

OUTLINE CONFORMS TO JEDEC OUTLINE TO-247AC .

		DIMEN	ISIONS			
SYMBOL	INC	HES	MILLIN	IETERS		
i	MIN.	MAX.	MIN.	MAX.	NOTES	
A	.183	.209	4,65	5.31		
A1	.087	.102	2.21	2.59		
A2	.059	.098	1,50	2,49		
ь	.039	.055	0,99	1,40		
ь1	.039	.053	0.99	1.35		LEAD ASSIGNMENTS
b2	.065	.094	1,65	2,39		
b3	.065	.092	1.65	2.34		HEXFET
b4	.102	.135	2.59	3.43		HERE!
b5	.102	.133	2,59	3.38		1 GATE
С	.015	.035	0.38	0.89		2 DRAIN
c1	.015	.033	0.38	0.84		3 SOURCE
D	.776	.815	19,71	20,70	4	4 DRAIN
D1	.515	-	13.08	-	5	
D2	.020	.053	0.51	1.35		
E	.602	.625	15.29	15,87	4	IGBTs, CoPACK
E1	.530	-	13.46	-		1 GATE
E2	.178	.216	4.52	5,49		2 COLLECTOR
e i	.215	BSC	5.46	BSC		3 EMITTER
øk	.0	10	0.	25		4 COLLECTOR
L [.559	.634	14.20	16,10		4. COLLEGION
L1	.146	.169	3.71	4.29		
øР	.140	.144	3,56	3,66		DIODES
øP1	-	.291	-	7.39		
Q	.209	.224	5,31	5.69		 1,- ANODE/OPEN
s	.217	BSC	5.51	BSC		2 CATHODE
1						3. – ANODE

TO-247AC Part Marking Information



TO-247AC package is not recommended for Surface Mount Application.

Note: For the most current drawing please refer to IR website at http://www.irf.com/package/

Ordering Information

Base part	Package Type	Standard Pack		Complete Part Number
		Form	Quantity	
AUIRFP2907Z	TO-247	Tube	25	AUIRFP2907Z

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