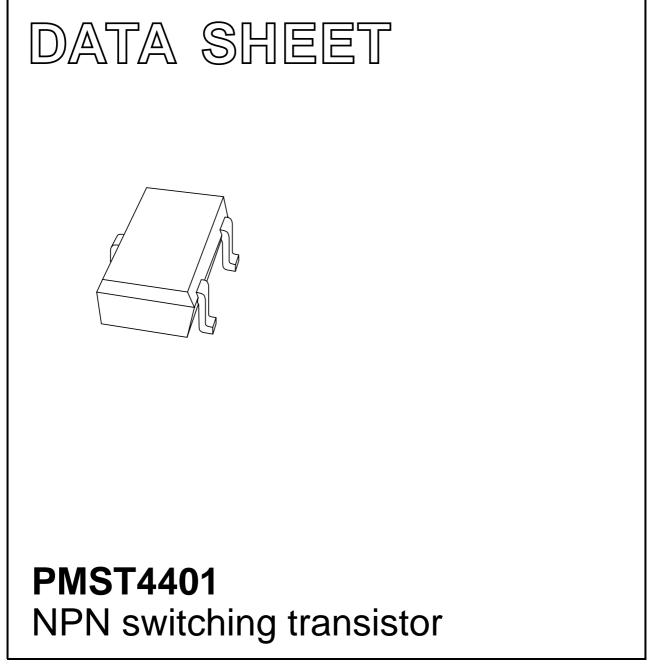
## DISCRETE SEMICONDUCTORS



Product data sheet Supersedes data of 1997 May 07 1999 Apr 22



### Product data sheet

## NPN switching transistor

### FEATURES

- High current (max. 600 mA)
- Low voltage (max. 40 V).

### APPLICATIONS

• General purpose switching and linear amplification, especially in portable equipment.

### DESCRIPTION

NPN switching transistor in a SOT323 plastic package. PNP complement: PMST4403.

#### MARKING

TYPE NUMBER	MARKING CODE <sup>(1)</sup>		
PMST4401	*2X		

#### Note

1. \* = - : Made in Hong Kong.

\* = t : Made in Malaysia.

### LIMITING VALUES

In accordance with the Absolute Maximum Rating System (IEC 134).

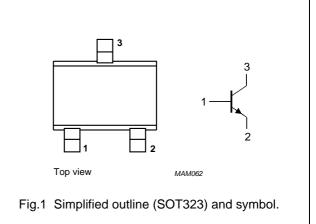
SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
V <sub>CBO</sub>	collector-base voltage	open emitter	_	60	V
V <sub>CEO</sub>	collector-emitter voltage	open base	_	40	V
V <sub>EBO</sub>	emitter-base voltage	open collector	-	6	V
I <sub>C</sub>	collector current (DC)		-	600	mA
I <sub>CM</sub>	peak collector current		-	600	mA
I <sub>BM</sub>	peak base current		-	200	mA
P <sub>tot</sub>	total power dissipation	$T_{amb} \le 25 \ ^{\circ}C$ ; note 1	-	200	mW
T <sub>stg</sub>	storage temperature		-65	+150	°C
Tj	junction temperature		_	150	°C
T <sub>amb</sub>	operating ambient temperature		-65	+150	°C

#### Note

1. Transistor mounted on an FR4 printed-circuit board.

#### PINNING

PIN	DESCRIPTION	
1	base	
2	emitter	
3	collector	



## **PMST4401**

## NPN switching transistor

## PMST4401

### THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
R <sub>th j-a</sub>	thermal resistance from junction to ambient	note 1	625	K/W

#### Note

1. Transistor mounted on an FR4 printed-circuit board.

### CHARACTERISTICS

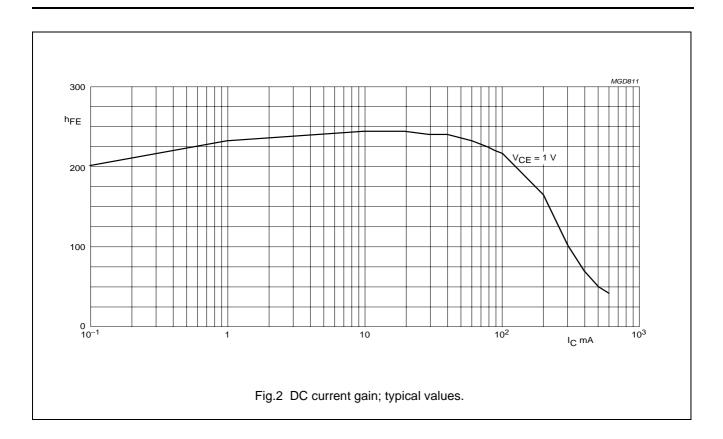
 $T_{amb}$  = 25 °C unless otherwise specified.

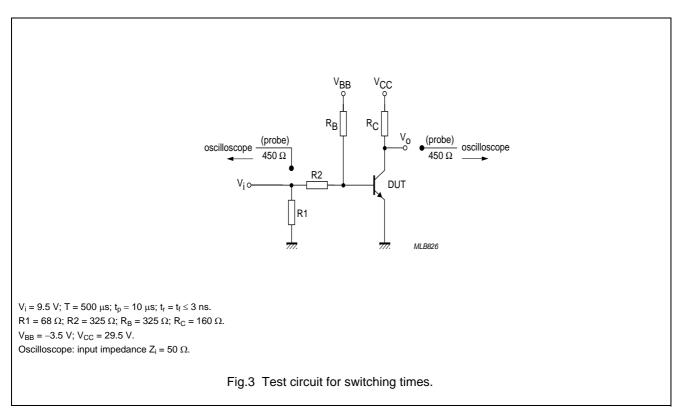
SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
I <sub>CBO</sub>	collector cut-off current	I <sub>E</sub> = 0; V <sub>CB</sub> = 60 V	_	50	nA
		I <sub>E</sub> = 0; V <sub>CB</sub> = 60 V; T <sub>j</sub> = 150 °C	_	10	μA
I <sub>EBO</sub>	emitter cut-off current	$I_{C} = 0; V_{EB} = 6 V$	_	50	nA
h <sub>FE</sub>	DC current gain	V <sub>CE</sub> = 1 V; (see Fig.2)			
		I <sub>C</sub> = 0.1 mA	20	_	
		$I_{\rm C} = 1  \rm{mA}$	40	_	
		I <sub>C</sub> = 10 mA	80	_	
		I <sub>C</sub> = 150 mA; note 1	100	300	
	DC current gain	I <sub>C</sub> = 500 mA; V <sub>CE</sub> = 2 V; note 1	40	-	
V <sub>CEsat</sub>	collector-emitter saturation voltage	I <sub>C</sub> = 150 mA; I <sub>B</sub> = 15 mA; note 1	_	400	mV
		I <sub>C</sub> = 500 mA; I <sub>B</sub> = 50 mA; note 1	-	750	mV
V <sub>BEsat</sub>	base-emitter saturation voltage	$I_{C} = 150 \text{ mA}; I_{B} = 15 \text{ mA}; \text{ note } 1$	_	950	mV
		$I_{\rm C} = 500 \text{ mA}; I_{\rm B} = 50 \text{ mA}; \text{ note } 1$	_	1.2	V
C <sub>c</sub>	collector capacitance	I <sub>E</sub> = i <sub>e</sub> = 0; V <sub>CB</sub> = 5 V; f = 1 MHz	_	8	pF
Ce	emitter capacitance	$I_E = i_e = 0; V_{EB} = 500 \text{ mV}; f = 1 \text{ MHz}$	_	30	pF
f <sub>T</sub>	transition frequency	I <sub>C</sub> = 20 mA; V <sub>CE</sub> = 10 V; f = 100 MHz	250	-	MHz
Switching t	imes (between 10% and 90% levels	<b>);</b> (see Fig.3)			
t <sub>on</sub>	turn-on time	I <sub>Con</sub> = 150 mA; I <sub>Bon</sub> = 15 mA;	-	35	ns
t <sub>d</sub>	delay time	I <sub>Boff</sub> = –15 mA	_	15	ns
t <sub>r</sub>	rise time	1	_	20	ns
t <sub>off</sub>	turn-off time	1	_	250	ns
t <sub>s</sub>	storage time	1	_	200	ns
t <sub>f</sub>	fall time	1	_	60	ns

#### Note

1. Pulse test:  $t_p \leq 300~\mu s;~\delta \leq 0.02.$ 

## NPN switching transistor



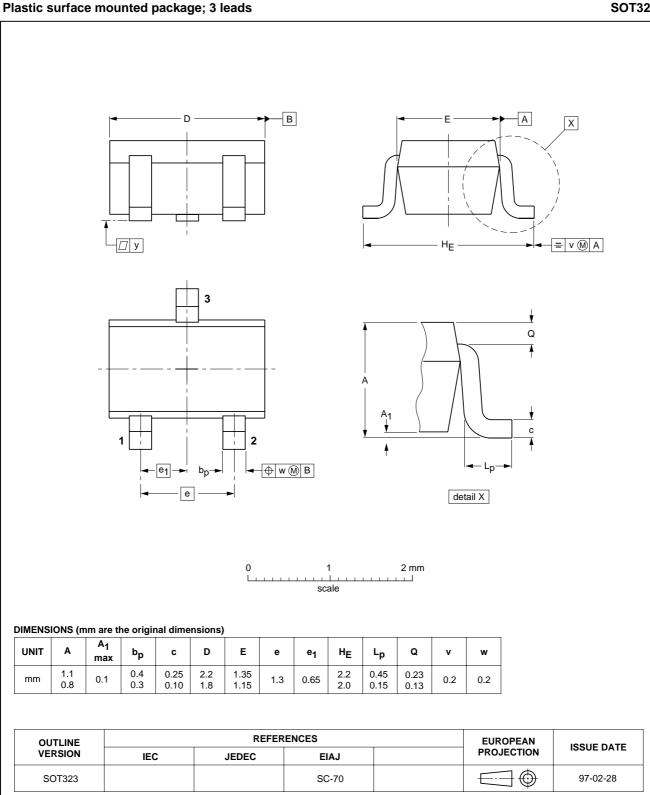


## PMST4401

**PMST4401** 

## NPN switching transistor

### **PACKAGE OUTLINE**



## NPN switching transistor

**PMST4401** 

### DATA SHEET STATUS

DOCUMENT STATUS <sup>(1)</sup>	PRODUCT STATUS <sup>(2)</sup>	DEFINITION
Objective data sheet	Development	This document contains data from the objective specification for product development.
Preliminary data sheet	Qualification	This document contains data from the preliminary specification.
Product data sheet	Production	This document contains the product specification.

#### Notes

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