

N-channel 100V 6.8 mΩ standard level MOSFET in TO220. 17 October 2013 Product data sheet

1. General description

Standard level N-channel MOSFET in TO220 package qualified to 175C. This product is designed and qualified for use in a wide range of industrial, communications and domestic equipment.

2. Features and benefits

- High efficiency due to low switching and conduction losses
- Improved dynamic avalanche performance
- Suitable for standard level gate drive

3. Applications

- DC-to-DC converters
- Load switching
- Motor control
- Server power supplies

4. Quick reference data

Table 1. C	Quick reference data						
Symbol	Parameter	Conditions		Min	Тур	Max	Unit
V _{DS}	drain-source voltage	T _j ≥ 25 °C; T _j ≤ 175 °C		-	-	100	V
I _D	drain current	T _{mb} = 25 °C; V _{GS} = 10 V; <u>Fig. 1</u>	[1]	-	-	100	А
P _{tot}	total power dissipation	T _{mb} = 25 °C; <u>Fig. 2</u>		-	-	269	W
Tj	junction temperature			-55	-	175	°C
Static chara	acteristics	1					
R _{DSon}	drain-source on-state resistance	V _{GS} = 10 V; I _D = 15 A; T _j = 100 °C; Fig. 12		-	-	12	mΩ
		V _{GS} = 10 V; I _D = 15 A; T _j = 25 °C; Fig. 13		-	5.4	6.8	mΩ
Dynamic ch	naracteristics	·					
Q _{GD}	gate-drain charge	V _{GS} = 10 V; I _D = 25 A; V _{DS} = 50 V; Fig. 15; Fig. 14		-	36	-	nC
Q _{G(tot)}	total gate charge	V_{GS} = 10 V; I _D = 25 A; V _{DS} = 50 V; Fig. 14; Fig. 15		-	125	-	nC





PSMN7R0-100PS

N-channel 100V 6.8 m Ω standard level MOSFET in TO220.

Symbol	Parameter	Conditions		Min	Тур	Мах	Unit
Avalanche ruggedness							
E _{DS(AL)S}	non-repetitive drain- source avalanche energy	V_{GS} = 10 V; $T_{j(init)}$ = 25 °C; I_D = 100 A; V_{sup} = 100 V; unclamped; R_{GS} = 50 Ω		-	-	316	mJ

[1] Continuous current is limited by package

5. Pinning information

Table 2.	Pinning	information		
Pin	Symbol	Description	Simplified outline	Graphic symbol
1	G	gate	mb	D
2	D	drain		
3	S	source		G-UFA
mb	D	mounting base; connected to drain		mbb076 S
			TO-220AB (SOT78)	

6. Ordering information

Table 3. Ordering in	formation					
Type number	Package					
	Name	Description	Version			
PSMN7R0-100PS	TO-220AB	plastic single-ended package; heatsink mounted; 1 mounting hole; 3-lead TO-220AB	SOT78			

7. Marking

Table 4. Marking codes	
Type number	Marking code
PSMN7R0-100PS	PSMN7R0-100PS

N-channel 100V 6.8 mΩ standard level MOSFET in TO220.

8. Limiting values

Table 5.Limiting values

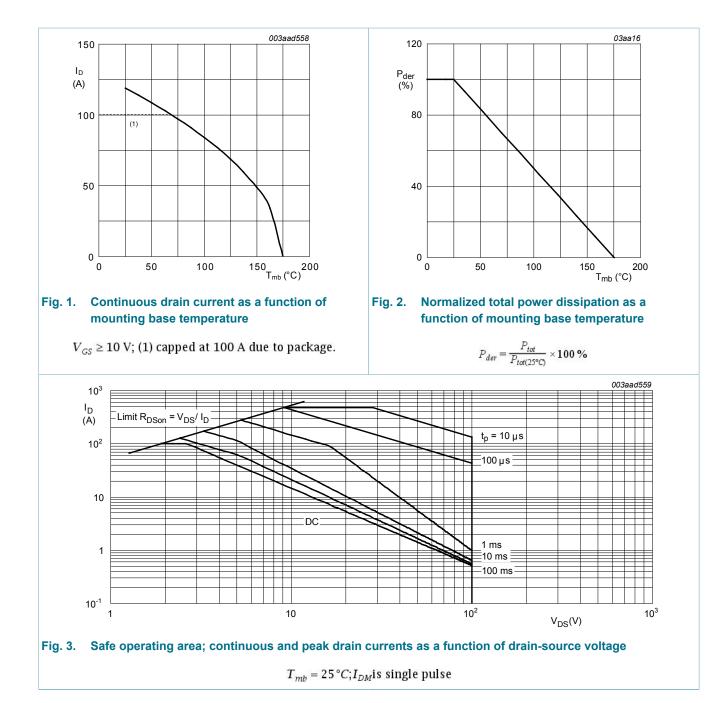
In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V _{DS}	drain-source voltage	T _j ≥ 25 °C; T _j ≤ 175 °C		-	100	V
V _{DGR}	drain-gate voltage	$T_j \le 175 \text{ °C}; T_j \ge 25 \text{ °C}; R_{GS} = 20 \text{ k}\Omega$		-	100	V
V _{GS}	gate-source voltage			-20	20	V
I _D	drain current	V _{GS} = 10 V; T _{mb} = 100 °C; <u>Fig. 1</u>		-	85	А
		V _{GS} = 10 V; T _{mb} = 25 °C; <u>Fig. 1</u>	[1]	-	100	А
I _{DM}	peak drain current	pulsed; $t_p \le 10 \ \mu s$; $T_{mb} = 25 \ ^{\circ}C$; Fig. 3		-	475	Α
P _{tot}	total power dissipation	T _{mb} = 25 °C; <u>Fig. 2</u>		-	269	W
T _{stg}	storage temperature			-55	175	°C
Tj	junction temperature			-55	175	°C
T _{sld(M)}	peak soldering temperature			-	260	°C
Source-dra	in diode					
I _S	source current	T _{mb} = 25 °C	[1]	-	100	А
I _{SM}	peak source current	pulsed; $t_p \le 10 \ \mu s$; $T_{mb} = 25 \ ^\circ C$		-	475	Α
Avalanche	ruggedness					
E _{DS(AL)S}	non-repetitive drain-source avalanche energy	$V_{GS} = 10 \text{ V}; \text{ T}_{j(init)} = 25 \text{ °C}; \text{ I}_{D} = 100 \text{ A};$ $V_{sup} = 100 \text{ V}; \text{ unclamped}; \text{ R}_{GS} = 50 \Omega$		-	316	mJ

[1] Continuous current is limited by package

PSMN7R0-100PS

N-channel 100V 6.8 m Ω standard level MOSFET in TO220.



9. Thermal characteristics

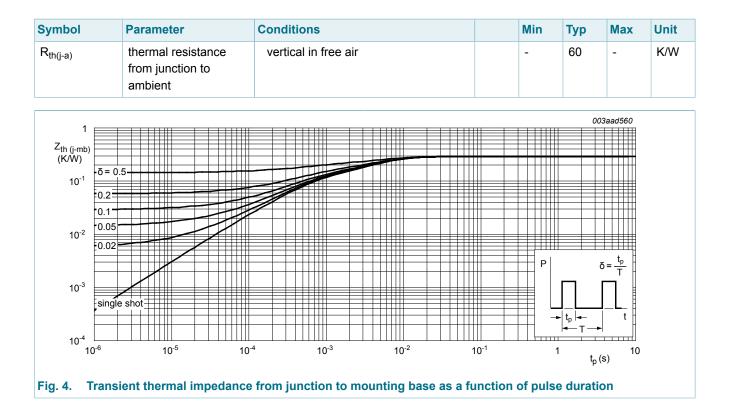
Table 6. The	rmal characteristics					
Symbol	Parameter	Conditions	Min	Тур	Max	Unit
R _{th(j-mb)}	thermal resistance from junction to mounting base	<u>Fig. 4</u>	-	0.3	0.56	K/W

PSMN7R0-100PS

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PSMN7R0-100PS

N-channel 100V 6.8 m Ω standard level MOSFET in TO220.



10. Characteristics

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
Static chara	acteristics	· · · · · ·	1			
V _{(BR)DSS}	drain-source	I _D = 0.25 mA; V _{GS} = 0 V; T _j = -55 °C	90	-	-	V
	breakdown voltage	I_D = 0.25 mA; V_{GS} = 0 V; T_j = 25 °C	100	-	-	V
V _{GS(th)}	gate-source threshold voltage	$I_D = 1 \text{ mA}; V_{DS} = V_{GS}; T_j = 175 \text{ °C};$ Fig. 10	1	-	-	V
		$I_D = 1 \text{ mA}; V_{DS} = V_{GS}; T_j = 25 \text{ °C};$ Fig. 11; Fig. 10	2	3	4	V
		$I_D = 1 \text{ mA}; V_{DS} = V_{GS}; T_j = -55 \text{ °C};$ Fig. 10	-	-	4.6	V
I _{DSS}	drain leakage current	V_{DS} = 100 V; V_{GS} = 0 V; T_j = 125 °C	-	-	150	μA
		V_{DS} = 100 V; V_{GS} = 0 V; T_j = 25 °C	-	0.08	5	μA
I _{GSS}	gate leakage current	V_{GS} = 20 V; V_{DS} = 0 V; T_j = 25 °C	-	10	100	nA
		V_{GS} = -20 V; V_{DS} = 0 V; T_j = 25 °C	-	10	100	nA
R _{DSon}	drain-source on-state resistance	V _{GS} = 10 V; I _D = 15 A; T _j = 100 °C; Fig. 12	-	-	12	mΩ
		V _{GS} = 10 V; I _D = 15 A; T _j = 175 °C; Fig. 12	-	15	19	mΩ

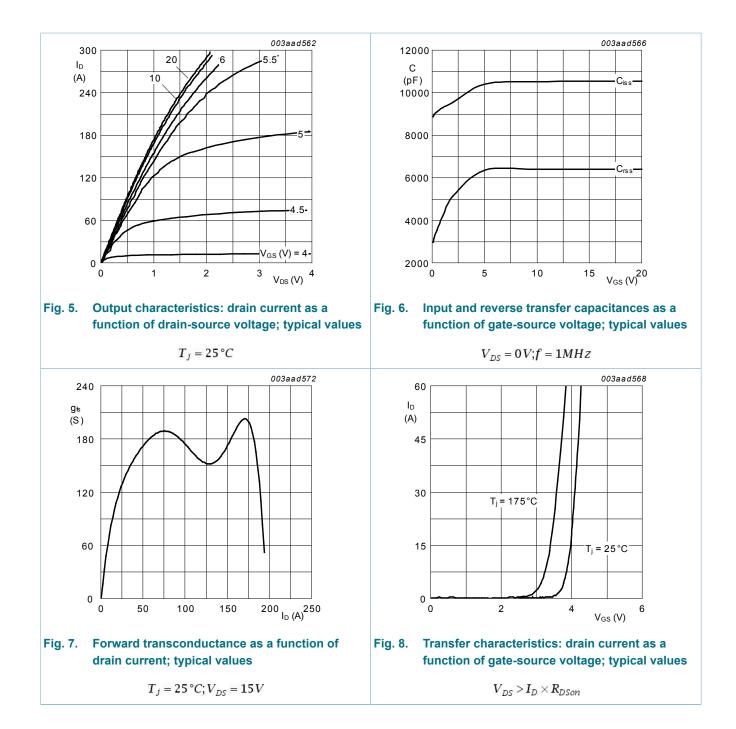
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N-channel 100V 6.8 m Ω standard level MOSFET in TO220.

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
		V _{GS} = 10 V; I _D = 15 A; T _j = 25 °C; Fig. 13	-	5.4	6.8	mΩ
R _G	internal gate resistance (AC)	f = 1 MHz	-	0.74	-	Ω
Dynamic ch	naracteristics	· · · ·	1			_
Q _{G(tot)}	total gate charge	I _D = 25 A; V _{DS} = 50 V; V _{GS} = 10 V; Fig. 14; Fig. 15	-	125	-	nC
		I _D = 0 A; V _{DS} = 0 V; V _{GS} = 10 V	-	100	-	nC
Q _{GS}	gate-source charge	I _D = 25 A; V _{DS} = 50 V; V _{GS} = 10 V; Fig. 15; Fig. 14	-	28	-	nC
Q _{GS(th)}	pre-threshold gate- source charge	I _D = 25 A; V _{DS} = 50 V; V _{GS} = 10 V; Fig. 15	-	19.4	-	nC
Q _{GS(th-pl)}	post-threshold gate- source charge		-	9	-	nC
Q _{GD}	gate-drain charge	I _D = 25 A; V _{DS} = 50 V; V _{GS} = 10 V; Fig. 15; Fig. 14	-	36	-	nC
V _{GS(pl)}	gate-source plateau voltage	V _{DS} = 50 V; <u>Fig. 15</u> ; <u>Fig. 14</u>	-	4.3	-	V
C _{iss}	input capacitance	V _{DS} = 50 V; V _{GS} = 0 V; f = 1 MHz;	-	6686	-	pF
C _{oss}	output capacitance	T _j = 25 °C; <u>Fig. 16</u>	-	438	-	pF
C _{rss}	reverse transfer capacitance		-	272	-	pF
t _{d(on)}	turn-on delay time	V_{DS} = 50 V; R _L = 2 Ω; V _{GS} = 10 V;	-	34.6	-	ns
t _r	rise time	R _{G(ext)} = 4.7 Ω; T _j = 25 °C	-	45.6	-	ns
t _{d(off)}	turn-off delay time		-	103.9	-	ns
t _f	fall time		-	49.5	-	ns
Source-dra	in diode	1	I			
V _{SD}	source-drain voltage	I_{S} = 25 A; V_{GS} = 0 V; T_{j} = 25 °C; <u>Fig. 17</u>	-	0.8	1.2	V
t _{rr}	reverse recovery time	$I_{\rm S}$ = 25 A; dI _S /dt = 100 A/µs; V _{GS} = 0 V;	-	64	-	ns
Q _r	recovered charge	V _{DS} = 50 V	-	167	-	nC

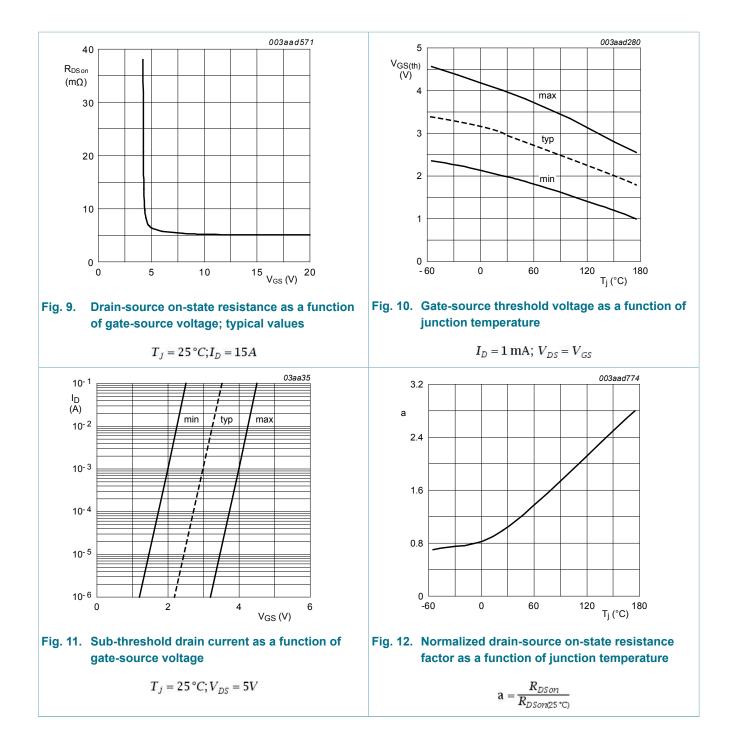
PSMN7R0-100PS

N-channel 100V 6.8 mΩ standard level MOSFET in TO220.



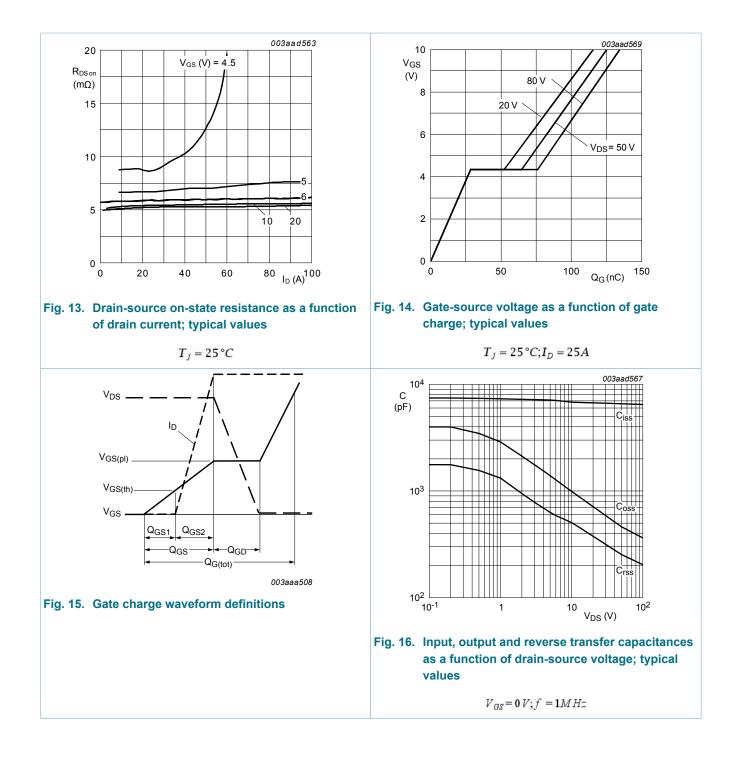
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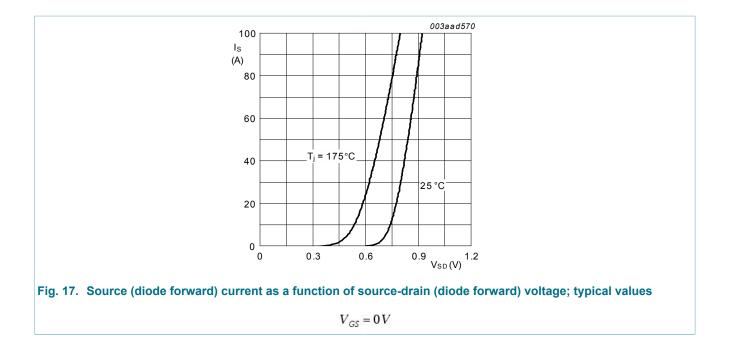
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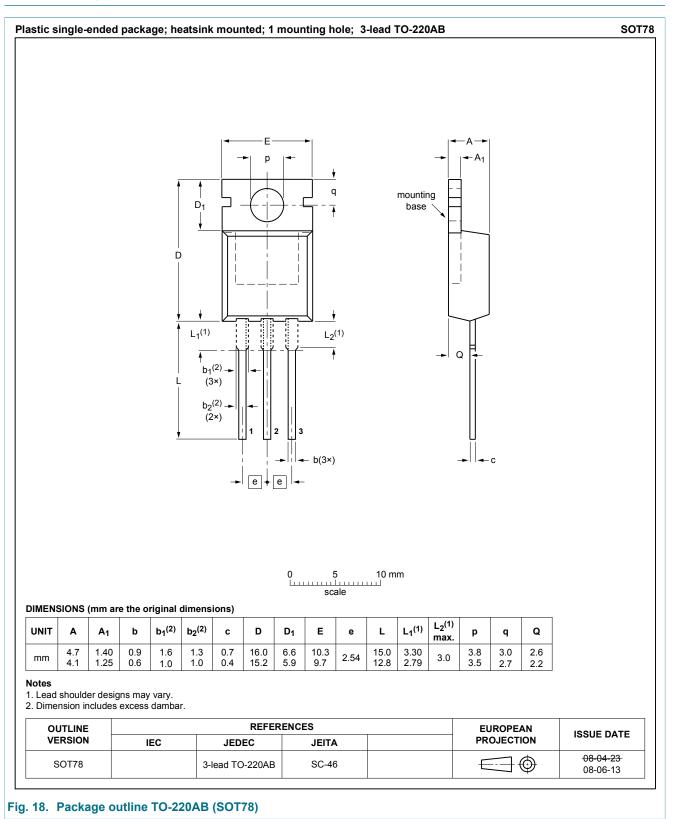
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11. Package outline



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12. Legal information

12.1 Data sheet status

Document status [1][2]	Product status [<u>3]</u>	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
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Product [short] data sheet	Production	This document contains the product specification.

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N-channel 100V 6.8 m Ω standard level MOSFET in TO220.

13. Contents

1	General description	1
2	Features and benefits	1
3	Applications	1
4	Quick reference data	1
5	Pinning information	2
6	Ordering information	2
7	Marking	2
8	Limiting values	3
9	Thermal characteristics	4
10	Characteristics	5
		•••••
11	Package outline	
11 12	Package outline Legal information	11
••	-	11 12
12	Legal information	11 12 12
12 12.1	Legal information Data sheet status	11 12 12

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