2SD1618

Bipolar Transistor 15V, 0.7A, Low VCE(sat), NPN Single PCP



Features

- · Low collector-to-emitter saturation voltage
- · Very small size making it easy to provide highdensity, small-sized hybrid IC's

Specifications

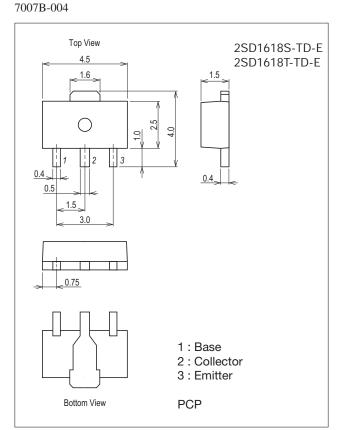
Absolute Maximum Ratings at Ta=25°C

	0			
Parameter	Symbol	Conditions	Ratings	Unit
Collector-to-Base Voltage	VCBO		20	V
Collector-to-Emitter Voltage	VCEO		15	V
Emitter-to-Base Voltage	VEBO		5	V
Collector Current	IC		0.7	Α
Collector Current (Pulse)	ICP		1.5	А
Collector Dissipation	De		500	mW
	PC	When mounted on ceramic substrate (250mm ² ×0.8mm)	1.3	W
Junction Temperature	Tj		150	°C
Storage Temperature	Tstg		-55 to +150	°C

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

Package Dimensions

unit : mm (typ)



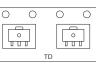
Product & Package Information

: PCP

- Package
- JEITA, JEDEC
- : SC-62, SOT-89, TO-243 • Minimum Packing Quantity : 1,000 pcs./reel

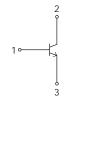
Packing Type: TD

Marking





Electrical Connection



Electrical Characteristics at Ta=25°C

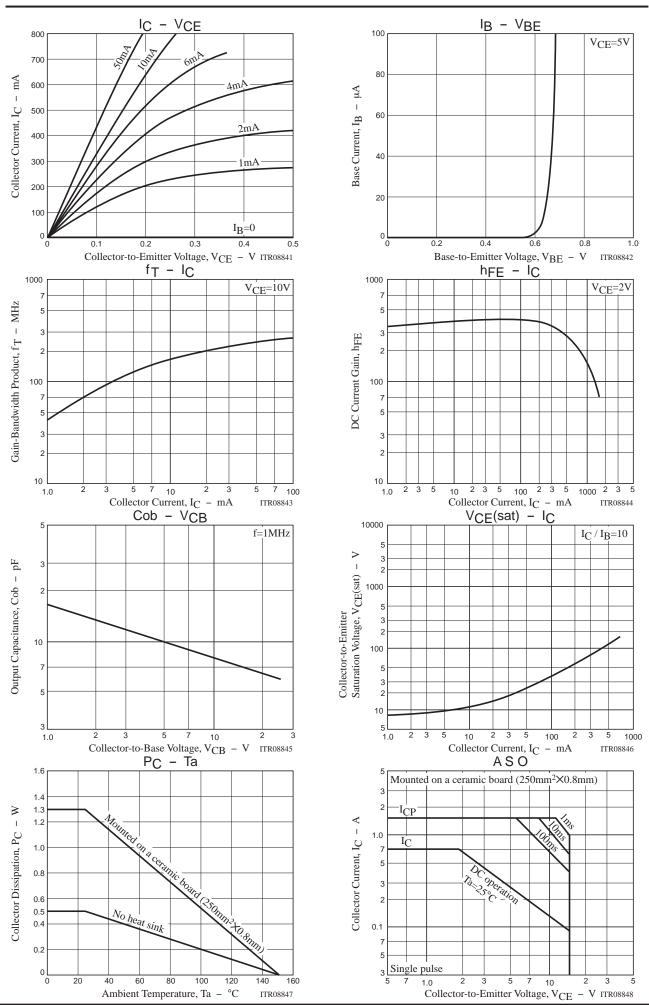
Parameter	Symbol	Conditions	Ratings			Linit	
Parameter	Symbol Conditions -		min	typ	max	Unit	
Collector Cutoff Current	ICBO	V _{CB} =15V, I _E =0A			0.1	μΑ	
Emitter Cutoff Current	IEBO	V _{EB} =4V, I _C =0A			0.1	μΑ	
	hFE1	V _{CE} =2V, I _C =50mA	140*		560*		
DC Current Gain	hFE2	V _{CE} =2V, I _C =500mA	60				
Gain-Bandwidth Product	fT	VCE=10V, IC=50mA		250		MHz	
Collector to Emitter Seturation Voltage	V _{CE} (sat)1	IC=5mA, IB=0.5mA		10	25	mV	
Collector-to-Emitter Saturation Voltage	V _{CE} (sat)2	IC=100mA, IB=10mA		30	80	mV	
Base-to-Emitter Saturation Voltage	V _{BE} (sat)	IC=100mA, IB=10mA		0.8	1.2	V	
Collector-to-Base Breakdown Voltage	V(BR)CBO	IC=10μA, IE=0A	20			V	
Collector-to-Emitter Breakdown Voltage	V(BR)CEO	IC=1mA, RBE=∞	15			V	
Emitter-to-Base Breakdown Voltage	V(BR)EBO	I _E =10μA, I _C =0A	5			V	
Output Capacitance	Cob	V _{CB} =10V, f=1MHz		8		pF	

 * : The 2SD1618 is classified by 50mA hFE as follows :

Rank	S	Т	U	
hFE	140 to 280	200 to 400	280 to 560	

Ordering Information

Device	Package	Shipping	memo
2SD1618S-TD-E	PCP	1,000pcs./reel	Dh Free
2SD1618T-TD-E	PCP	1,000pcs./reel	Pb Free



Bag Packing Specification 2SD1618S-TD-E, 2SD1618T-TD-E

1. Packing Format

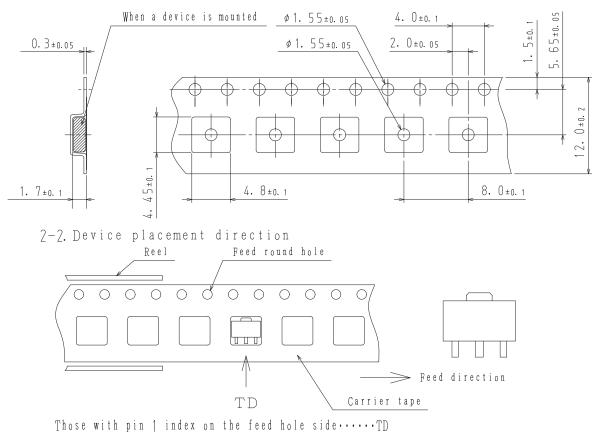
Package Name	Carrier Tape	rier Tape Maximum Number of devices contained (pcs)			Packing format		
	Туре	Reel	Inner box	Outer box	Inner BOX (C-1) Outer BOX (A-7)		
РСР	PCP	1,000	4,000	24,000	4 reels contained 6 inner boxes contained		
				2	Dimensions:mm (external) Dimensions:mm (external)		
					183×72×185 440×195×210		
Packing met	h o d			(u 1	n i t :mm) The form of a label may change in physical distribution process,		
0			<	6	59 108		
	Type LOT Quan Orig Reel la	No. tity in	-> (17 -> (00 (2) -> As NOTE	н плининининининининини в ют оо атту о, ос н плининининининининининининининининининин	0.05310C* Instance Instance DIFFUSION:*****) Instance SPECIAL SPECIAL #1000000000000000000000000000000000000		
				Label LEAD FRH			

LEAD FREE 4

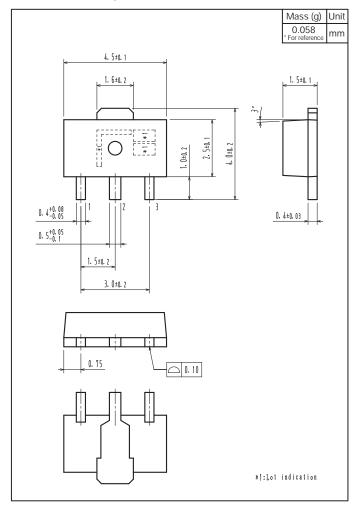
JEITA Phase 3

2. Taping configuration

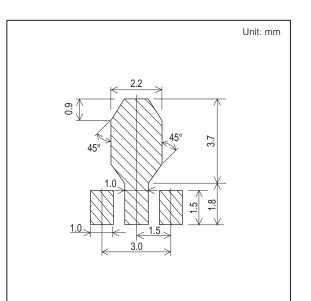
2-1. Carrier tape size (unit:mm)



Outline Drawing 2SD1618S-TD-E, 2SD1618T-TD-E



Land Pattern Example



ON Semiconductor and the ON logo are registered trademarks of Semiconductor Components Industries, LLC (SCILLC). SCILLC owns the rights to a number of patents, trademarks, copyrights, trade secrets, and other intellectual property. A listing of SCILLC's product/patent coverage may be accessed at www.onsemi.com/site/pdf/Patent-Marking.pdf. SCILLC reserves the right to make changes without further notice to any products herein. SCILLC makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does SCILLC assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. "Typical" parameters which may be provided in SCILLC data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typical" must be validated for each customer application by customer's technical experts. SCILLC does not convey any license under its patent rights nor the rights of others. SCILLC products are not designed, intended, or authorized for use as components in systems intended for surgical implant into the body, or other applications intended to support or use SCILLC products for any such unintended or unauthorized application, Buyer shall indemnify and hold SCILLC was negligent regarding the design or manufacture of the part. SCILLC protuntly and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death maleges that SCILLC was negligent regarding the design or manufacture of the part. SCILLC is an Equal Opportunity/Affirmative Action Employer. This literature is subject to all applicable copyright laws and is not for resale in any manner.

Mouser Electronics

Authorized Distributor

Click to View Pricing, Inventory, Delivery & Lifecycle Information:

ON Semiconductor: 2SD1618S-TD-E 2SD1618T-TD-E