# **Switching Diode**

### Features

- Low Leakage Current Applications
- Medium Speed Switching Times
- Available in 8 mm Tape and Reel Use BAS116LT1G to order the 7 inch/3,000 unit reel
- S and NSV Prefix for Automotive and Other Applications Requiring Unique Site and Control Change Requirements; AEC–Q101 Qualified and PPAP Capable
- These Devices are Pb–Free, Halogen Free/BFR Free and are RoHS Compliant



## **ON Semiconductor®**

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Rating	Symbol	Value	Unit
Continuous Reverse Voltage	V <sub>R</sub>	75	Vdc
Peak Forward Current	١ <sub>F</sub>	200	mAdc
Peak Forward Surge Current	I <sub>FM(surge)</sub>	500	mAdc

### THERMAL CHARACTERISTICS

Characteristic	Symbol	Мах	Unit
Total Device Dissipation FR–5 Board (Note 1) $T_A = 25^{\circ}C$	PD	225	mW
Derate above 25°C		1.8	mW/°C
Thermal Resistance, Junction-to-Ambient	$R_{\thetaJA}$	556	°C/W
Total Device Dissipation Alumina Substrate (Note 2) $T_A = 25^{\circ}C$	PD	300	mW
Derate above 25°C		2.4	mW/°C
Thermal Resistance, Junction-to-Ambient	$R_{\thetaJA}$	417	°C/W
Junction and Storage Temperature	T <sub>J</sub> , T <sub>stg</sub>	–55 to +150	°C

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

1. FR-5 =  $1.0 \times 0.75 \times 0.062$  in.

2. Alumina = 0.4  $\times$  0.3  $\times$  0.024 in. 99.5% alumina.



SOT-23 (TO-236) CASE 318 STYLE 8

### MARKING DIAGRAM



JV = Specific Device Code M = Date Code\* = Pb-Free Package

(Note: Microdot may be in either location)

\*Date Code orientation and/or overbar may vary depending upon manufacturing location.

### **ORDERING INFORMATION**

Device Package		Shipping <sup>†</sup>	
BAS116LT1G SBAS116LT1G	SOT-23 (Pb-Free)	3000 / Tape & Reel	
BAS116LT3G NSVBAS116LT3G	SOT-23 (Pb-Free)	10000 / Tape & Reel	

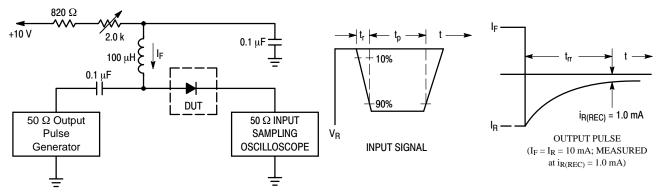
+For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

# BAS116L

### **ELECTRICAL CHARACTERISTICS** ( $T_A = 25^{\circ}C$ unless otherwise noted)

Characteristic	Symbol	Min	Max	Unit		
OFF CHARACTERISTICS						
Reverse Breakdown Voltage ( $I_{BR}$ = 100 µAdc)	V <sub>(BR)</sub>	75	-	Vdc		
Reverse Voltage Leakage Current (V <sub>R</sub> = 75 Vdc) (V <sub>R</sub> = 75 Vdc, T <sub>J</sub> = 150°C)	I <sub>R</sub>	-	5.0 80	nAdc		
Forward Voltage (I <sub>F</sub> = 1.0 mAdc) (I <sub>F</sub> = 10 mAdc) (I <sub>F</sub> = 50 mAdc) (I <sub>F</sub> = 150 mAdc)	VF	- - - -	900 1000 1100 1250	mV		
Diode Capacitance ( $V_R = 0 V$ , f = 1.0 MHz)	CD	-	2.0	pF		
Reverse Recovery Time ( $I_F = I_R = 10 \text{ mAdc}$ ) (Figure 1)	t <sub>rr</sub>	-	3.0	μs		

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.



1. A 2.0 k $\Omega$  variable resistor adjusted for a Forward Current (IF) of 10 mA.

2. Input pulse is adjusted so  $I_{R(peak)}$  is equal to 10 mA.

3. t<sub>p</sub> » t<sub>rr</sub>

Figure 1. Recovery Time Equivalent Test Circuit

## BAS116L

### **TYPICAL CHARACTERISTICS**

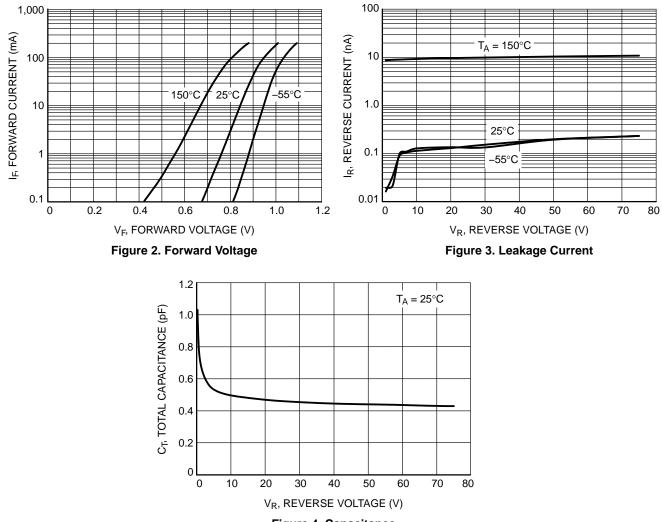
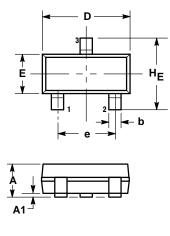


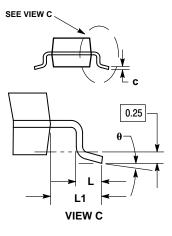
Figure 4. Capacitance

### BAS116L

#### PACKAGE DIMENSIONS

SOT-23 (TO-236) CASE 318-08 ISSUE AP





NOTES: 1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982. 2. CONTROLLING DIMENSION: INCH.

- CONTROLLING DIMENSION: INCH.
  MAXIMUM LEAD THICKNESS INCLUDES LEAD FINISH THICKNESS. MINIMUM LEAD THICKNESS IS THE MINIMUM
- THICKNESS OF BASE MATERIAL.
  DIMENSIONS D AND E DO NOT INCLUDE MOLD FLASH, PROTRUSIONS, OR GATE BURRS.

	MILLIMETERS			INCHES		
DIM	MIN	NOM	MAX	MIN	NOM	MAX
Α	0.89	1.00	1.11	0.035	0.040	0.044
A1	0.01	0.06	0.10	0.001	0.002	0.004
b	0.37	0.44	0.50	0.015	0.018	0.020
С	0.09	0.13	0.18	0.003	0.005	0.007
D	2.80	2.90	3.04	0.110	0.114	0.120
Е	1.20	1.30	1.40	0.047	0.051	0.055
е	1.78	1.90	2.04	0.070	0.075	0.081
L	0.10	0.20	0.30	0.004	0.008	0.012
L1	0.35	0.54	0.69	0.014	0.021	0.029
HE	2.10	2.40	2.64	0.083	0.094	0.104
θ	0°		10°	0°		10°

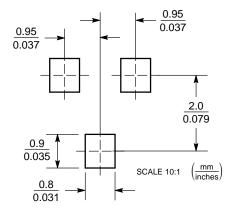
STYLE 8:

PIN 1. ANODE 2. NO CONNE

NO CONNECTION
 CATHODE

#### . CATHODE

### SOLDERING FOOTPRINT\*



\*For additional information on our Pb–Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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