CPH3456

• ON-Resistance $R_{DS}(on)1=54m\Omega$ (typ)

• Pb-Free, Halogen Free and RoHS Compliance

Power MOSFET 20V, 71mΩ, 3.5A, Single N-Channel



VDSS	R _{DS} (on) Max	ID Max	
20V	71 mΩ@4.5V		
	103 mΩ@2.5V	3.5A	
	156 mΩ@1.8V		



Absolute Maximum Ratings at Ta = 25°C Unit Parameter Symbol Value Drain to Source Voltage VDSS 20 V V Gate to Source Voltage ±12 VGSS Drain Current (DC) 3.5 А ١D Drain Current (Pulse) 14 А IDP $\mathsf{PW}{\leq}10\mu s,\,duty\,cycle{\leq}1\%$ Power Dissipation When mounted on ceramic substrate PD 1.0 w (900mm²×0.8mm) °C Junction Temperature Τj 150 °C Storage Temperature Tstg -55 to +150

This product is designed to "ESD immunity < 200V*", so please take care when handling.

* Machine Model

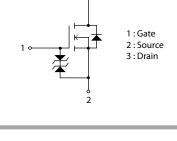
Features

• 1.8V Drive

Specifications

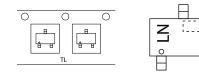
Thermal Resistance Ratings

Parameter	Symbol	Value	Unit
Junction to Ambient			
When mounted on ceramic substrate	R_{\thetaJA}	125	°C/W
(900mm ² ×0.8mm)			



Packing Type:TL

Marking



Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

ORDERING INFORMATION

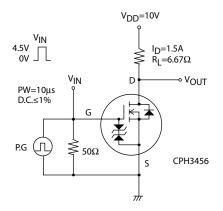
See detailed ordering and shipping information on page 5 of this data sheet.

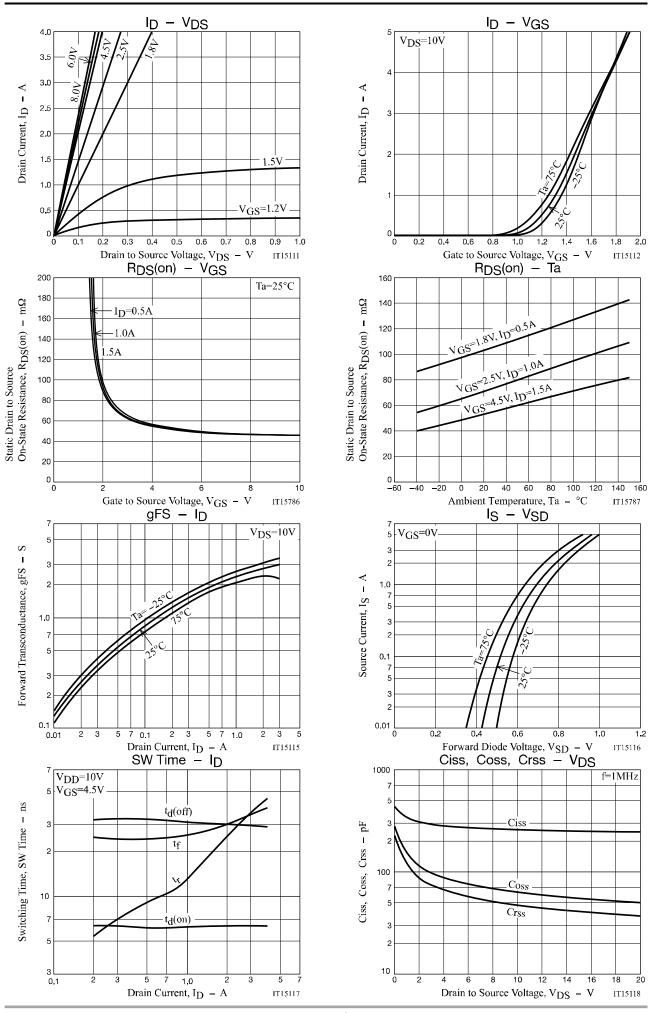
Electrical Characteristics at $Ta = 25^{\circ}C$

Parameter	Currente al			Value		
Parameter	Symbol	Conditions	min	typ	max	Unit
Drain to Source Breakdown Voltage	V(BR)DSS	I _D =1mA, V _{GS} =0V	20			V
Zero-Gate Voltage Drain Current	IDSS	V _{DS} =20V, V _{GS} =0V			1	μΑ
Gate to Source Leakage Current	IGSS	V _{GS} =±8V, V _{DS} =0V			±10	μΑ
Gate Threshold Voltage	V _{GS} (th)	V _{DS} =10V, I _D =1mA	0.4		1.3	V
Forward Transconductance	9FS	V _{DS} =10V, I _D =1.5A		2.8		S
	R _{DS} (on)1	I _D =1.5A, V _{GS} =4.5V		54	71	mΩ
Static Drain to Source On-State Resistance	R _{DS} (on)2	I _D =1A, V _{GS} =2.5V		73	103	mΩ
	R _{DS} (on)3	I _D =0.5A, V _{GS} =1.8V		104	156	mΩ
Input Capacitance	Ciss			260		pF
Output Capacitance	Coss	V _{DS} =10V, f=1MHz		65		pF
Reverse Transfer Capacitance	Crss			50		pF
Turn-ON Delay Time	t _d (on)			6.2		ns
Rise Time	tr			19		ns
Turn-OFF Delay Time	t _d (off)	See specified Test Circuit		30		ns
Fall Time	tf			28		ns
Total Gate Charge	Qg			2.8		nC
Gate to Source Charge	Qgs	V _{DS} =10V, V _{GS} =4.5V, I _D =3.5A		0.6		nC
Gate to Drain "Miller" Charge	Qgd	1		0.9		nC
Forward Diode Voltage	V _{SD}	I _S =3.5A, V _{GS} =0V		0.85	1.2	V

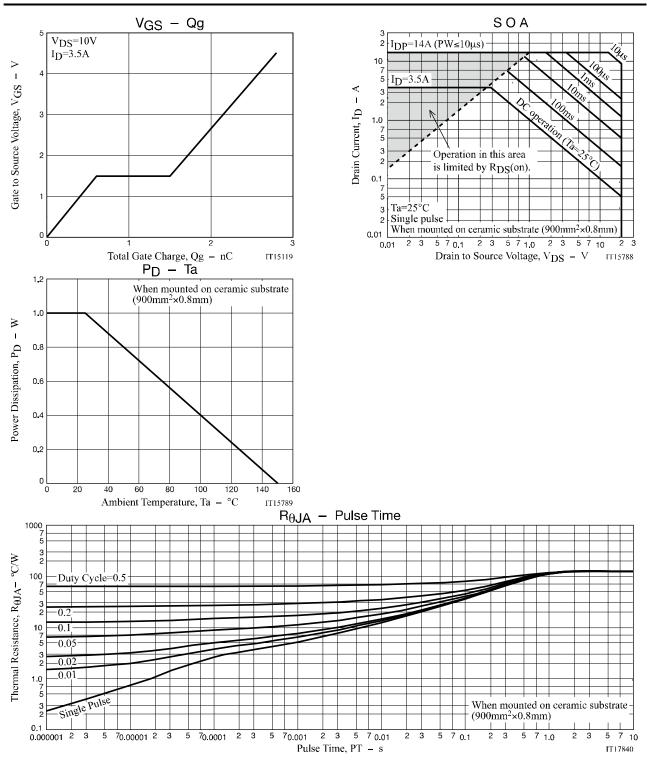
Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

Switching Time Test Circuit





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Package Dimensions

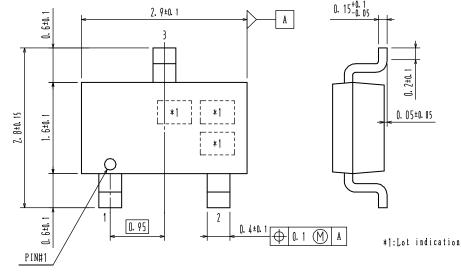
CPH3456-TL-H/ CPH3456-TL-W

CPH3

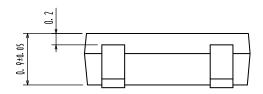
CASE 318BA ISSUE O

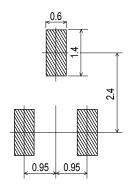
Unit : mm

- 1 : Gate
- 2 : Source
- 3 : Drain



Recommended Soldering Footprint





ORDERING INFORMATION

Device	Package	Shipping	Note	
CPH3456-TL-H	CPH3, SC-59	3,000	Pb-Free and	
CPH3456-TL-W	SOT-23, TO-236	pcs. / reel	Halogen Free	

Note on usage : Since the CPH3456 is a MOSFET product, please avoid using this device in the vicinity of highly charged objects.

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