CPH6442

Power MOSFET 60V, $43m\Omega$, 6A, Single N-Channel



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- 4V Drive
- Low On-Resistance
- ESD Diode-Protected Gate
- Pb-Free, and RoHS Compliance
- Halogen Free Compliance : CPH6442-TL-W

VDSS	RDS(on) Max	ID Max
	43 mΩ@10V	
60V	59 mΩ@4.5V	6A
	65 mΩ@4V	

Electrical Connection N-Channel

1, 2, 5, 6

Specifications

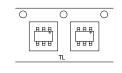
Absolute Maximum Ratings at Ta = 25°C

Parameter	Symbol	Value	Unit
Drain to Source Voltage	V _{DSS}	60	٧
Gate to Source Voltage	VGSS	±20	٧
Drain Current (DC)	ID	6	Α
Drain Current (Pulse) PW≤10μs, duty cycle≤1%	I _{DP}	24	А
Power Dissipation When mounted on ceramic substrate (900mm² × 0.8mm)	PD	1.6	W
Junction Temperature	Tj	150	°C
Storage Temperature	Tstg	–55 to +150	°C

Packing Type : TL

Marking

1 : Drain 2 : Drain 3 : Gate 4 : Source 5 : Drain





Thermal Resistance Ratings

Parameter	Symbol	Value	Unit
Junction to Ambient			
When mounted on ceramic substrate	$R_{\theta JA}$	78.1	°C/W
(900mm ² × 0.8mm)			

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

ORDERING INFORMATION

See detailed ordering and shipping information on page 5 of this data sheet.

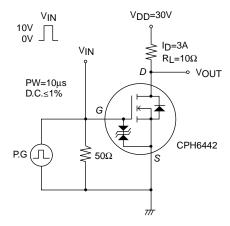
CPH6442

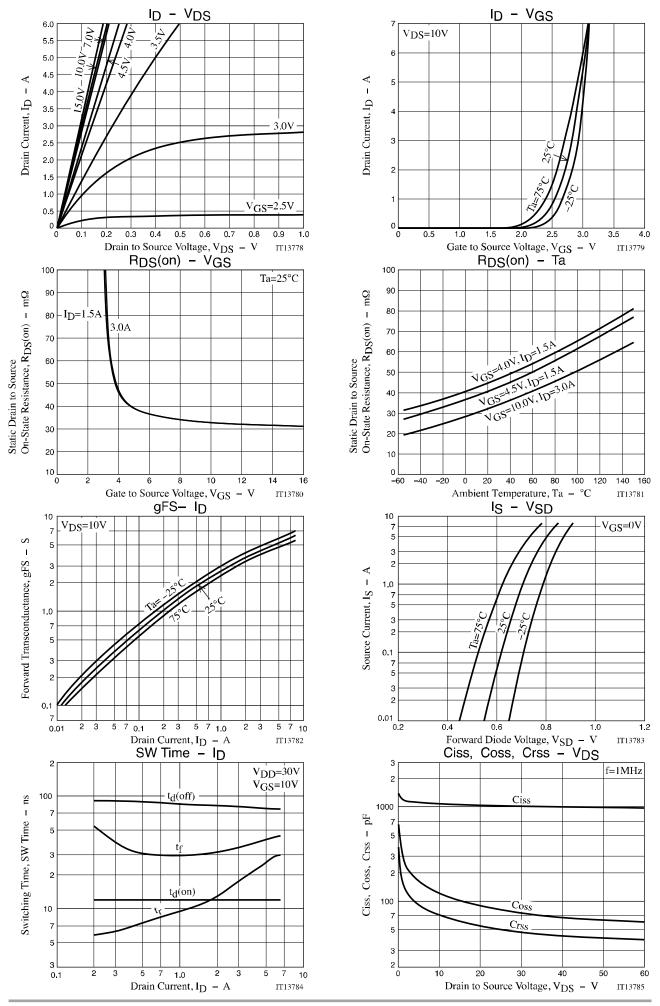
Electrical Characteristics at Ta = 25°C

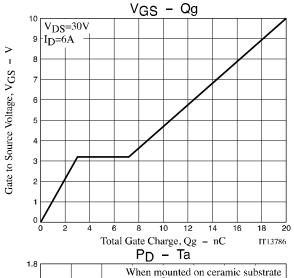
Ptu	0	Conditions		Value		11.2
Parameter	Symbol	Conditions	min	typ	max	Unit
Drain to Source Breakdown Voltage	V(BR)DSS	I _D =1mA, V _{GS} =0V	60			V
Zero-Gate Voltage Drain Current	IDSS	V _{DS} =60V, V _{GS} =0V			1	μΑ
Gate to Source Leakage Current	IGSS	V _{GS} =±16V, V _{DS} =0V			±10	μΑ
Gate Threshold Voltage	V _{GS} (th)	V _{DS} =10V, I _D =1mA	1.2		2.6	٧
Forward Transconductance	9FS	V _{DS} =10V, I _D =3A	2.6	4.4		S
	R _{DS} (on)1	I _D =3A, V _{GS} =10V		33	43	mΩ
Static Drain to Source On-State Resistance	R _{DS} (on)2	I _D =1.5A, V _{GS} =4.5V		42	59	mΩ
	R _{DS} (on)3	I _D =1.5A, V _{GS} =4V		46	65	mΩ
Input Capacitance	Ciss			1040		pF
Output Capacitance	Coss	V _{DS} =20V, f=1MHz		90		pF
Reverse Transfer Capacitance	Crss			55		pF
Turn-ON Delay Time	t _d (on)			12		ns
Rise Time	t _r	Out to the Art Olive II		18		ns
Turn-OFF Delay Time	t _d (off)	See specified Test Circuit		80		ns
Fall Time	tf			35		ns
Total Gate Charge	Qg			20		nC
Gate to Source Charge	Qgs	V _{DS} =30V, V _{GS} =10V, I _D =6A		3.0		nC
Gate to Drain "Miller" Charge	Qgd	7		4.2		nC
Forward Diode Voltage	V _{SD}	I _S =6A, V _{GS} =0V		0.82	1.2	V

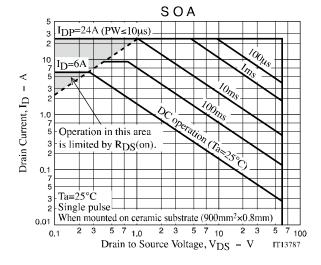
Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

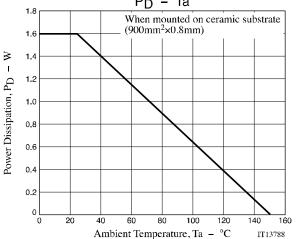
Switching Time Test Circuit

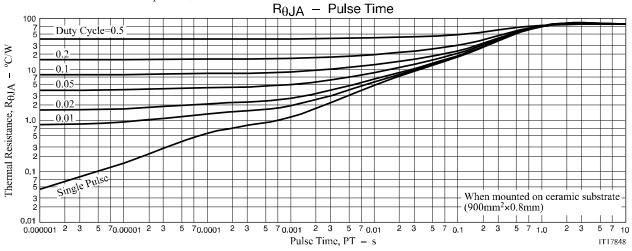












Package Dimensions

CPH6442-TL-E / CPH6442-TL-W

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CASE 318BD ISSUE O

Unit: mm

1: Drain

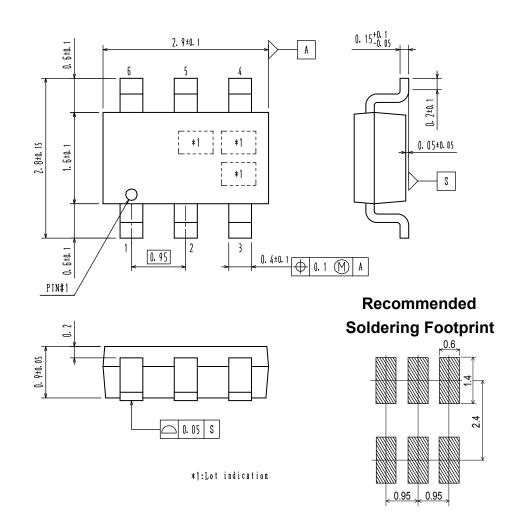
2: Drain

3: Gate

4 : Source

5: Drain

6: Drain



ORDERING INFORMATION

Device	Package	Shipping	Note
CPH6442-TL-E	CPH6, SC-74	3,000 pcs. / Tape & Reel	Pb-Free
CPH6442-TL-W	SOT-26, SOT-457	3,000 pcs. / Tape & Neel	Pb-Free and Halogen Free

Note on usage: Since the CPH6442 is a MOSFET product, please avoid using this device in the vicinity of highly charged objects.

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