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# N-Channel IGBT 600V, 20A, VCE(sat);1.45V TO-3PF-3L with Low VF Switching Diode

#### **Features**

- IGBT V<sub>CE</sub>(sat)=1.45V typ. (I<sub>C</sub>=20A, V<sub>GE</sub>=15V)
- IGBT t<sub>f</sub>=67ns typ.
- Diode  $V_F=1.5V$  typ.  $(I_F=20A)$
- Diode t<sub>rr</sub>=70ns typ.

- Adaption of full isolation type package
- Enhansment type
- Maxium junction temperature Tj=175°C

## **Applications**

• Power factor correction of white goods appliance

• General purpose inverter

## **Specifications**

**Absolute Maximum Ratings** at Ta = 25°C, Unless otherwise specified

Parameter	Symbol	Conditions		Ratings	Unit
Collector to Emitter Voltage	VCES			600	٧
Gate to Emitter Voltage	VGES			±20	V
	1 +4		@ Tc=25°C *2	40	Α
Collector Current (DC)	IC*1	Limited by Tjmax	@ Tc=100°C *2	20	Α
Collector Current (Pulse)	ICP	Pulse width Limited by Tjmax		80	Α
Diode Average Output Current	IO			20	Α
Allowable Power Dissipation	PD	Tc=25°C (Our ideal heat dissipation condition) *2		64	W

Note: \*1 Collector Current is calculated from the following formula.

 $I_{C}(Tc) = \frac{Tjmax - Tc}{R_{th}(j-c) \times V_{CE}(sat)(Tjmax, I_{C}(Tc))}$ 

\*2 Our condition is radiation from backside.

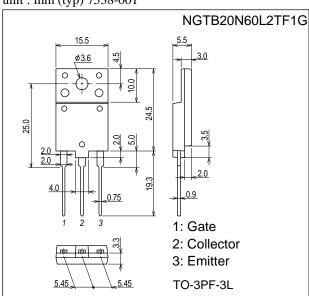
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The method is applying silicone grease to the backside of the device and attaching the device to water-cooled radiator made of aluminium.

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

#### **Package Dimensions**

unit: mm (typ) 7538-001



#### **Ordering & Package Information**

Device	Package	Shipping	note
NGTB20N60L2TF1G	TO-3PF-3L SC-94	30 pcs. / tube	Pb-Free

#### Marking

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# **Electrical Connection**

Continued from preceding page.

Parameter	Symbol	Conditions	Ratings	Unit
Junction Temperature	Tj		175	°C
Storage Temperature	Tstg		- 55 to +175	°C

# **Electrical Characteristics** at Ta = 25°C, Unless otherwise specified

Parameter	Symbol	Conditions	Ratings		Unit	
Parameter	Symbol	Conditions	min	typ	max	Unit
Collector to Emitter Breakdown Voltage	V(BR)CES	I <sub>C</sub> =500μA, V <sub>GE</sub> =0V	600			V
Collector to Emitter Cut off Current	ICES	V <sub>CE</sub> =600V, V <sub>GE</sub> =0V T <sub>c</sub> =25°C T <sub>c</sub> =150°C			10	μΑ
Collector to Emitter Cut on Current					1	mA
Gate to Emitter Leakage Current	IGES	V <sub>GE</sub> =±20V, V <sub>CE</sub> =0V			±100	nA
Gate to Emitter Threshold Voltage	V <sub>GE</sub> (th)	V <sub>CE</sub> =20V, I <sub>C</sub> =250μA	4.5		6.5	V
Collector to Emitter Seturation Valters		V <sub>GE</sub> =15V, I <sub>C</sub> =20A Tc=25°C		1.45	1.65	V
Collector to Emitter Saturation Voltage	VCE (sat)	Tc=150°C		1.8		V
Diode Forward Voltage	VF	IF=20A		1.5		V
Input Capacitance	Cies			2000		pF
Output Capacitance	Coes	V <sub>CE</sub> =20V,f=1MHz		60		pF
Reverse Transfer Capacitance	Cres			50		pF
Turn-ON Delay Time	t <sub>d</sub> (on)			60		ns
Rise Time	t <sub>r</sub>	V <sub>C</sub> C=300V,I <sub>C</sub> =20A		37		ns
Turn-ON Time	ton	R <sub>G</sub> =30Ω,L=200μH		400		ns
Turn-OFF Delay Time	t <sub>d</sub> (off)	V <sub>GE</sub> =0V/15V, Vclamp=400V See Fig.1, Fig.2		193		ns
Fall Time	tf			67		ns
Turn-OFF Time	toff			281		ns
Total Gate Charge	Qg	V <sub>CE</sub> =300V, V <sub>GE</sub> =15V, I <sub>C</sub> =20A		84		nC
Gate to Emitter Charge	Qge			16		nC
Gate to Collector "Miller" Charge	Qgc			37		nC
Diode Reverse Recovery Time	t <sub>rr</sub>	I <sub>F</sub> =10A , di/dt=100A/μs, V <sub>CC</sub> =50V See Fig.3		70	_	ns

# Thermal Characteristics at Ta = 25°C, Unless otherwise specified

Parameter	Symbol	Conditions	Ratings	Unit
Thermal Resistance IGBT (junction- case)	Rth(j-c)(IGBT)	Tc=25°C (our ideal heat dissipation condition)*2	2.33	°C /W
Thermal Resistance Diode (junction- case)	Rth(j-c)(Diode)	Tc=25°C (our ideal heat dissipation condition)*2	2.36	°C /W
Thermal Resistance (junction- atmosphere)	Rth(j-a)		47.5	°C /W

**Fig.1 Switching Time Test Circuit** 

Clamp Di
200µH
DUT
VCC

**Fig.2 Timing Chart** 

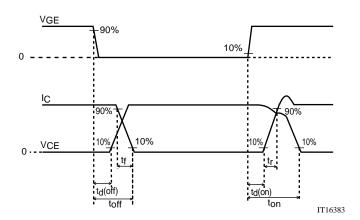
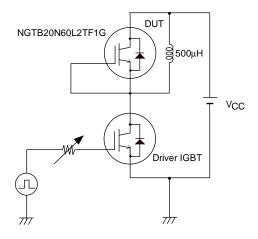
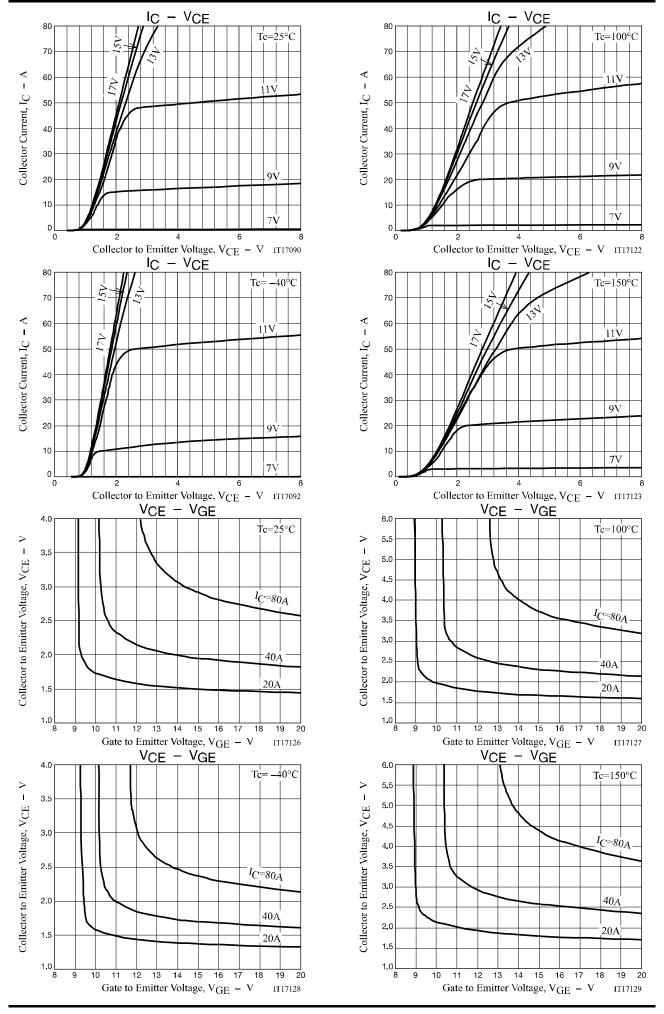
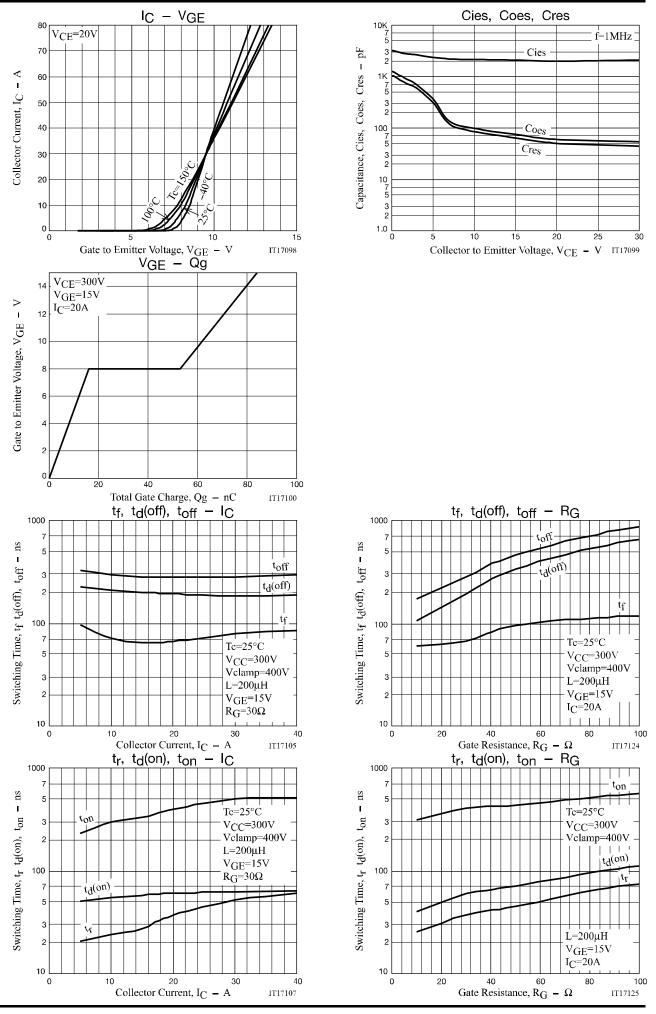
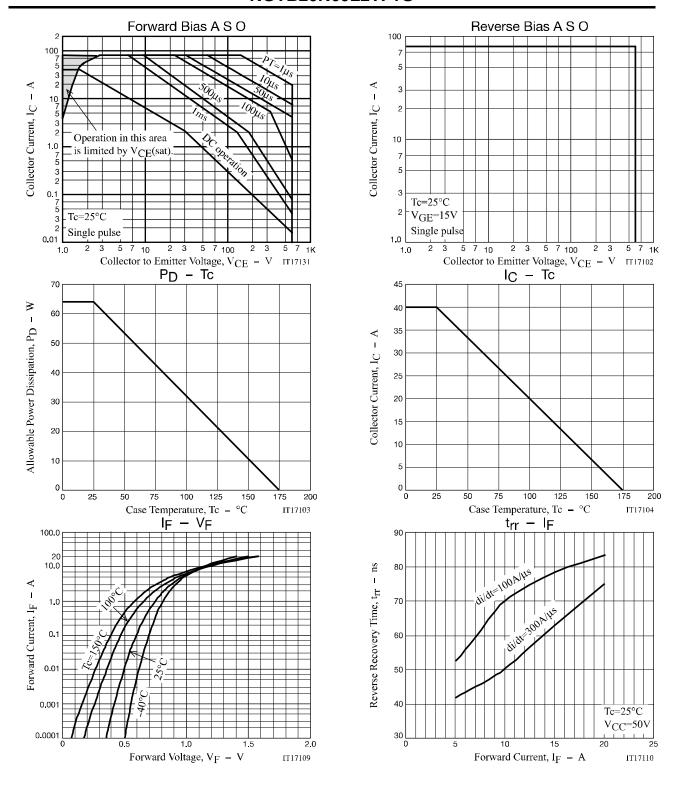


Fig.3 Reverse Recovery Time Test Circuit



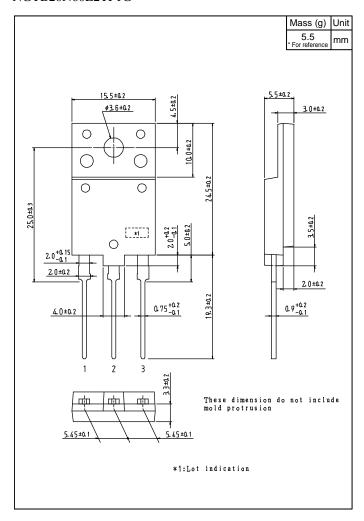






## **Outline Drawing**

NGTB20N60L2TF1G



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