Switch-mode Power Rectifier 60 V, 20 A

Features and Benefits

- Low Power Loss/High Efficiency
- High Surge Capacity
- 20 A Total (10 A Per Diode Leg)
- Guard-Ring for Stress Protection
- These Devices are Pb-Free and are RoHS Compliant*

Applications

- Power Supply Output Rectification
- Power Management
- Instrumentation

Mechanical Characteristics:

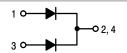
- Case: Epoxy, Molded
- Epoxy Meets UL 94 V-0 @ 0.125 in
- Weight: 1.9 Grams (Approximately)
- Finish: All External Surfaces Corrosion Resistant and Terminal Leads are Readily Solderable
- Lead Temperature for Soldering Purposes: 260°C Max. for 10 Seconds
- Shipped 50 Units Per Plastic Tube



ON Semiconductor®

www.onsemi.com

SCHOTTKY BARRIER RECTIFIER 20 AMPERES 60 VOLTS



TO-220

CASE 221A

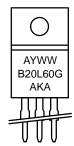
STYLE 6



MARKING DIAGRAM







A = Assembly Location

Y = Year
WW = Work Week
B20L60 = Device Code
G = Pb-Free Package
AKA = Polarity Designator

ORDERING INFORMATION

	Device	Package	Shipping
	MBR20L60CTG	TO-220 (Pb-Free)	50 Units / Rail
	MBRF20L60CTG	TO-220FP (Pb-Free)	50 Units / Rail

^{*}For additional information on our Pb–Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

MAXIMUM RATINGS (Per Diode Leg)

Rating	Symbol	Value	Unit
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage	V _{RRM} V _{RWM} V _R	60	V
Average Rectified Forward Current MBR20L60CT (Rated V_R) T_C = 138°C Per Diode MBRF20L60CT (Rated V_R) T_C = 123°C Per Device	I _{F(AV)}	10 20	А
Nonrepetitive Peak Surge Current (Surge applied at rated load conditions halfwave, single phase, 60 Hz)	I _{FSM}	240	A
Operating Junction Temperature (Note 1)	TJ	-55 to +150	°C
Storage Temperature	T _{stg}	- 65 to +175	°C
ESD Ratings: Machine Model = C Human Body Model = 3B		> 400 > 8000	V
Maximum Repetitive Peak Avalanche Voltage $(t_p < 1~\mu s, T_J < 150^{\circ}C, I_{AR} < 51~A)$	V _{ARM}	85	V
Maximum Single–Pulse Peak Avalanche Voltage $(t_p < 1~\mu s, T_J < 150^{\circ} C, I_{AR} < 51~A)$	V _{ASM}	85	V

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

THERMAL CHARACTERISTICS

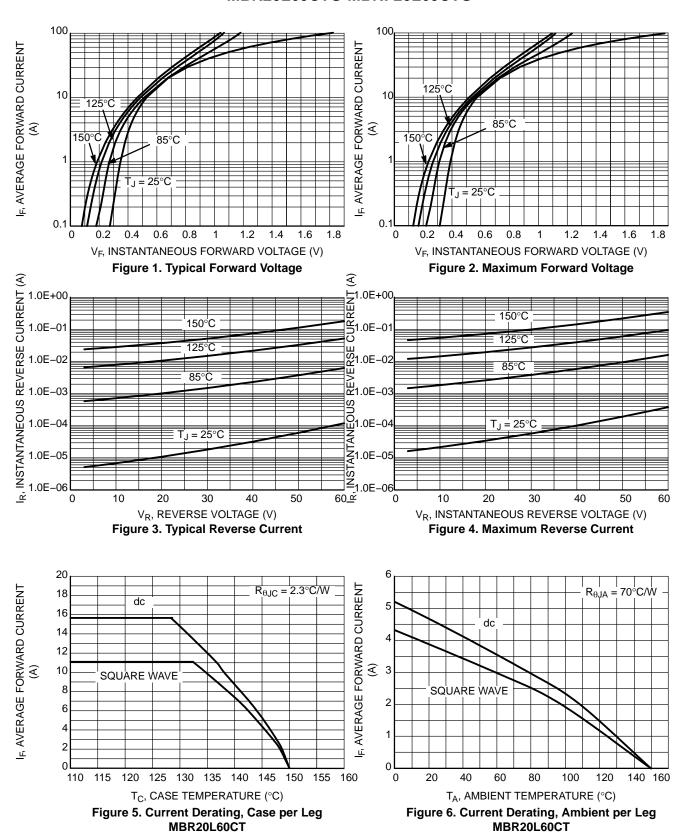
Characte	eristic	Symbol	Value	Unit
Maximum Thermal Resistance				°C/W
MBR20L60CTG	Junction-to-Case	$R_{\theta JC}$	2.3	
	Junction-to-Ambient	$R_{\theta JA}$	70	
MBRF20L60CTG	Junction-to-Case	$R_{ heta JC}$	5.2	
	Junction-to-Ambient	$R_{\theta JA}$	75	

ELECTRICAL CHARACTERISTICS (Per Diode Leg)

Characteristic	Symbol	Тур	Max	Unit
Maximum Instantaneous Forward Voltage (Note 2) $ \begin{aligned} &(I_F=10 \text{ A, } T_C=25^\circ\text{C}) \\ &(I_F=10 \text{ A, } T_C=125^\circ\text{C}) \\ &(I_F=20 \text{ A, } T_C=25^\circ\text{C}) \\ &(I_F=20 \text{ A, } T_C=125^\circ\text{C}) \end{aligned} $	VF	0.53 0.49 0.68 0.64	0.57 0.54 0.73 0.69	V
Maximum Instantaneous Reverse Current (Note 2) (Rated DC Voltage, $T_C = 25^{\circ}C$) (Rated DC Voltage, $T_C = 125^{\circ}C$)	İR	118 52	380 96	μA mA

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions. 2. Pulse Test: Pulse Width = 300 μ s, Duty Cycle \leq 2.0%.

^{1.} The heat generated must be less than the thermal conductivity from Junction–to–Ambient: $dP_D/dT_J < 1/R_{\theta JA}$.



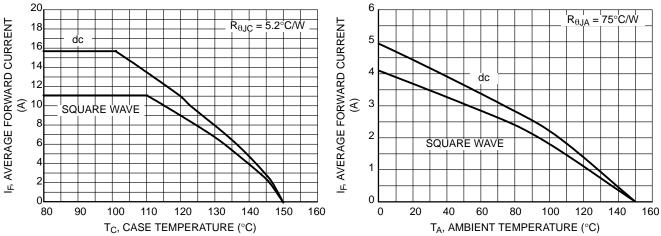


Figure 7. Current Derating, Case per Leg MBRF20L60CT

Figure 8. Current Derating, Ambient per Leg MBRF20L60CT

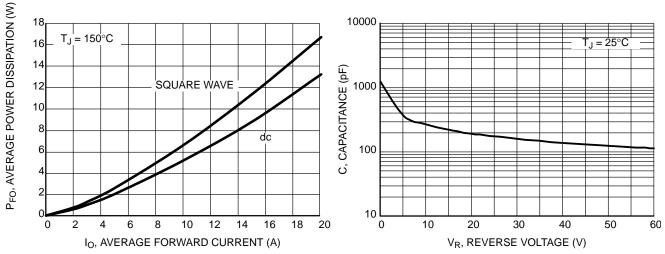


Figure 9. Forward Power Dissipation

Figure 10. Capacitance

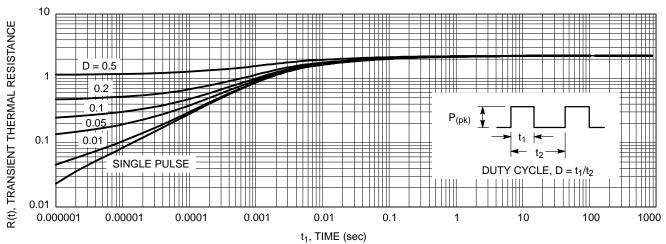


Figure 11. Thermal Response Junction-to-Case, per Leg for MBR20L60CT

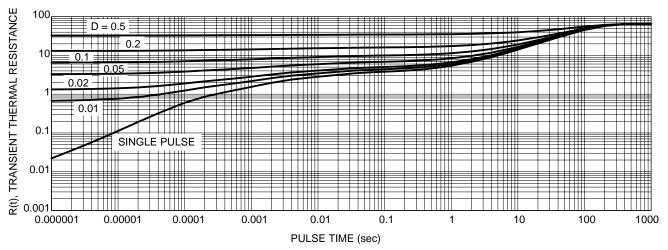


Figure 12. Thermal Response Junction-to-Ambient, per Leg for MBR20L60CT

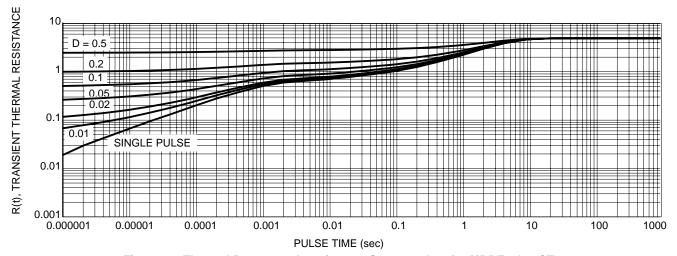


Figure 13. Thermal Response Junction-to-Case, per Leg for MBRF20L60CT

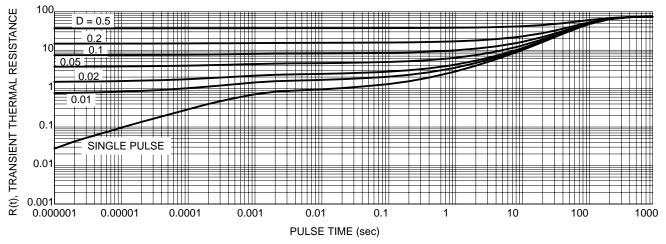
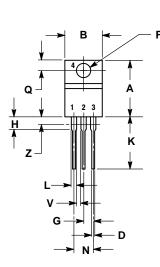
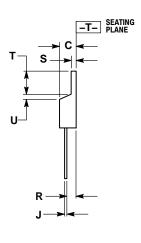


Figure 14. Thermal Response Junction-to-Ambient, per Leg for MBRF20L60CT

PACKAGE DIMENSIONS

TO-220 CASE 221A-09 **ISSUE AH**





- NOTES:
 1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
 2. CONTROLLING DIMENSION: INCH.
 3. DIMENSION Z DEFINES A ZONE WHERE ALL BODY AND LEAD IRREGULARITIES ARE ALLOWED.

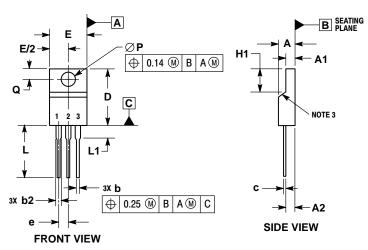
	INCHES		MILLIMETERS	
DIM	MIN	MAX	MIN	MAX
Α	0.570	0.620	14.48	15.75
В	0.380	0.415	9.66	10.53
С	0.160	0.190	4.07	4.83
D	0.025	0.038	0.64	0.96
F	0.142	0.161	3.61	4.09
G	0.095	0.105	2.42	2.66
Н	0.110	0.161	2.80	4.10
J	0.014	0.024	0.36	0.61
K	0.500	0.562	12.70	14.27
L	0.045	0.060	1.15	1.52
N	0.190	0.210	4.83	5.33
Q	0.100	0.120	2.54	3.04
R	0.080	0.110	2.04	2.79
S	0.045	0.055	1.15	1.39
T	0.235	0.255	5.97	6.47
U	0.000	0.050	0.00	1.27
٧	0.045		1.15	
Z		0.080		2.04

- STYLE 6:
 PIN 1. ANODE
 2. CATHODE
 3. ANODE
 4. CATHODE

PACKAGE DIMENSIONS

TO-220 FULLPACK, 3-LEAD

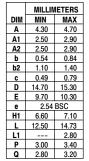
CASE 221AH ISSUE F



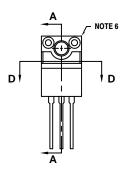
NOTES:

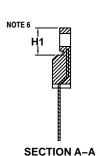
- DIMENSIONING AND TOLERANCING PER ASME
- Y14.5M, 1994.
- 2. CONTROLLING DIMENSION: MILLIMETERS.
 3. CONTOUR UNCONTROLLED IN THIS AREA.
- S. OMMOGO ON TO ELD MITHS AND E DO NOT INCLUDE MOLD FLASH AND GATE PROTRUSIONS. MOLD FLASH AND GATE PROTRUSIONS NOT TO EXCEED 0.13 PER SIDE. THESE DIMENSIONS ARE TO BE MEASURED AT OUTERMOST EXTREME OF THE PLASTIC BODY.

 S. DIMENSION b2 DOES NOT INCLUDE DAMBAR PROTRUSION.
- DIMENSION b2 DOES NOT INCLUDE DAMBAR PROTRUSION.
 LEAD WIDTH INCLUDING PROTRUSION SHALL NOT EXCEED 2.00.
 CONTOURS AND FEATURES OF THE MOLDED PACKAGE BODY
- CONTOURS AND FEATURES OF THE MOLDED PACKAGE BODY MAY VARY WITHIN THE ENVELOP DEFINED BY DIMENSIONS A1 AND H1 FOR MANUFACTURING PURPOSES.









ALTERNATE CONSTRUCTION

FULLPAK is a trademark of Semiconductor Components Industries, LLC.

ON Semiconductor and the limited States and/or other countries. SCILLC owns the rights to a number of patents, trademarks, copyrights, trade secrets, and other intellectual property. A listing of SCILLC's product/patent coverage may be accessed at www.onsemi.com/site/pdf/Patent-Marking.pdf. SCILLC reserves the right to make changes without further notice to any products herein. SCILLC makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does SCILLC assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. "Typical" parameters which may be provided in SCILLC data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. SCILLC does not convey any license under its patent rights nor the rights of others. SCILLC products are not designed, intended, or authorized for use as components in systems intended for surgical implant into the body, or other applications intended to support or sustain life, or for any other application in which the failure of the SCILLC product could create a situation where personal injury or death may occur. Should Buyer purchase or use SCILLC products for any such unintended or unauthorized application, Buyer shall indemnify and hold SCILLC and its officers, employees, subsidiaries, affliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that SCILLC was negligent regarding the design or manufacture of the part. SCILLC is an Equal Opportunity/Affirmative Action Employer. This li

PUBLICATION ORDERING INFORMATION

LITERATURE FULFILLMENT:

Literature Distribution Center for ON Semiconductor
P.O. Box 5163, Denver, Colorado 80217 USA
Phone: 303–675–2175 or 800–344–3860 Toll Free USA/Canada

Fax: 303-675-2173 or 600-344-3660 foll Free USA/Canada Fax: 303-675-2176 or 800-344-3867 Toll Free USA/Canada Email: orderlit@onsemi.com N. American Technical Support: 800-282-9855 Toll Free USA/Canada

Europe, Middle East and Africa Technical Support: Phone: 421 33 790 2910

Phone: 421 33 790 2910 Japan Customer Focus Center Phone: 81–3–5817–1050 ON Semiconductor Website: www.onsemi.com

Order Literature: http://www.onsemi.com/orderlit

For additional information, please contact your local Sales Representative

Mouser Electronics

Authorized Distributor

Click to View Pricing, Inventory, Delivery & Lifecycle Information:

ON Semiconductor: