

# MBR30H100CTG, MBRF30H100CTG

## Switch-mode Power Rectifier 100 V, 30 A

### Features and Benefits

- Low Forward Voltage: 0.67 V @ 125°C
- Low Power Loss/High Efficiency
- High Surge Capacity
- 175°C Operating Junction Temperature
- 30 A Total (15 A Per Diode Leg)
- These are Pb-Free Devices

### Applications

- Power Supply – Output Rectification
- Power Management
- Instrumentation

### Mechanical Characteristics:

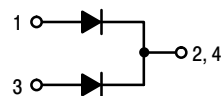
- Case: Epoxy, Molded
- Epoxy Meets UL 94 V-0 @ 0.125 in
- Weight: 1.9 Grams (Approximately)
- Finish: All External Surfaces Corrosion Resistant and Terminal Leads are Readily Solderable
- Lead Temperature for Soldering Purposes:  
260°C Max. for 10 Seconds
- ESD Rating: Human Body Model = 3B  
Machine Model = C



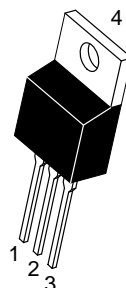
ON Semiconductor®

[www.onsemi.com](http://www.onsemi.com)

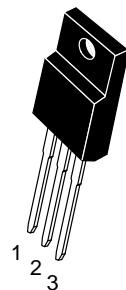
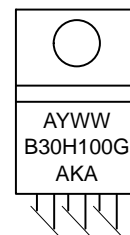
## SCHOTTKY BARRIER RECTIFIER 30 AMPERES 100 VOLTS



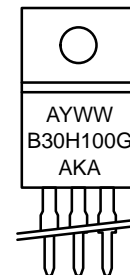
### MARKING DIAGRAMS



TO-220  
CASE 221A  
STYLE 6



TO-220 FULLPAK  
CASE 221AH



A = Assembly Location  
Y = Year  
WW = Work Week  
B30H100 = Device Code  
G = Pb-Free Package  
AKA = Polarity Designator

### ORDERING INFORMATION

See detailed ordering and shipping information in the package dimensions section on page 2 of this data sheet.

# MBR30H100CTG, MBRF30H100CTG

## MAXIMUM RATINGS (Per Diode Leg)

Rating	Symbol	Value	Unit
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage	$V_{RRM}$ $V_{RWM}$ $V_R$	100	V
Average Rectified Forward Current ( $T_C = 156^\circ\text{C}$ ) Per Diode Per Device	$I_{F(AV)}$	15 30	A
Peak Repetitive Forward Current (Square Wave, 20 kHz, $T_C = 151^\circ\text{C}$ )	$I_{FM}$	30	A
Nonrepetitive Peak Surge Current (Surge applied at rated load conditions halfwave, single phase, 60 Hz)	$I_{FSM}$	250	A
Operating Junction Temperature (Note 1)	$T_J$	+175	$^\circ\text{C}$
Storage Temperature	$T_{stg}$	-65 to +175	$^\circ\text{C}$
Voltage Rate of Change (Rated $V_R$ )	dv/dt	10,000	V/ $\mu\text{s}$
Controlled Avalanche Energy (see test conditions in Figures 13 and 14)	$W_{AVAIL}$	200	mJ
ESD Ratings: Machine Model = C Human Body Model = 3B		> 400 > 8000	V

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

1. The heat generated must be less than the thermal conductivity from Junction-to-Ambient:  $dP_D/dT_J < 1/R_{\theta JA}$ .

## THERMAL CHARACTERISTICS

Characteristic	Symbol	Value	Unit
Maximum Thermal Resistance (MBR30H100CTG) – Junction-to-Case – Junction-to-Ambient (MBRF30H100CTG) – Junction-to-Case – Junction-to-Ambient	$R_{\theta JC}$ $R_{\theta JA}$ $R_{\theta JC}$ $R_{\theta JA}$	2.0 60 4.2 75	$^\circ\text{C/W}$

## ELECTRICAL CHARACTERISTICS (Per Diode Leg)

Characteristic	Symbol	Min	Typ	Max	Unit
Maximum Instantaneous Forward Voltage (Note 2) ( $i_F = 15\text{ A}$ , $T_J = 25^\circ\text{C}$ ) ( $i_F = 15\text{ A}$ , $T_J = 125^\circ\text{C}$ ) ( $i_F = 30\text{ A}$ , $T_J = 25^\circ\text{C}$ ) ( $i_F = 30\text{ A}$ , $T_J = 125^\circ\text{C}$ )	$V_F$	– – – –	0.76 0.64 0.88 0.76	0.80 0.67 0.93 0.80	V
Maximum Instantaneous Reverse Current (Note 2) (Rated DC Voltage, $T_J = 125^\circ\text{C}$ ) (Rated DC Voltage, $T_J = 25^\circ\text{C}$ )	$i_R$	– –	1.1 0.0008	6.0 0.0045	mA

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

2. Pulse Test: Pulse Width = 300  $\mu\text{s}$ , Duty Cycle  $\leq 2.0\%$ .

## ORDERING INFORMATION

Device Order Number	Package Type	Shipping <sup>†</sup>
MBR30H100CTG	TO-220 (Pb-Free)	50 Units / Rail
MBRF30H100CTG	TO-220FP (Pb-Free)	50 Units / Rail

# MBR30H100CTG, MBRF30H100CTG

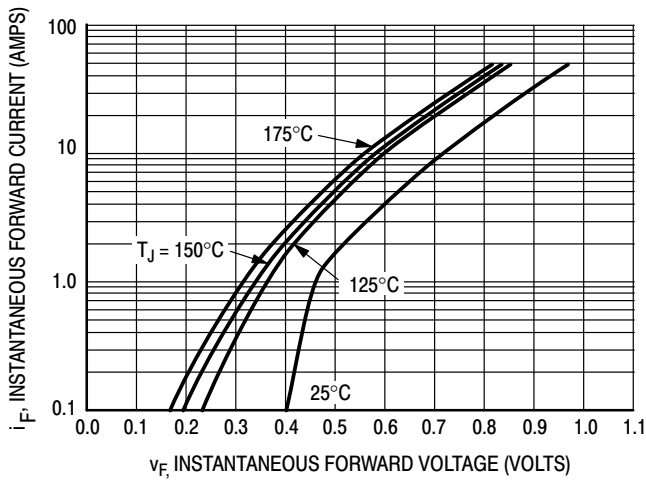


Figure 1. Typical Forward Voltage

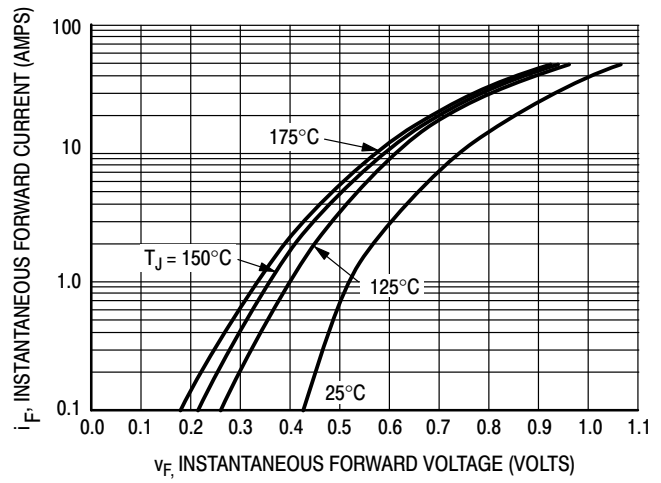


Figure 2. Maximum Forward Voltage

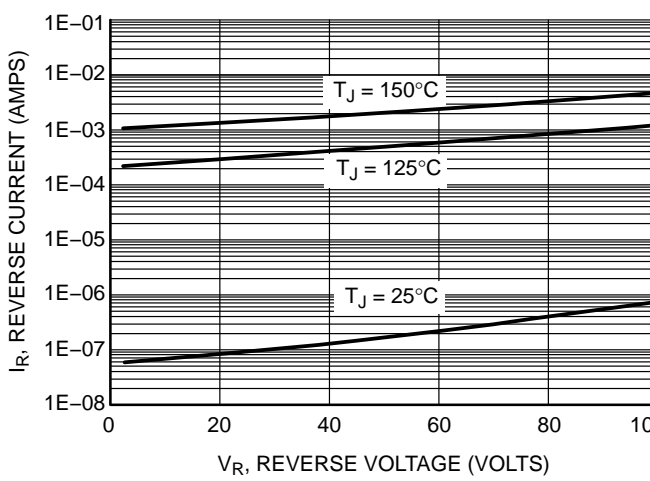


Figure 3. Typical Reverse Current

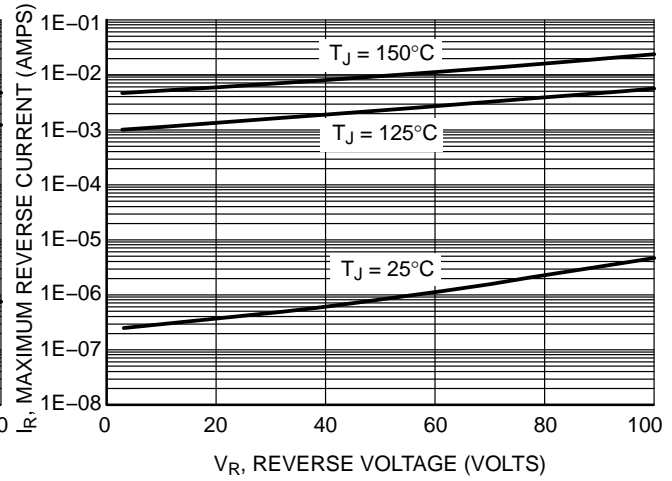


Figure 4. Maximum Reverse Current

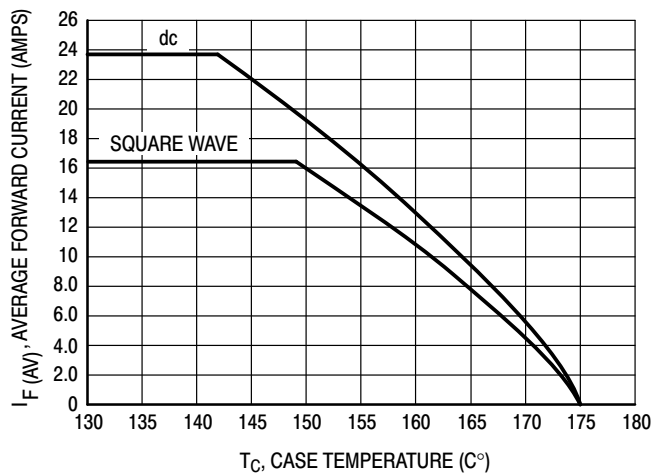


Figure 5. Current Derating, Case Per Leg

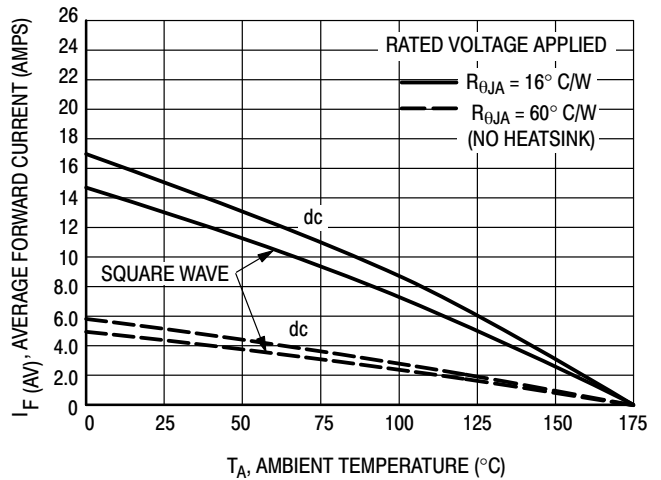


Figure 6. Current Derating, Ambient Per Leg

# MBR30H100CTG, MBRF30H100CTG

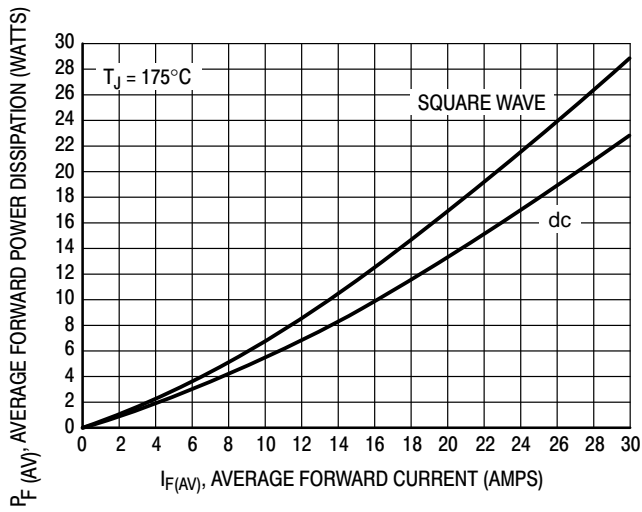


Figure 7. Forward Power Dissipation

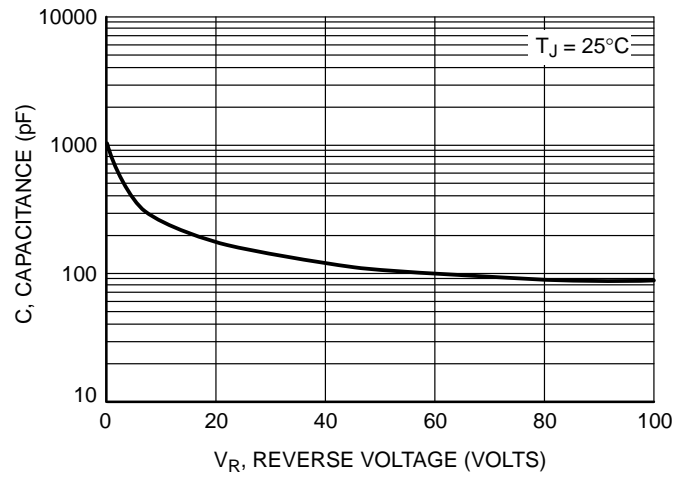


Figure 8. Capacitance

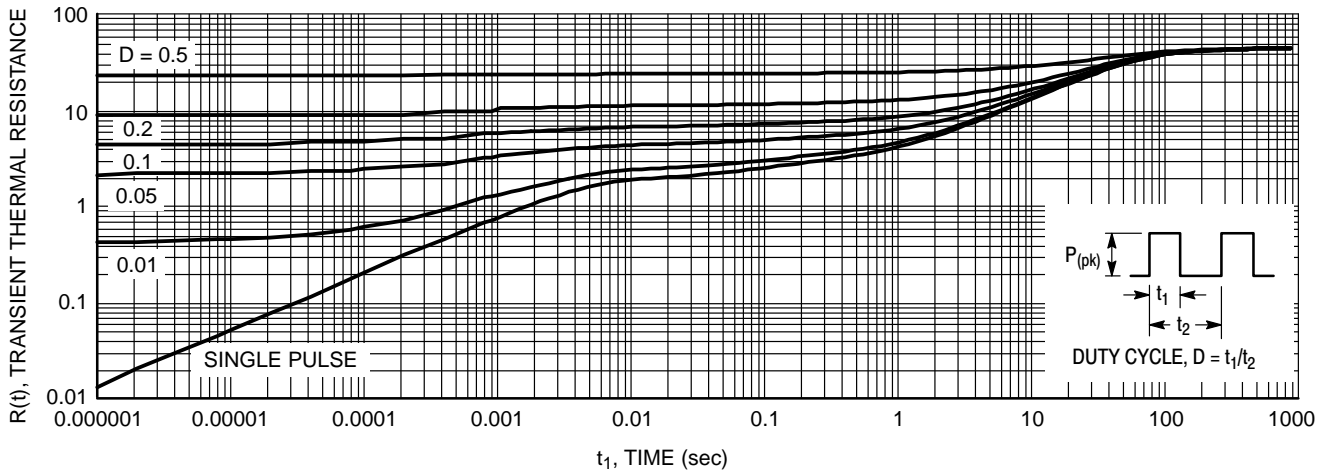


Figure 9. Thermal Response Junction-to-Ambient for MBR30H100CT

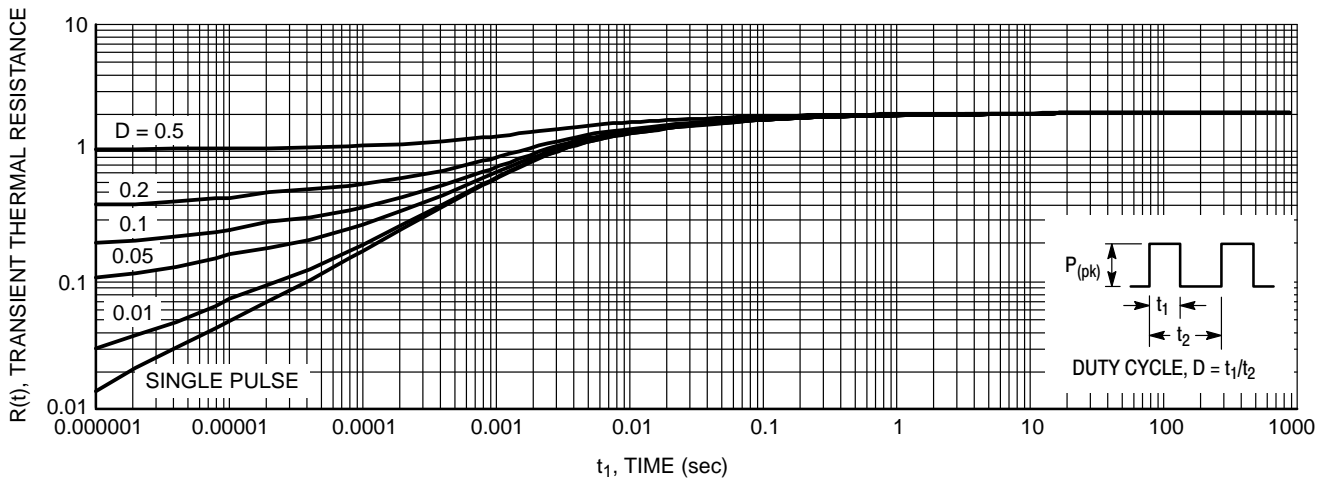


Figure 10. Thermal Response Junction-to-Case for MBR30H100CT

# MBR30H100CTG, MBRF30H100CTG

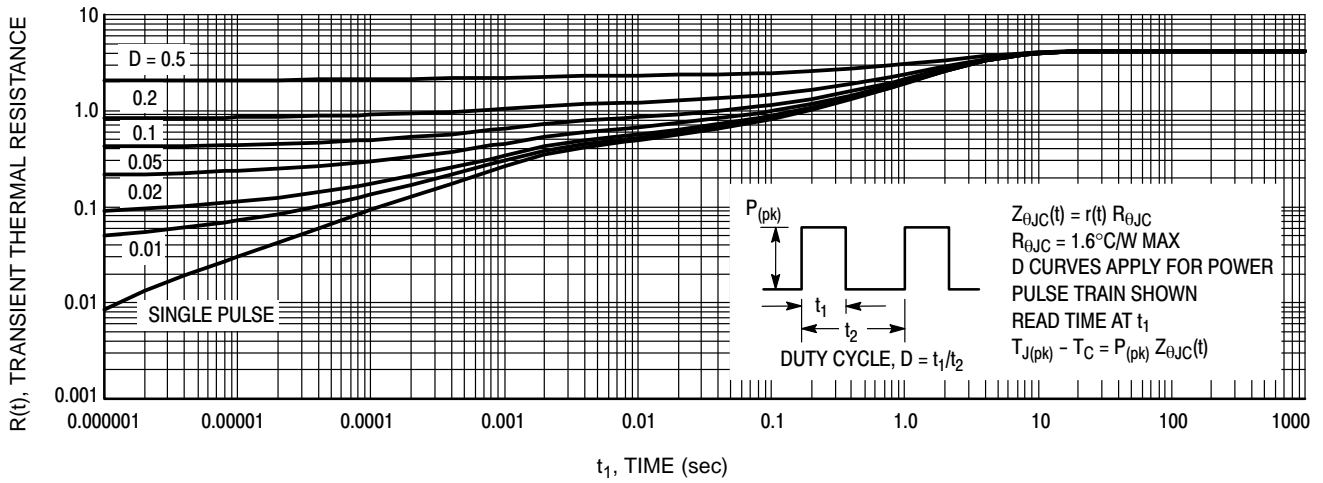


Figure 11. Thermal Response Junction-to-Case for MBRF30H100CT

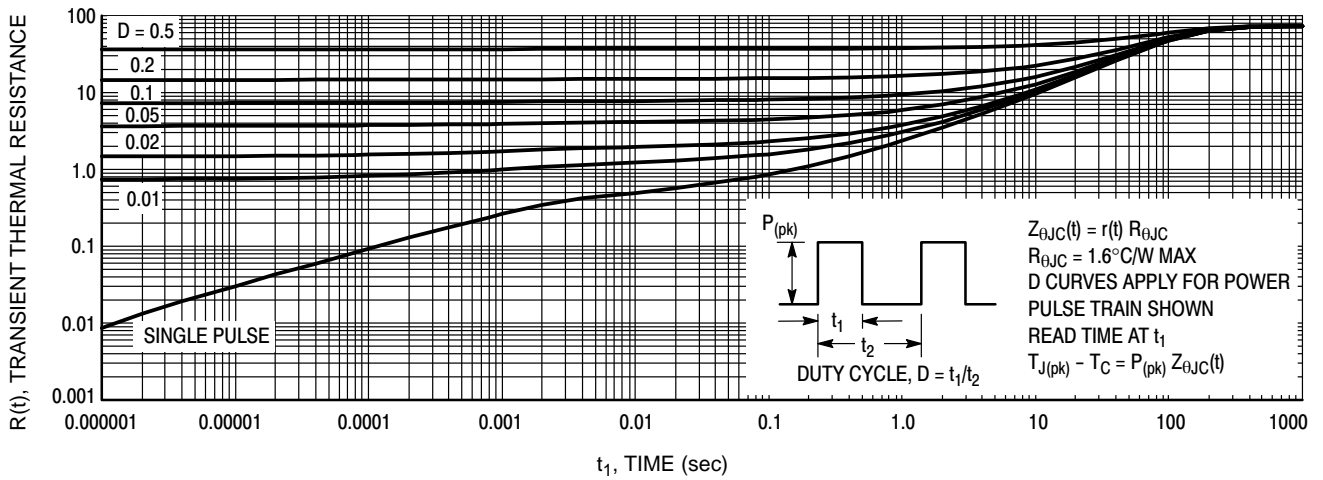


Figure 12. Thermal Response Junction-to-Ambient for MBRF30H100CT

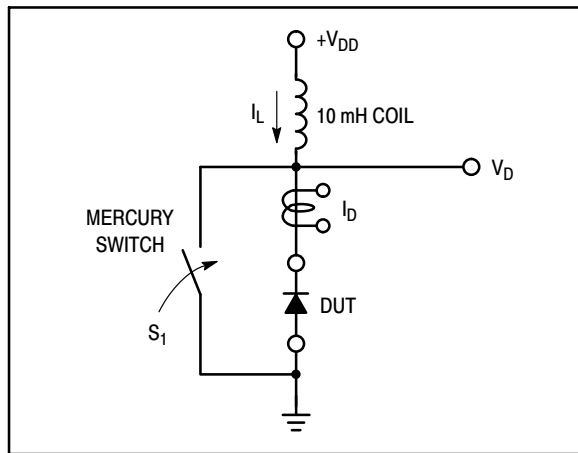


Figure 13. Test Circuit

The unclamped inductive switching circuit shown in Figure 13 was used to demonstrate the controlled avalanche capability of this device. A mercury switch was used instead of an electronic switch to simulate a noisy environment when the switch was being opened.

When  $S_1$  is closed at  $t_0$  the current in the inductor  $I_L$  ramps up linearly; and energy is stored in the coil. At  $t_1$  the switch is opened and the voltage across the diode under test begins to rise rapidly, due to  $di/dt$  effects, when this induced voltage reaches the breakdown voltage of the diode, it is clamped at  $BV_{DUT}$  and the diode begins to conduct the full load current which now starts to decay linearly through the diode, and goes to zero at  $t_2$ .

By solving the loop equation at the point in time when  $S_1$  is opened; and calculating the energy that is transferred to the diode it can be shown that the total energy transferred is equal to the energy stored in the inductor plus a finite amount of energy from the  $V_{DD}$  power supply while the diode is in breakdown (from  $t_1$  to  $t_2$ ) minus any losses due to finite component resistances. Assuming the component resistive

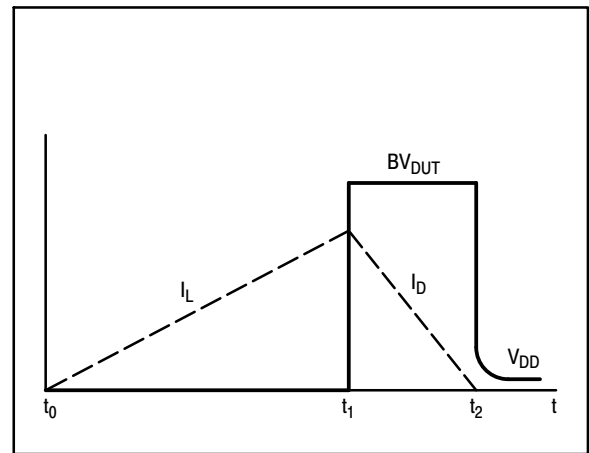


Figure 14. Current-Voltage Waveforms

elements are small Equation (1) approximates the total energy transferred to the diode. It can be seen from this equation that if the  $V_{DD}$  voltage is low compared to the breakdown voltage of the device, the amount of energy contributed by the supply during breakdown is small and the total energy can be assumed to be nearly equal to the energy stored in the coil during the time when  $S_1$  was closed, Equation (2).

**EQUATION (1):**

$$W_{AVAL} \approx \frac{1}{2} L I_{LPK}^2 \left( \frac{BV_{DUT}}{BV_{DUT} + V_{DD}} \right)$$

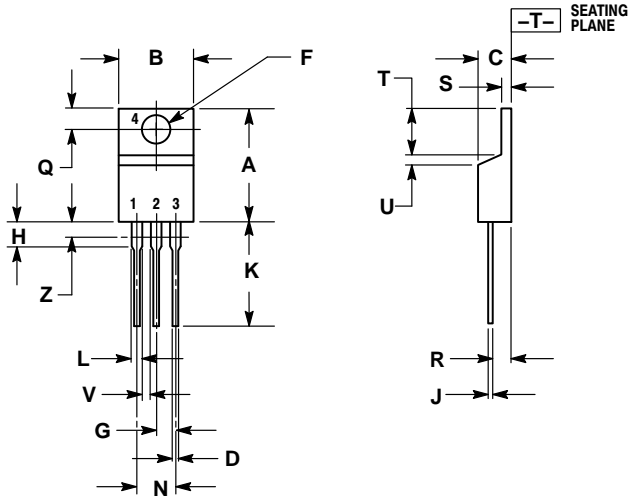
**EQUATION (2):**

$$W_{AVAL} \approx \frac{1}{2} L I_{LPK}^2$$

# MBR30H100CTG, MBRF30H100CTG

## PACKAGE DIMENSIONS

TO-220  
CASE 221A-09  
ISSUE AH



- NOTES:
1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
  2. CONTROLLING DIMENSION: INCH.
  3. DIMENSION Z DEFINES A ZONE WHERE ALL BODY AND LEAD IRREGULARITIES ARE ALLOWED.

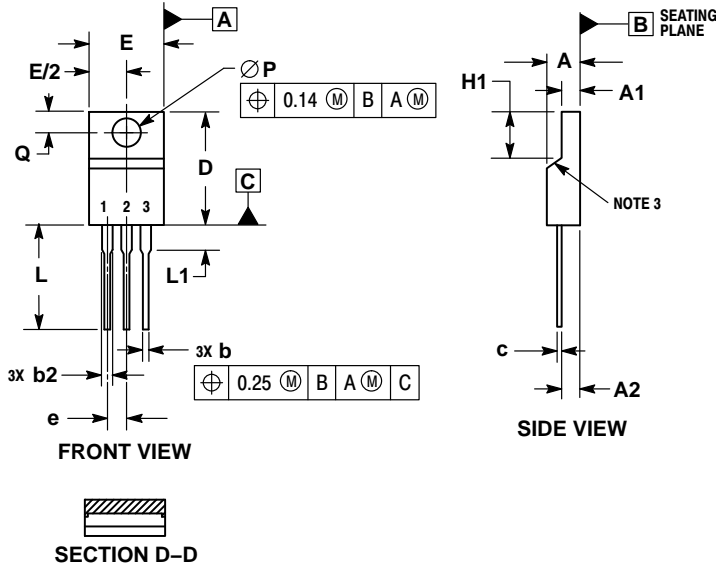
DIM	INCHES		MILLIMETERS	
	MIN	MAX	MIN	MAX
A	0.570	0.620	14.48	15.75
B	0.380	0.415	9.66	10.53
C	0.160	0.190	4.07	4.83
D	0.025	0.038	0.64	0.96
F	0.142	0.161	3.61	4.09
G	0.095	0.105	2.42	2.66
H	0.110	0.161	2.80	4.10
J	0.014	0.024	0.36	0.61
K	0.500	0.562	12.70	14.27
L	0.045	0.060	1.15	1.52
N	0.190	0.210	4.83	5.33
Q	0.100	0.120	2.54	3.04
R	0.080	0.110	2.04	2.79
S	0.045	0.055	1.15	1.39
T	0.235	0.255	5.97	6.47
U	0.000	0.050	0.00	1.27
V	0.045	---	1.15	---
Z	---	0.080	---	2.04

- STYLE 6:
- PIN 1. ANODE
  - CATHODE
  - ANODE
  - CATHODE

# MBR30H100CTG, MBRF30H100CTG

## PACKAGE DIMENSIONS

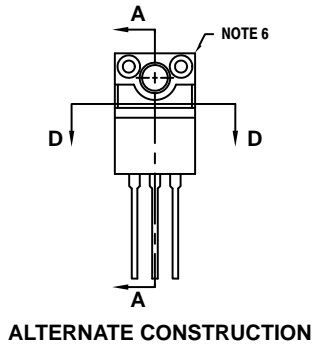
### TO-220 FULLPACK, 3-LEAD CASE 221AH ISSUE F



#### NOTES:


1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.
2. CONTROLLING DIMENSION: MILLIMETERS.
3. CONTOUR UNCONTROLLED IN THIS AREA.
4. DIMENSIONS D AND E DO NOT INCLUDE MOLD FLASH AND GATE PROTRUSIONS. MOLD FLASH AND GATE PROTRUSIONS NOT TO EXCEED 0.13 PER SIDE. THESE DIMENSIONS ARE TO BE MEASURED AT OUTERMOST EXTREME OF THE PLASTIC BODY.
5. DIMENSION b2 DOES NOT INCLUDE DAMBAR PROTRUSION. LEAD WIDTH INCLUDING PROTRUSION SHALL NOT EXCEED 2.00.
6. CONTOURS AND FEATURES OF THE MOLDED PACKAGE BODY MAY VARY WITHIN THE ENVELOPE DEFINED BY DIMENSIONS A1 AND H1 FOR MANUFACTURING PURPOSES.

SECTION D-D



ALTERNATE CONSTRUCTION

SECTION A-A

ON Semiconductor and the  are registered trademarks of Semiconductor Components Industries, LLC (SCILLC) or its subsidiaries in the United States and/or other countries. SCILLC owns the rights to a number of patents, trademarks, copyrights, trade secrets, and other intellectual property. A listing of SCILLC's product/patent coverage may be accessed at [www.onsemi.com/site/pdf/Patent-Marketing.pdf](http://www.onsemi.com/site/pdf/Patent-Marketing.pdf). SCILLC reserves the right to make changes without further notice to any products herein. SCILLC makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does SCILLC assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. "Typical" parameters which may be provided in SCILLC data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. SCILLC does not convey any license under its patent rights nor the rights of others. SCILLC products are not designed, intended, or authorized for use as components in systems intended for surgical implant into the body, or other applications intended to support or sustain life, or for any other application in which the failure of the SCILLC product could create a situation where personal injury or death may occur. Should Buyer purchase or use SCILLC products for any such unintended or unauthorized application, Buyer shall indemnify and hold SCILLC and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that SCILLC was negligent regarding the design or manufacture of the part. SCILLC is an Equal Opportunity/Affirmative Action Employer. This literature is subject to all applicable copyright laws and is not for resale in any manner.

#### PUBLICATION ORDERING INFORMATION

##### LITERATURE FULFILLMENT:

Literature Distribution Center for ON Semiconductor  
P.O. Box 5163, Denver, Colorado 80217 USA  
Phone: 303-675-2175 or 800-344-3860 Toll Free USA/Canada  
Fax: 303-675-2176 or 800-344-3867 Toll Free USA/Canada  
Email: [orderlit@onsemi.com](mailto:orderlit@onsemi.com)

N. American Technical Support: 800-282-9855 Toll Free  
USA/Canada  
Europe, Middle East and Africa Technical Support:  
Phone: 421 33 790 2910  
Japan Customer Focus Center  
Phone: 81-3-5817-1050

ON Semiconductor Website: [www.onsemi.com](http://www.onsemi.com)

Order Literature: <http://www.onsemi.com/orderlit>

For additional information, please contact your local Sales Representative



# Mouser Electronics

Authorized Distributor

Click to View Pricing, Inventory, Delivery & Lifecycle Information:

[ON Semiconductor:](#)

[MBRF30H100CTG](#) [MBR30H100CTG](#)