Surface Mount Schottky Power Rectifier

POWERMITE[®] Power Surface Mount Package

The Schottky Powermite[®] employs the Schottky Barrier principle with a barrier metal and epitaxial construction that produces optimal forward voltage drop-reverse current tradeoff. The advanced packaging techniques provide for a highly efficient micro miniature, space saving surface mount Rectifier. With its unique heatsink design, the Powermite[®] has the same thermal performance as the SMA while being 50% smaller in footprint area. Because of its small size, it is ideal for use in portable and battery powered products such as cellular and cordless phones, chargers, notebook computers, printers, PDAs and PCMCIA cards. Typical applications are AC-DC and DC-DC converters, reverse battery protection, and "ORing" of multiple supply voltages and any other application where performance and size are critical.

Features

- Low Profile Maximum Height of 1.1 mm
- Small Footprint Footprint Area of 8.45 mm²
- Low V_F Provides Higher Efficiency and Extends Battery Life
- Supplied in 12 mm Tape and Reel
- Low Thermal Resistance with Direct Thermal Path of Die on Exposed Cathode Heat Sink
- This is a Pb–Free Device

Mechanical Characteristics:

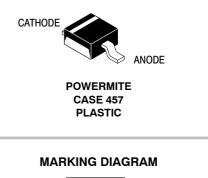
- Powermite[®] is JEDEC Registered as D0-216AA
- Case: Molded Epoxy
- Epoxy Meets UL 94 V-0 @ 0.125 in
- Weight: 16.3 mg (Approximately)
- Lead and Mounting Surface Temperature for Soldering Purposes: 260°C Maximum for 10 Seconds

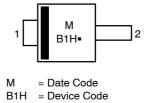


ON Semiconductor®

http://onsemi.com

SCHOTTKY BARRIER RECTIFIER 1.0 AMPERES, 100 VOLTS





= Pb-Free Package

ORDERING INFORMATION

Device	Package	Shipping [†]
MBRM1H100T3G	Powermite (Pb-Free)	12000/Tape & Reel

+ For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

MAXIMUM RATINGS

Rating		Value	Unit
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage	V _{RRM} V _{RWM} V _R	100	V
Average Rectified Forward Current $(T_L = 168^{\circ}C)$	Ι _Ο	1.0	A
Non-Repetitive Peak Surge Current (Surge Applied at Rated Load Conditions Halfwave, Single Phase, 60 Hz)	I _{FSM}	50	A
Storage and Operating Junction Temperature Range (Note 1)	T _{stg} , T _J	-65 to +175	°C

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

1. The heat generated must be less than the thermal conductivity from

Junction–to–Ambient: $dP_D/dT_J < 1/R_{\theta JA}$.

THERMAL CHARACTERISTICS

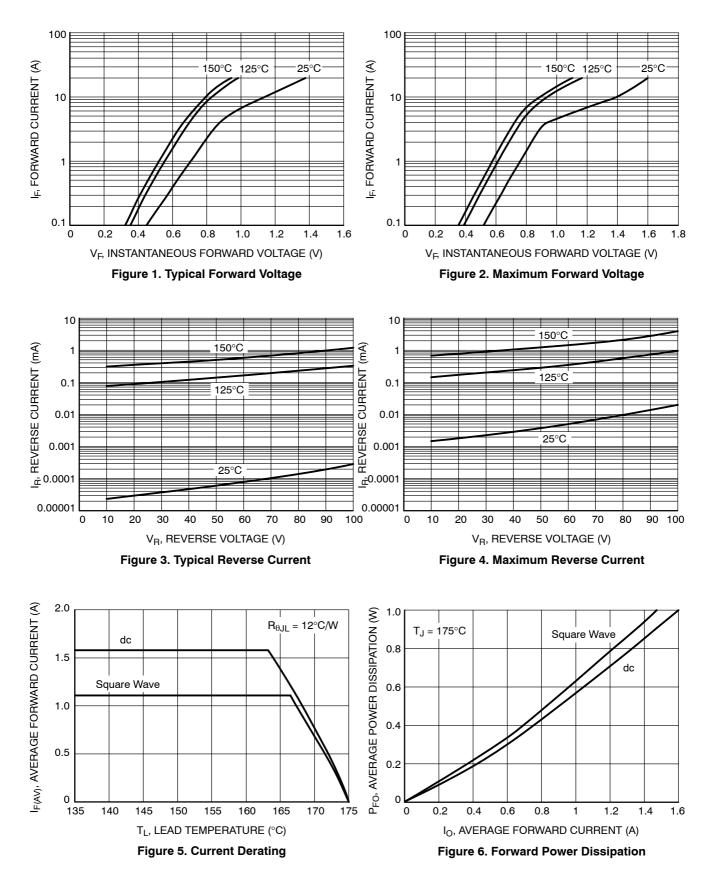
Characteristic		Value	Unit
Thermal Resistance, Junction-to-Lead (Note 2)	Ψ_{JCL}	12	°C/W
Thermal Resistance, Junction-to-Ambient (Note 2)	R _{0JA}	75	°C/W
Thermal Resistance, Junction-to-Ambient (Note 3)	R _{θJA}	260	°C/W

ELECTRICAL CHARACTERISTICS

Characteristic	Symbol	Value	Unit
	V _F	0.76 0.84 0.61 0.68	V
Maximum Instantaneous Reverse Current (Note 4) (Rated dc Voltage, $T_J = 25^{\circ}C$) (Rated dc Voltage, $T_J = 125^{\circ}C$)	I _R	20 1.0	μA mA

2. Mounted with 700 mm² copper pad size (Approximately 1 in²) 1 oz FR4 Board. 3. Mounted with pad size approximately 20 mm² copper, 1 oz FR4 Board. 4. Pulse Test: Pulse Width \leq 380 µs, Duty Cycle \leq 2.0%.

TYPICAL CHARACTERISTICS



TYPICAL CHARACTERISTICS

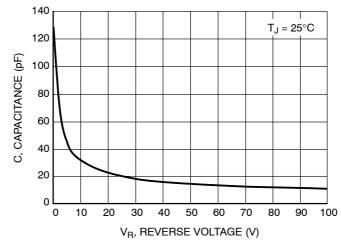
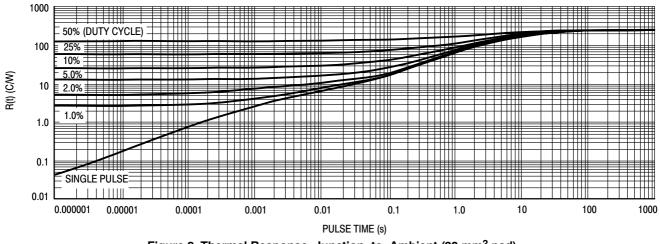
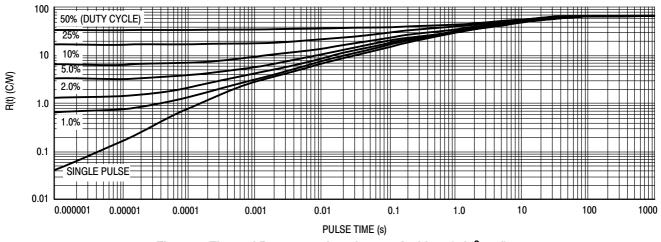
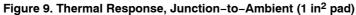


Figure 7. Capacitance





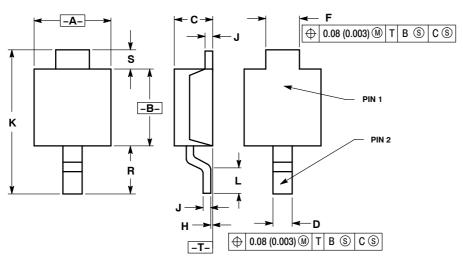




PACKAGE DIMENSIONS



CASE 457-04 ISSUE F



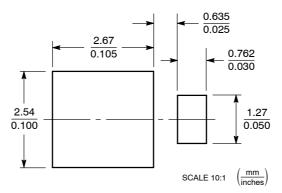
NOTES:

 DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.

 CONTROLLING DIMENSION: MILLIMETER.
DIMENSION A DOES NOT INCLUDE MOLD FLASH, PROTRUSIONS OR GATE BURRS. MOLD FLASH, PROTRUSIONS OR GATE BURRS SHALL NOT EXCEED 0.15 (0.006) PER SIDE.

	MILLIMETERS		INCHES	
DIM	MIN	MAX	MIN	MAX
Α	1.75	2.05	0.069	0.081
В	1.75	2.18	0.069	0.086
C	0.85	1.15	0.033	0.045
D	0.40	0.69	0.016	0.027
F	0.70	1.00	0.028	0.039
Н	-0.05	+0.10	-0.002	+0.004
J	0.10	0.25	0.004	0.010
K	3.60	3.90	0.142	0.154
L	0.50	0.80	0.020	0.031
R	1.20	1.50	0.047	0.059
S	0.50 REF		0.019 REF	

SOLDERING FOOTPRINT*



*For additional information on our Pb–Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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