# **Amplifier Transistors**

## **NPN Silicon**

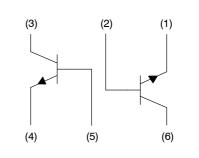
### Features

• These Devices are Pb–Free, Halogen Free/BFR Free and are RoHS Compliant



## **ON Semiconductor®**

http://onsemi.com



## Rating

**MAXIMUM RATINGS** 

Collector - Emitter Voltage	V <sub>CEO</sub>	45	Vdc
Collector - Base Voltage	V <sub>CBO</sub>	55	Vdc
Emitter – Base Voltage	V <sub>EBO</sub>	6.0	Vdc
Collector Current – Continuous	Ι <sub>C</sub>	200	mAdc

Symbol

Value

Unit

THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Total Device Dissipation (Note 1) T <sub>A</sub> = 25°C	P <sub>D</sub>	150	mW
Thermal Resistance, Junction-to-Ambient	$R_{\theta JA}$	833	°C/W
Junction and Storage Temperature	T <sub>J</sub> , T <sub>stg</sub>	-55 to +150	°C

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

1. Device mounted on FR4 glass epoxy printed circuit board using the minimum recommended foot print.



### MARKING DIAGRAM



= Specific Device Code

= Date Code

1T

Μ

= Pb-Free Package

(Note: Microdot may be in either location)

### **ORDERING INFORMATION**

Device	Device Package	
MBT6429DW1T1G	SC-88 (Pb-Free)	3000 / Tape & Reel

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

## **ELECTRICAL CHARACTERISTICS** (T<sub>A</sub> = 25°C unless otherwise noted)

Characteristic	Symbol	Min	Max	Unit
OFF CHARACTERISTICS		-		-
Collector – Emitter Breakdown Voltage $(I_{C} = 1.0 \text{ mAdc}, I_{B} = 0)$	V <sub>(BR)CEO</sub>	45	-	Vdc
Collector – Base Breakdown Voltage $(I_{C} = 0.1 \text{ mAdc}, I_{E} = 0)$	V <sub>(BR)CBO</sub>	55	_	Vdc
Collector Cutoff Current (V <sub>CE</sub> = 30 Vdc)	ICES	_	0.1	μAdc
Collector Cutoff Current $(V_{CB} = 30 \text{ Vdc}, I_E = 0)$	I <sub>CBO</sub>	_	0.01	μAdc
Emitter Cutoff Current ( $V_{EB} = 5.0 \text{ Vdc}, I_C = 0$ )	I <sub>EBO</sub>	_	0.01	μAdc
ON CHARACTERISTICS				
$ \begin{array}{l} DC \mbox{ Current Gain} \\ (I_C = 0.01 \mbox{ mAdc}, V_{CE} = 5.0 \mbox{ Vdc}) \\ (I_C = 0.1 \mbox{ mAdc}, V_{CE} = 5.0 \mbox{ Vdc}) \\ (I_C = 1.0 \mbox{ mAdc}, V_{CE} = 5.0 \mbox{ Vdc}) \\ (I_C = 10 \mbox{ mAdc}, V_{CE} = 5.0 \mbox{ Vdc}) \end{array} $	h <sub>FE</sub>	500 500 500 500	_ 1250 _ _	_
Collector – Emitter Saturation Voltage ( $I_C = 10 \text{ mAdc}, I_B = 0.5 \text{ mAdc}$ ) ( $I_C = 100 \text{ mAdc}, I_B = 5.0 \text{ mAdc}$ )	V <sub>CE(sat)</sub>		0.2 0.6	Vdc
Base – Emitter On Voltage (I <sub>C</sub> = 1.0 mAdc, V <sub>CE</sub> = 5.0 Vdc)	V <sub>BE(on)</sub>	0.56	0.66	Vdc
SMALL-SIGNAL CHARACTERISTICS				
Current – Gain – Bandwidth Product ( $I_C = 1.0 \text{ mAdc}$ , $V_{CE} = 5.0 \text{ Vdc}$ , f = 100 MHz)	f <sub>T</sub>	100	700	MHz
Output Capacitance (V <sub>CB</sub> = 10 Vdc, I <sub>E</sub> = 0, f = 1.0 MHz)	C <sub>obo</sub>	-	3.0	pF
Input Capacitance (V <sub>EB</sub> = 0.5 Vdc, I <sub>C</sub> = 0, f = 1.0 MHz)	C <sub>ibo</sub>	-	8.0	pF

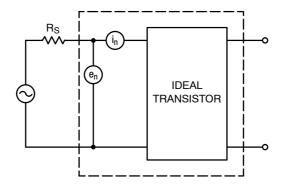
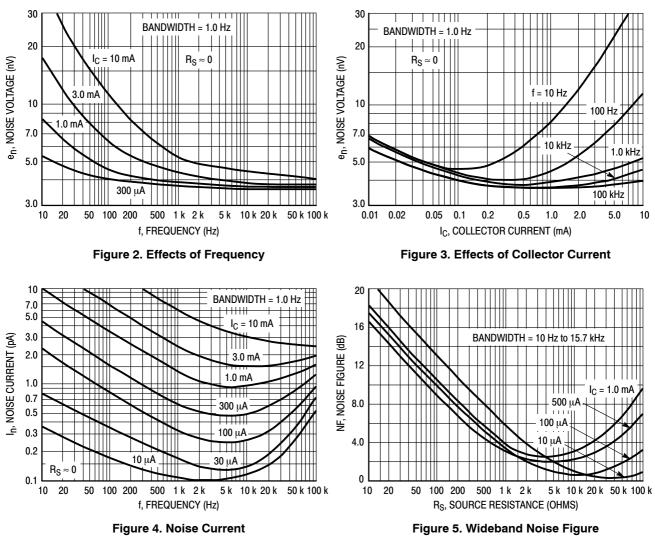


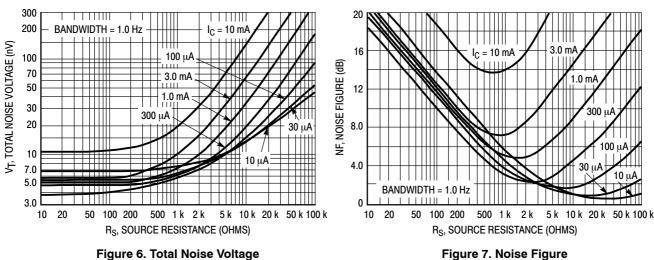
Figure 1. Transistor Noise Model

### **NOISE CHARACTERISTICS**

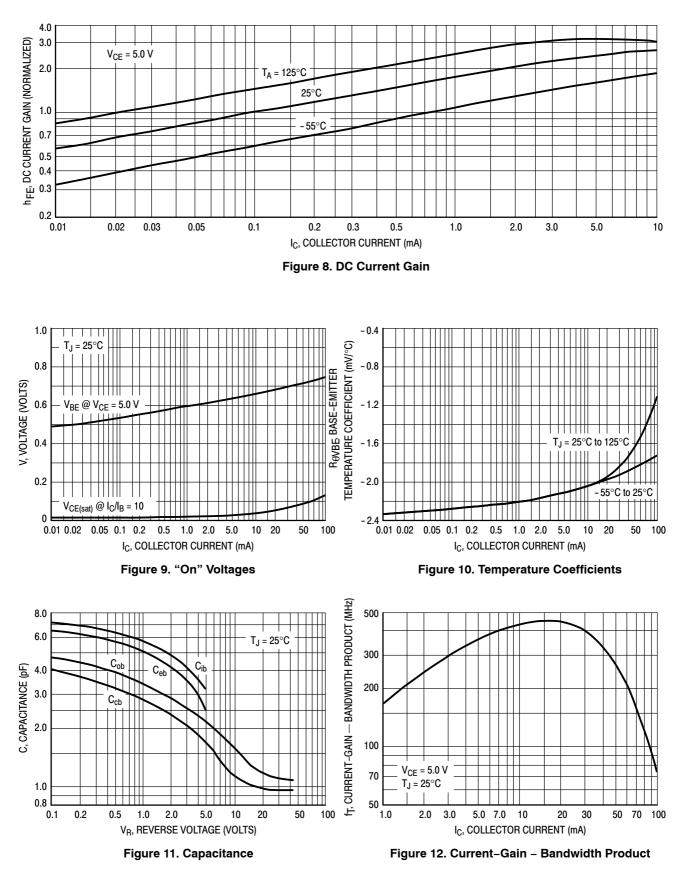
 $(V_{CE} = 5.0 \text{ Vdc}, T_A = 25^{\circ}C)$ 

#### **NOISE VOLTAGE**





100 Hz NOISE DATA

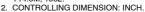


#### PACKAGE DIMENSIONS

#### SC-88/SC70-6/SOT-363

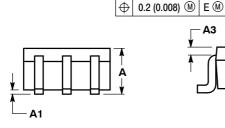
CASE 419B-02 ISSUE W

NOTES: 1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M. 1982.



3. 419B-01 OBSOLETE, NEW STANDARD 419B-02.

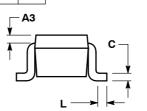
	MILLIMETERS			INCHES		
DIM	MIN	NOM	MAX	MIN	NOM	MAX
Α	0.80	0.95	1.10	0.031	0.037	0.043
A1	0.00	0.05	0.10	0.000	0.002	0.004
A3	0.20 REF			0.008 REF		
b	0.10	0.21	0.30	0.004	0.008	0.012
С	0.10	0.14	0.25	0.004	0.005	0.010
D	1.80	2.00	2.20	0.070	0.078	0.086
Е	1.15	1.25	1.35	0.045	0.049	0.053
е	0.65 BSC			0.026 BSC		
L	0.10	0.20	0.30	0.004	0.008	0.012
HE	2.00	2.10	2.20	0.078	0.082	0.086



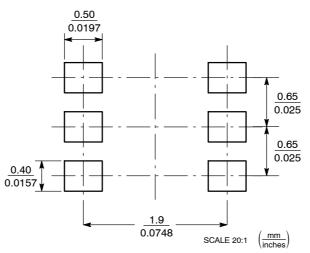
-E

**b** 6 PL

HE



SOLDERING FOOTPRINT\*



\*For additional information on our Pb–Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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