Plastic Medium-Power NPN Silicon Transistor

This device is useful for high-voltage general purpose applications.

Features

- Suitable for Transformerless, Line-Operated Equipment
- Thermopad Construction Provides High Power Dissipation Rating for High Reliability
- These Devices are Pb-Free and are RoHS Compliant*
- Complementary to MJE350

MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Collector–Emitter Voltage	V_{CEO}	300	Vdc
Emitter-Base Voltage	V _{EB}	3.0	Vdc
Collector Current – Continuous	Ic	500	mAdc
Total Power Dissipation @ T _C = 25°C Derate above 25°C	P _D	20 0.16	W mW/°C
Operating and Storage Junction Temperature Range	T _J , T _{stg}	-65 to +150	°C

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

THERMAL CHARACTERISTICS

Characteristic

Characteristic	Symbol	Max	Unit
Thermal Resistance, Junction-to-Case	$\theta_{\sf JC}$	6.25	°C/W

ELECTRICAL CHARACTERISTICS (T_C = 25°C unless otherwise noted)

Symbol

Min

Max

Unit

OFF CHARACTERISTICS				
Collector–Emitter Sustaining Voltage (I _C = 1.0 mAdc, I _B = 0)	V _{CEO(sus)}	300	-	Vdc
Collector Cutoff Current (V _{CB} = 300 Vdc, I _E = 0)	I _{CBO}	_	100	μAdc
Emitter Cutoff Current (V _{EB} = 3.0 Vdc, I _C = 0)	I _{EBO}	_	100	μAdc

ON CHARACTERISTICS

DC Current Gain	h _{FE}			_
$(I_C = 50 \text{ mAdc}, V_{CE} = 10 \text{ Vdc})$		30	240	

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

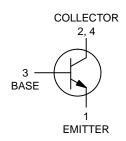


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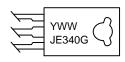
0.5 AMPERE POWER TRANSISTOR NPN SILICON 300 VOLTS, 20 WATTS

SCHEMATIC





MARKING DIAGRAM



Y = Year

WW = Work Week

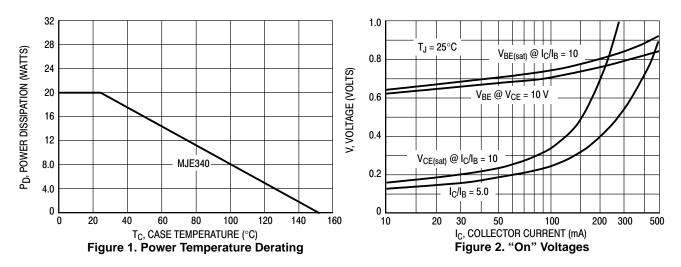
JE340 = Device Code

G = Pb-Free Package

ORDERING INFORMATION

Device	Package	Shipping
MJE340G	TO-225 (Pb-Free)	500 Units/Box

^{*}For additional information on our Pb–Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.



ACTIVE-REGION SAFE OPERATING AREA

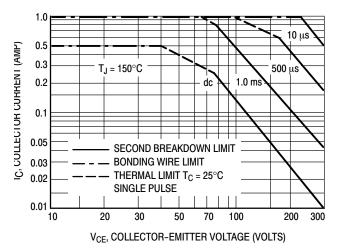
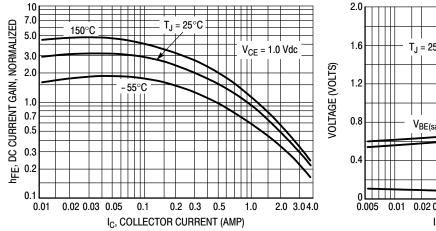


Figure 3. MJE340

There are two limitations on the power handling ability of a transistor: average junction temperature and second breakdown. Safe operating area curves indicate I_C-V_{CE} limits of the transistor that must be observed for reliable operation; i.e., the transistor must not be subjected to greater dissipation than the curves indicate.The data of Figure 3 is based on $T_{J(pk)}=150^{\circ}C;\ T_C$ is variable depending on conditions. Second breakdown pulse limits are valid for duty cycles to 10% provided $T_{J(pk)}\leq 150^{\circ}C.$ At high case temperatures, thermal limitations will reduce the power that can be handled to values less than the limitations imposed by second breakdown.



2.0

1.6

T_J = 25°C

1.2

0.8

V_{BE(sat)} @ I_C/I_B = 10

V_{BE(on)} @ V_{CE} = 1.0 V

0.005 0.01 0.02 0.03 0.05 0.1 0.2 0.3 0.5 1.0 2.0 3.04.0

I_C, COLLECTOR CURRENT (AMP)

Figure 4. DC Current Gain

Figure 5. "On" Voltage

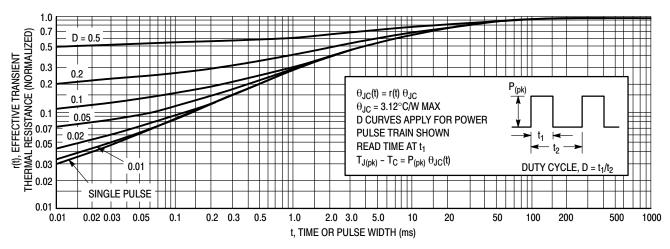


Figure 6. Thermal Response

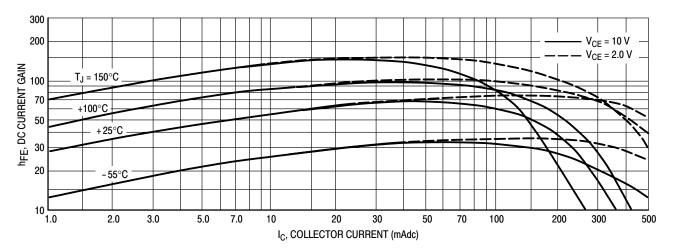
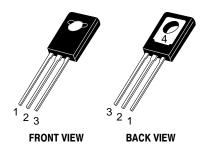
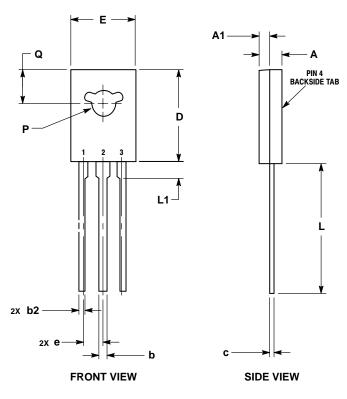


Figure 7. DC Current Gain

PACKAGE DIMENSIONS



TO-225 CASE 77-09 **ISSUE AC**



NOTES:

- 1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.
 2. CONTROLLING DIMENSION: MILLIMETERS.
 3. NUMBER AND SHAPE OF LUGS OPTIONAL.

	MILLIMETERS		
DIM	MIN	MAX	
Α	2.40	3.00	
A1	1.00	1.50	
b	0.60	0.90	
b2	0.51	0.88	
С	0.39	0.63	
D	10.60	11.10	
Е	7.40	7.80	
е	2.04	2.54	
L	14.50	16.63	
L1	1.27	2.54	
Р	2.90	3.30	
Q	3.80	4.20	

STYLE 1:

PIN 1. EMITTER 2., 4. COLLECTOR 3. BASE

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