IGBT

This Insulated Gate Bipolar Transistor (IGBT) features a robust and cost effective Field Stop (FS) Trench construction, and provides superior performance in demanding switching applications, offering both low on-state voltage and minimal switching loss. The IGBT is well suited for resonant or soft switching applications. Incorporated into the device is a rugged co-packaged free wheeling diode with a low forward voltage.

Features

- Low Saturation Voltage using Trench with Fieldstop Technology
- Low Switching Loss Reduces System Power Dissipation
- Optimized for Low Case Temperature in IH Cooker Application
- Low Gate Charge
- These are Pb-Free Devices

Typical Applications

- Inductive Heating
- Consumer Appliances
- Soft Switching

ABSOLUTE MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Collector-emitter voltage	V _{CES}	1200	V
Collector current @ Tc = 25°C @ Tc = 100°C	Ι _c	30 15	A
Pulsed collector current, T _{pulse} limited by T _{Jmax}	I _{CM}	120	A
Diode forward current @ Tc = 25°C @ Tc = 100°C	I _F	30 15	A
Diode pulsed current, T _{pulse} limited by T _{Jmax}	I _{FM}	100	A
Gate-emitter voltage	V_{GE}	±20	V
Power Dissipation @ Tc = 25°C @ Tc = 100°C	@ Tc = 25°C 156		W
Operating junction temperature range			°C
Storage temperature range	T _{stg}	-55 to +150	°C
Lead temperature for soldering, 1/8" from case for 5 seconds	ead temperature for soldering, 1/8" T _{SLD} 260		°C

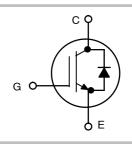
Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

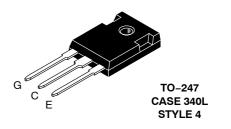


ON Semiconductor®

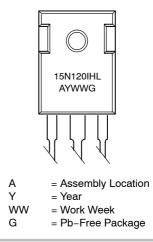
http://onsemi.com

15 A, 1200 V V_{CEsat} = 1.8 V E_{off} = 0.56 mJ





MARKING DIAGRAM



ORDERING INFORMATION

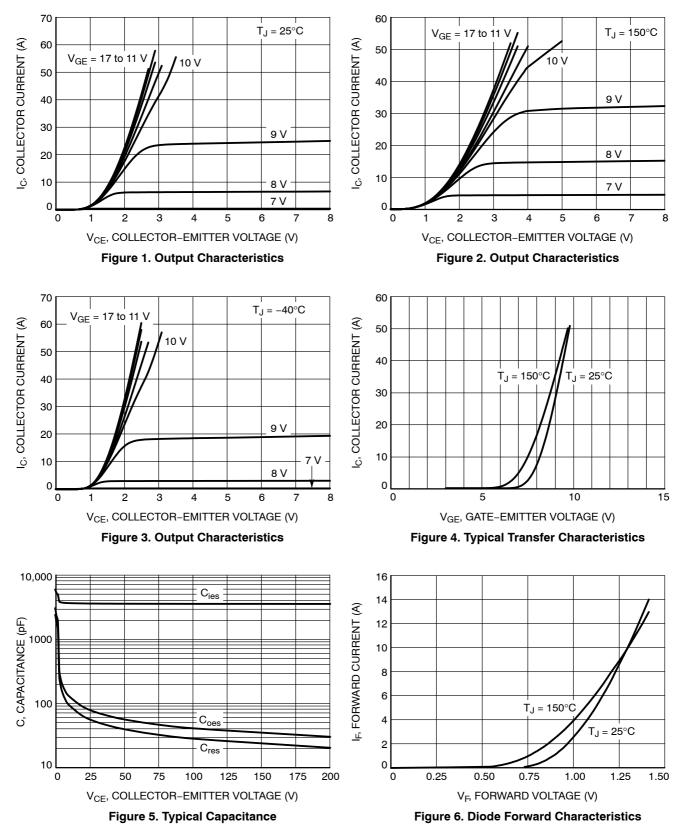
Device	Package	Shipping
NGTB15N120IHLWG	TO-247 (Pb-Free)	30 Units / Rail

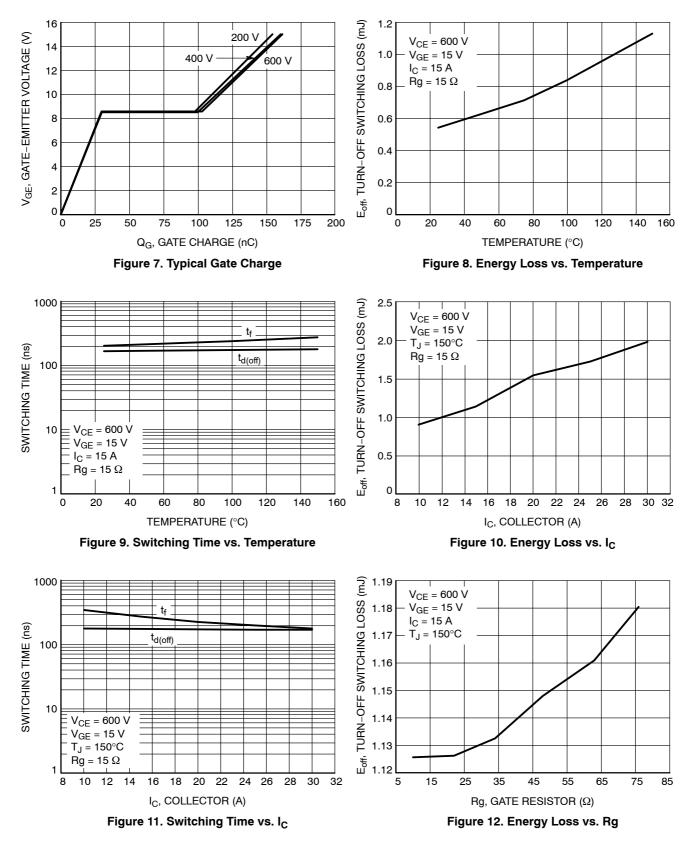
THERMAL CHARACTERISTICS

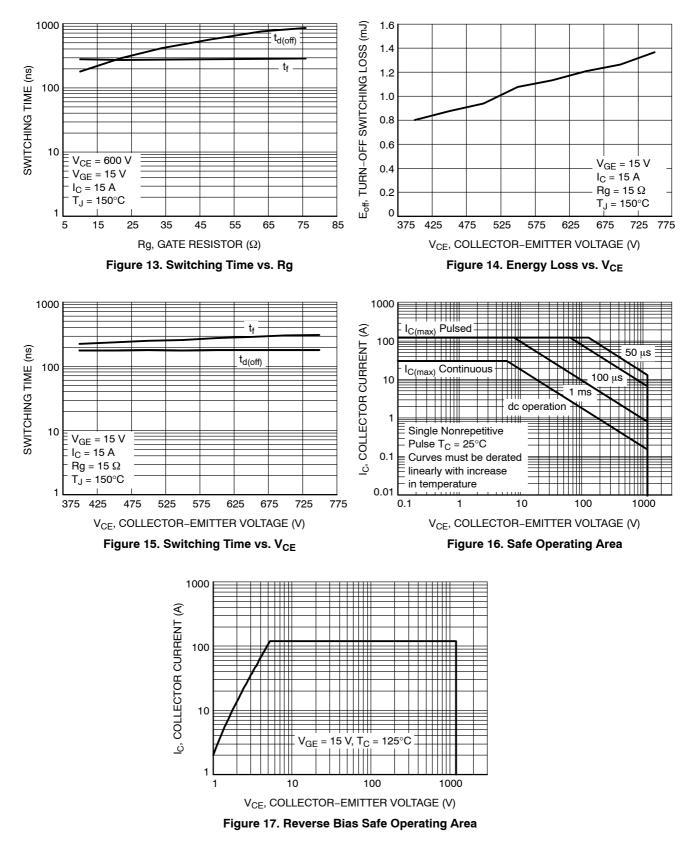
Rating	Symbol	Value	Unit
Thermal resistance junction-to-case, for IGBT	$R_{ ext{ heta}JC}$	0.8	°C/W
Thermal resistance junction-to-case, for Diode	$R_{ ext{ heta}JC}$	2.0	°C/W
Thermal resistance junction-to-ambient	$R_{ hetaJA}$	60	°C/W

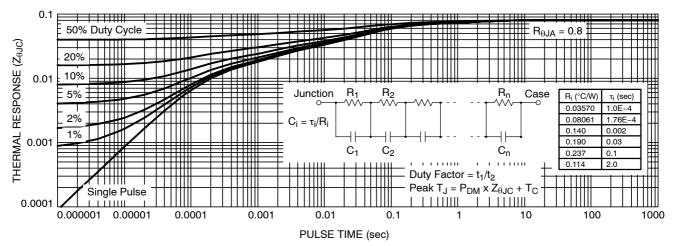
ELECTRICAL CHARACTERISTICS ($T_J = 25^{\circ}C$ unless otherwise specified)

Parameter	Test Conditions	Symbol	Min	Тур	Max	Unit
STATIC CHARACTERISTIC	·					
Collector-emitter breakdown voltage, gate-emitter short-circuited	V_{GE} = 0 V, I _C = 500 μ A	V _{(BR)CES}	1200	_	-	V
Collector-emitter saturation voltage	V_{GE} = 15 V, I _C = 15 A V _{GE} = 15 V, I _C = 15 A, T _J = 150°C	V _{CEsat}	-	1.8 2.0	2.2	V
Gate-emitter threshold voltage	$V_{GE} = V_{CE}$, $I_C = 150 \ \mu A$	V _{GE(th)}	4.5	5.5	6.5	V
Collector-emitter cut-off current, gate- emitter short-circuited	V_{GE} = 0 V, V_{CE} = 1200 V V_{GE} = 0 V, V_{CE} = 1200 V, T_{J} = 150°C	I _{CES}			0.5 2.0	mA
Gate leakage current, collector-emitter short-circuited	V_{GE} = 20 V, V_{CE} = 0 V	I _{GES}	_	-	100	nA
DYNAMIC CHARACTERISTIC	·			•		
Input capacitance		C _{ies}	-	3600	-	pF
Output capacitance	V_{CE} = 20 V, V_{GE} = 0 V, f = 1 MHz	C _{oes}	-	88	-	
Reverse transfer capacitance		C _{res}	-	63	-	
Gate charge total		Qg		160		nC
Gate to emitter charge	V_{CE} = 600 V, I _C = 15 A, V _{GE} = 15 V	Q _{ge}		30		
Gate to collector charge		Q _{gc}		73		
SWITCHING CHARACTERISTIC, INDUCT						
Turn-off delay time	$T_{J} = 25^{\circ}C$	t _{d(off)}		165		ns
Fall time	$V_{CC} = 600 \text{ V}, \text{ I}_{C} = 15 \text{ A}$ $\text{R}_{g} = 15 \Omega$	t _f		200		
Turn-off switching loss	V _{GE} = 0 V/ 15V	E _{off}		0.56		mJ
Turn-off delay time	T _J = 125°C	t _{d(off)}		180		ns
Fall time	$V_{CC} = 600 \text{ V}, \text{ I}_{C} = 15 \text{ A}$ $R_{g} = 15 \Omega$	t _f		260		
Turn-off switching loss	V _{GE} = 0 V/ 15V	E _{off}		0.95		mJ
DIODE CHARACTERISTIC						
Forward voltage	V _{GE} = 0 V, I _F = 15 A V _{GE} = 0 V, I _F = 15 A, T _J = 150°C	V _F		1.4 1.5	1.6	V











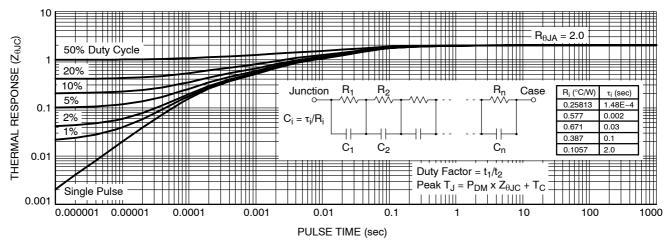


Figure 19. Diode Transient Thermal Impedance

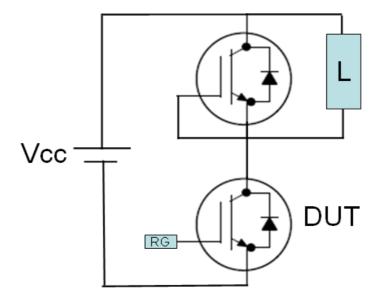
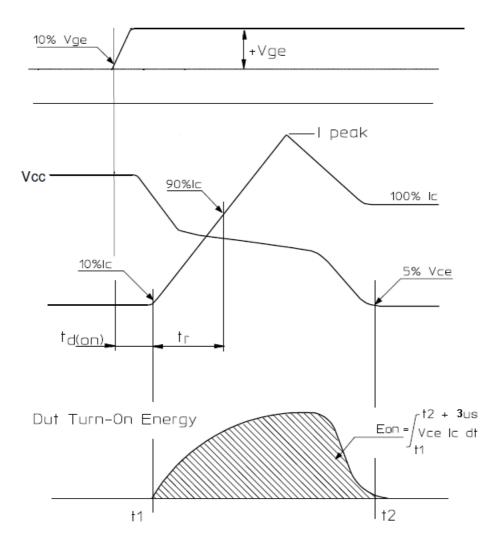


Figure 20. Test Circuit for Switching Characteristics





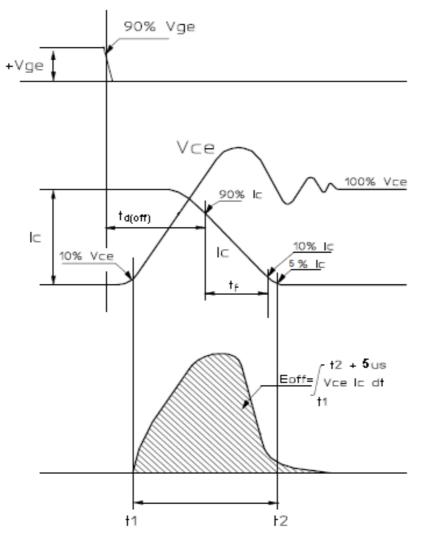
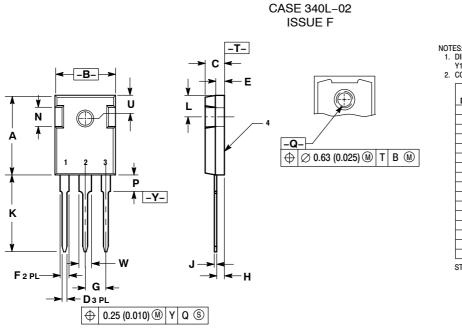


Figure 22. Definition of Turn Off Waveform

PACKAGE DIMENSIONS

TO-247



 1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
2. CONTROLLING DIMENSION: MILLIMETER.

	MILLIN	IETERS	INCHES	
DIM	MIN	MAX	MIN	MAX
Α	20.32	21.08	0.800	8.30
В	15.75	16.26	0.620	0.640
C	4.70	5.30	0.185	0.209
D	1.00	1.40	0.040	0.055
Е	1.90	2.60	0.075	0.102
F	1.65	2.13	0.065	0.084
G	5.45 BSC		0.215 BSC	
н	1.50	2.49	0.059	0.098
J	0.40	0.80	0.016	0.031
K	19.81	20.83	0.780	0.820
L	5.40	6.20	0.212	0.244
Ν	4.32	5.49	0.170	0.216
Р		4.50		0.177
Q	3.55	3.65	0.140	0.144
U	6.15	BSC	0.242 BSC	
W	2.87	3.12	0.113	0.123

STYLE 4: PIN 1. GATE

COLLECTOR
EMITTER

4. COLLECTOR

ON Semiconductor and a registered trademarks of Semiconductor Components Industries, LLC (SCILLC). SCILLC owns the rights to a number of patents, trademarks, copyrights, trade secrets, and other intellectual property. A listing of SCILLC's product/patent coverage may be accessed at www.onsemi.com/site/pdf/Patent-Marking.pdf. SCILLC makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does SCILLC assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. "Typical" parameters which may be provided in SCILLC data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. SCILLC products are not designed, intended, or authorized for use as components intended for surgical implant into the body, or other applications intended to support or sustain life, or for any other application in which the failure of the SCILLC product could create a situation where personal injury or death may occur. Should Buyer purchase or use SCILLC products for any such unintended or unauthorized application, Buyer shall indemnify and hold SCILLC and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that SCILLC was negligent regarding the design or manufacture of the part. SCILLC is an Equal Opportunity/Affirmative Action Employer. This literature is subject to all applicable copyright laws and is not for resale in any manner.

PUBLICATION ORDERING INFORMATION

LITERATURE FULFILLMENT:

Literature Distribution Center for ON Semiconductor P.O. Box 5163, Denver, Colorado 80217 USA Phone: 303–675–2175 or 800–344–3860 Toll Free USA/Canada Fax: 303–675–2176 or 800–344–3867 Toll Free USA/Canada Email: orderlit@onsemi.com N. American Technical Support: 800–282–9855 Toll Free USA/Canada Europe, Middle East and Africa Technical Support:

Phone: 421 33 790 2910 Japan Customer Focus Center Phone: 81-3-5817-1050 ON Semiconductor Website: www.onsemi.com

Order Literature: http://www.onsemi.com/orderlit

For additional information, please contact your local Sales Representative

Mouser Electronics

Authorized Distributor

Click to View Pricing, Inventory, Delivery & Lifecycle Information:

ON Semiconductor: NGTB15N120IHLWG