# **IGBT**

This Insulated Gate Bipolar Transistor (IGBT) features a robust and cost effective Field Stop (FS) Trench construction, and provides superior performance in demanding switching applications, offering both low on state voltage and minimal switching loss. The IGBT is well suited for half bridge resonant applications. Incorporated into the device is a soft and fast co–packaged free wheeling diode with a low forward voltage.

#### **Features**

- Low Saturation Voltage using Trench with Fieldstop Technology
- Low Switching Loss Reduces System Power Dissipation
- Low Gate Charge
- Soft, Fast Free Wheeling Diode
- These are Pb-Free Devices

## **Typical Applications**

- Inverter Welding
- UPS Systems

#### **ABSOLUTE MAXIMUM RATINGS**

Rating	Symbol	Value	Unit
Collector-emitter voltage	V <sub>CES</sub>	600	V
Collector current @ T <sub>C</sub> = 25°C @ T <sub>C</sub> = 100°C	lc	60 30	А
Pulsed collector current, T <sub>pulse</sub> limited by T <sub>Jmax</sub>	I <sub>CM</sub>	120	Α
Diode forward current @ T <sub>C</sub> = 25°C @ T <sub>C</sub> = 100°C	I <sub>F</sub>	60 30	A
Diode pulsed current, T <sub>pulse</sub> limited by T <sub>Jmax</sub>	I <sub>FM</sub>	120	Α
Gate-emitter voltage	$V_{GE}$	±20	V
Power Dissipation @ T <sub>C</sub> = 25°C @ T <sub>C</sub> = 100°C	P <sub>D</sub>	189 76	W
Operating junction temperature range	TJ	-55 to +150	°C
Storage temperature range	T <sub>stg</sub>	-55 to +150	°C
Lead temperature for soldering, 1/8" from case for 5 seconds	T <sub>SLD</sub>	260	°C

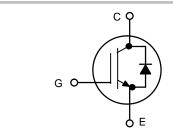
Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

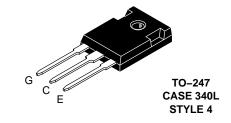


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http://onsemi.com

30 A, 600 V V<sub>CEsat</sub> = 1.9 V E<sub>off</sub> = 0.54 mJ





#### **MARKING DIAGRAM**



A = Assembly Location

Y = Year WW = Work Week G = Pb-Free Package

#### **ORDERING INFORMATION**

Device	Package	Shipping
NGTB30N60SWG	TO-247 (Pb-Free)	30 Units / Rail

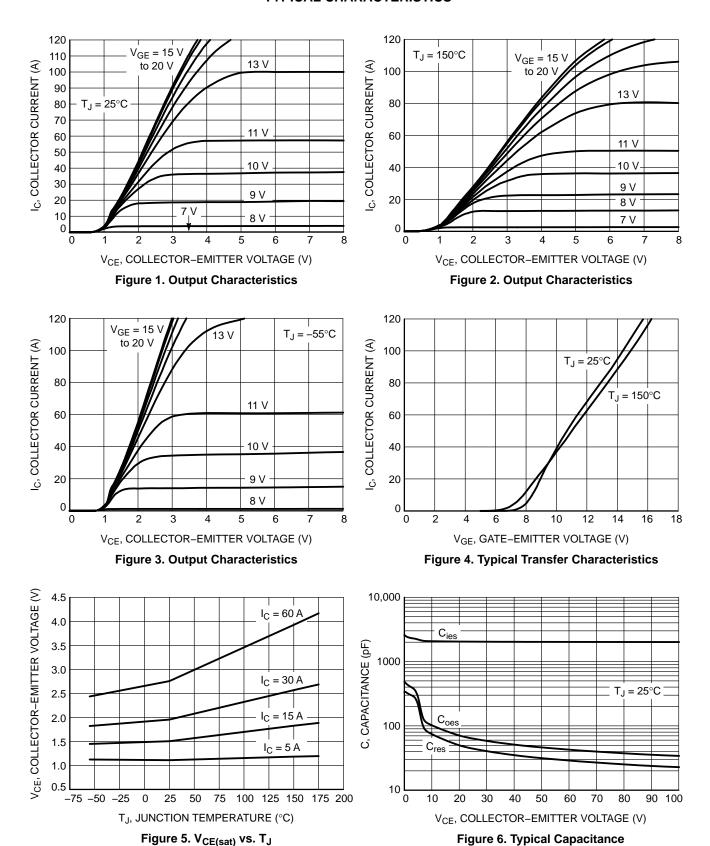
### THERMAL CHARACTERISTICS

Rating	Symbol	Value	Unit
Thermal resistance junction-to-case, for IGBT	$R_{ heta JC}$	0.66	°C/W
Thermal resistance junction-to-case, for Diode	$R_{ heta JC}$	2.73	°C/W
Thermal resistance junction-to-ambient	$R_{ heta JA}$	40	°C/W

### **ELECTRICAL CHARACTERISTICS** (T<sub>J</sub> = 25°C unless otherwise specified)

Parameter	Test Conditions	Symbol	Min	Тур	Max	Unit
STATIC CHARACTERISTIC						•
Collector–emitter breakdown voltage, gate–emitter short–circuited	$V_{GE} = 0 \text{ V, } I_{C} = 500  \mu\text{A}$	V <sub>(BR)CES</sub>	600	-	-	V
Collector-emitter saturation voltage	V <sub>GE</sub> = 15 V, I <sub>C</sub> = 30 A V <sub>GE</sub> = 15 V, I <sub>C</sub> = 30 A, T <sub>J</sub> = 150°C	V <sub>CEsat</sub>	- -	1.9 2.6	2.2	V
Gate-emitter threshold voltage	$V_{GE} = V_{CE}, I_{C} = 150 \mu A$	$V_{GE(th)}$	4.5	5.5	6.5	V
Collector-emitter cut-off current, gate- emitter short-circuited	V <sub>GE</sub> = 0 V, V <sub>CE</sub> = 600 V V <sub>GE</sub> = 0 V, V <sub>CE</sub> = 600 V, T <sub>J</sub> = 150°C	I <sub>CES</sub>	- -	_ _	0.2 2	mA
Gate leakage current, collector–emitter short–circuited	V <sub>GE</sub> = 20 V , V <sub>CE</sub> = 0 V	I <sub>GES</sub>	-	-	100	nA
DYNAMIC CHARACTERISTIC						
Input capacitance		C <sub>ies</sub>	-	2040	-	pF
Output capacitance	$V_{CE} = 20 \text{ V}, V_{GE} = 0 \text{ V}, f = 1 \text{ MHz}$	C <sub>oes</sub>	-	70	_	
Reverse transfer capacitance	1	C <sub>res</sub>	-	50	_	1
Gate charge total		$Q_g$		90		nC
Gate to emitter charge	$V_{CE} = 480 \text{ V}, I_{C} = 30 \text{ A}, V_{GE} = 15 \text{ V}$	Q <sub>ge</sub>		19		
Gate to collector charge	1	Q <sub>gc</sub>		45		
SWITCHING CHARACTERISTIC, INDUC	TIVE LOAD					•
Turn-on delay time		t <sub>d(on)</sub>		57		ns
Rise time	1	t <sub>r</sub>		32		1
Turn-off delay time	$T_J = 25^{\circ}C$ $V_{CC} = 400 \text{ V, } I_C = 30 \text{ A}$	t <sub>d(off)</sub>		109		1
Fall time	$R_g = 10 \Omega$ $V_{GE} = 0 \text{ V/ } 15 \text{ V}$	t <sub>f</sub>		91		1
Turn-on switching loss	VGE = 0 V/ 13 V	E <sub>on</sub>		0.75		mJ
Turn-off switching loss	1	E <sub>off</sub>		0.54		mJ
Turn-on delay time		t <sub>d(on)</sub>		56		ns
Rise time	1	t <sub>r</sub>		34		1
Turn-off delay time	$T_J = 150^{\circ}C$ $V_{CC} = 400 \text{ V, } I_C = 30 \text{ A}$	t <sub>d(off)</sub>		113		1
Fall time	$R_g = 10 \Omega$ $V_{GE} = 0 \text{ V/ } 15 \text{ V}$	t <sub>f</sub>		172		1
Turn-on switching loss	V <sub>GE</sub> = 0 V/ 15 V	E <sub>on</sub>		0.91		mJ
Turn-off switching loss		E <sub>off</sub>		0.87		mJ
DIODE CHARACTERISTIC		•				•
Forward voltage	V <sub>GE</sub> = 0 V, I <sub>F</sub> = 30 A V <sub>GE</sub> = 0 V, I <sub>F</sub> = 30 A, T <sub>J</sub> = 150°C	V <sub>F</sub>		2.3 2.5	2.5	V
Reverse recovery time	T <sub>J</sub> = 25°C	t <sub>rr</sub>		200		ns
Reverse recovery charge	$I_F = 30 \text{ Å}, V_R = 400 \text{ V}$ $di_F/dt = 200 \text{ A/}\mu\text{s}$	Q <sub>rr</sub>		1000		nc
Reverse recovery current	1	I <sub>rrm</sub>		9		Α

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.



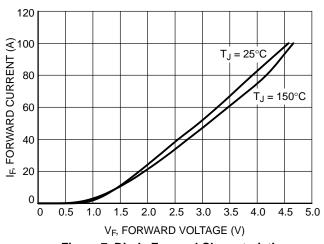


Figure 7. Diode Forward Characteristics

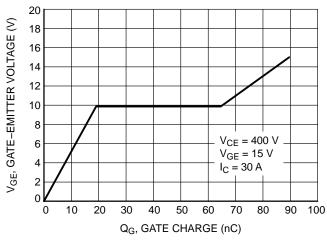


Figure 8. Typical Gate Charge

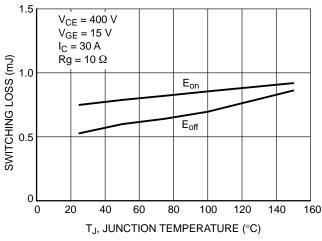


Figure 9. Switching Loss vs. Temperature

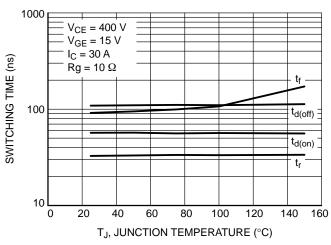
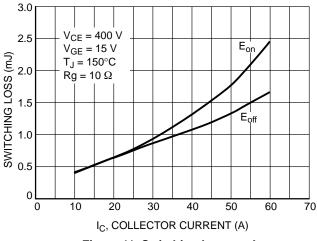


Figure 10. Switching Time vs. Temperature





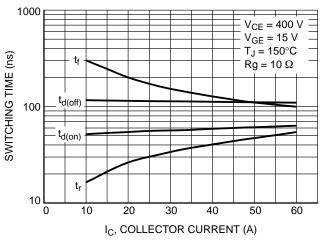


Figure 12. Switching Time vs. I<sub>C</sub>

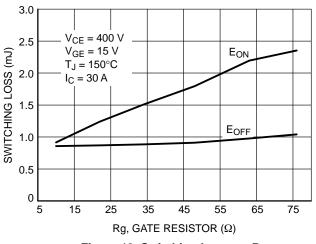


Figure 13. Switching Loss vs. Rg

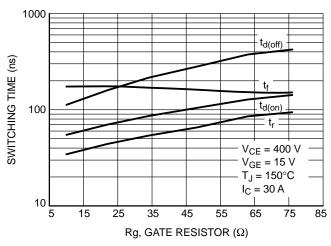


Figure 14. Switching Time vs. Rg

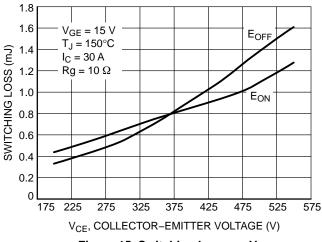


Figure 15. Switching Loss vs. V<sub>CE</sub>

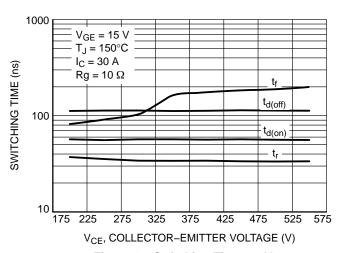


Figure 16. Switching Time vs. V<sub>CE</sub>

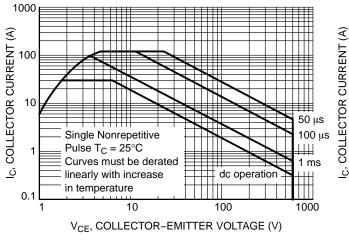


Figure 17. Safe Operating Area

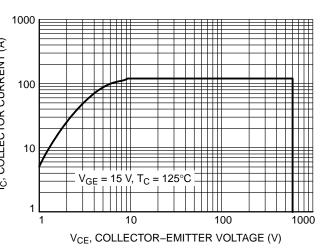


Figure 18. Reverse Bias Safe Operating Area

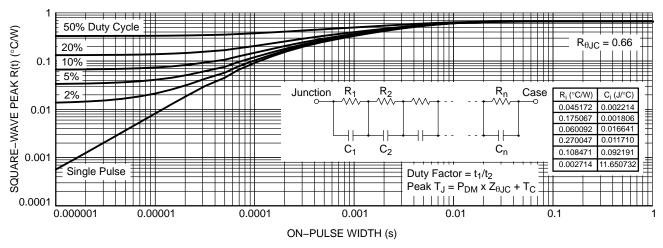


Figure 19. IGBT Die Self-heating Square-wave Duty Cycle Transient Thermal Response

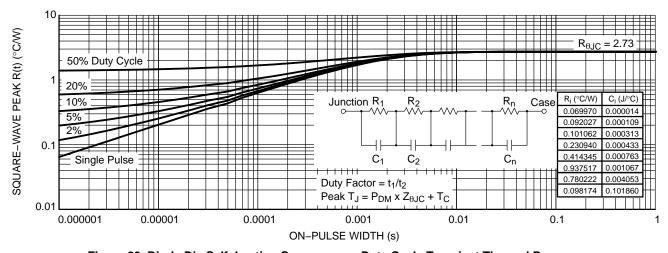
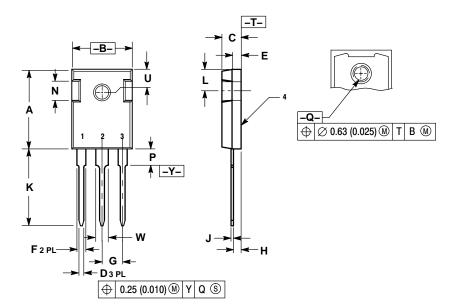


Figure 20. Diode Die Self-heating Square-wave Duty Cycle Transient Thermal Response

#### PACKAGE DIMENSIONS

#### TO-247 CASE 340L-02 ISSUE F



- NOTES:
  1. DIMENSIONING AND TOLERANCING PER ANSI
- Y14.5M, 1982.
  2. CONTROLLING DIMENSION: MILLIMETER.

	MILLIMETERS		INCHES		
DIM	MIN	MAX	MIN	MAX	
Α	20.32	21.08	0.800	8.30	
В	15.75	16.26	0.620	0.640	
C	4.70	5.30	0.185	0.209	
D	1.00	1.40	0.040	0.055	
Е	1.90	2.60	0.075	0.102	
F	1.65	2.13	0.065	0.084	
G	5.45 BSC		0.215 BSC		
Н	1.50	2.49	0.059	0.098	
7	0.40	0.80	0.016	0.031	
K	19.81	20.83	0.780	0.820	
L	5.40	6.20	0.212	0.244	
N	4.32	5.49	0.170	0.216	
Р		4.50		0.177	
Q	3.55	3.65	0.140	0.144	
U	6.15	BSC	0.242 BSC		
W	2.87	3.12	0.113	0.123	

#### STYLE 4:

- PIN 1. GATE 2. COLLECTOR

  - 3. EMITTER 4 COLLECTOR

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