Very Low Leakage Trench-based Schottky Rectifier

Features

- Fine Lithography Trench–based Schottky Technology for Very Low Forward Voltage and Low Leakage
- Fast Switching with Exceptional Temperature Stability
- Low Power Loss and Lower Operating Temperature
- Higher Efficiency for Achieving Regulatory Compliance
- Low Thermal Resistance
- High Surge Capability
- These are Pb–Free Devices

Typical Applications

- Switching Power Supplies including Notebook / Netbook Adapters, ATX and Flat Panel Display
- High Frequency and DC–DC Converters
- Freewheeling and OR–ing diodes
- Reverse Battery Protection
- Instrumentation

Mechanical Characteristics

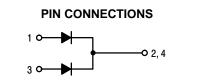
- Case: Epoxy, Molded
- Epoxy Meets Flammability Rating UL 94-0 @ 0.125 in
- Finish: All External Surfaces Corrosion Resistant and Terminal Leads are Readily Solderable
- Lead Temperature for Soldering Purposes: 260°C Maximum for 10 sec

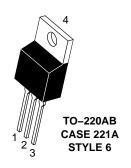


ON Semiconductor®

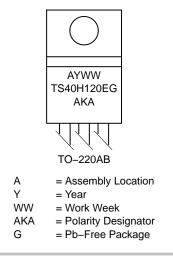
www.onsemi.com

VERY LOW LEAKAGE, SCHOTTKY BARRIER RECTIFIERS 40 AMPERES, 120 VOLTS





MARKING DIAGRAM



ORDERING INFORMATION

See detailed ordering and shipping information in the package dimensions section on page 4 of this data sheet.

MAXIMUM RATINGS

| Rating | | Symbol | Value | Unit |
|---|-------------------------|--|-------------|------|
| Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage | | V _{RRM} V _{RWM} V _R | 120 | V |
| Average Rectified Forward Current (Rated V_R , $T_C = 124^{\circ}C$) (Rated V_R , $T_C = 134^{\circ}C$) | Per device Per diode | I _{F(AV)} | 40 20 | A |
| Peak Repetitive Forward Current (Rated V _R , Square Wave, 20 kHz, $T_C = 120^{\circ}C$) (Rated V _R , Square Wave, 20 kHz, $T_C = 130^{\circ}C$) | Per device Per diode | I _{FRM} | 80 40 | A |
| Nonrepetitive Peak Surge Current (Surge applied at rated load conditions halfwave, single phase, 60 Hz) | | I _{FSM} | 250 | A |
| Operating Junction Temperature | | TJ | -40 to +150 | °C |
| Storage Temperature | | T _{stg} | -40 to +150 | °C |
| Voltage Rate of Change (Rated V _R) | | dV/dt | 36 | V/ns |

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

THERMAL CHARACTERISTICS

| Rating | Symbol | NTST40H120ECTG | Unit |
|--|-------------------------------|----------------|--------------|
| Maximum Thermal Resistance per Device Junction-to-Case Junction-to-Ambient | $R_{	heta JC} \ R_{	heta JA}$ | 0.81 70 | °C/W °C/W |

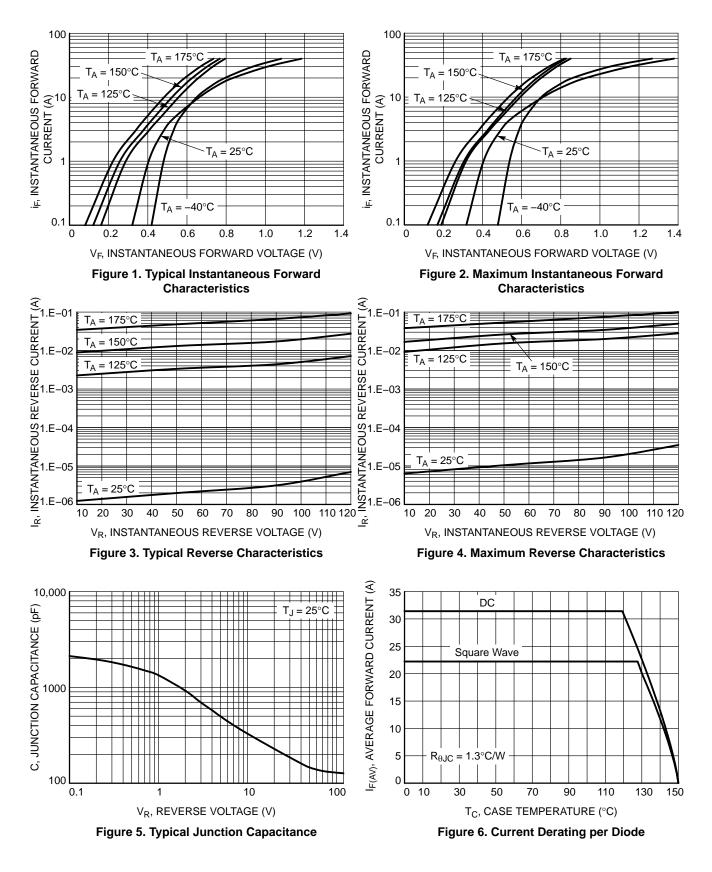
ELECTRICAL CHARACTERISTICS (Per Leg unless otherwise noted)

| Rating | Symbol | Тур | Max | Unit |
|---|----------------|----------------------|---------------|----------|
| | ۷ _F | 0.54 0.67 0.84 | 0.93 | V |
| $(I_F = 5 A, T_J = 125^{\circ}C)$ $(I_F = 10 A, T_J = 125^{\circ}C)$ $(I_F = 20 A, T_J = 125^{\circ}C)$ | | 0.47 0.56 0.66 | _ _ 0.7 | |
| Maximum Instantaneous Reverse Current (Note 1) $(V_R = 90 \text{ V}, T_J = 25^{\circ}\text{C})$ $(V_R = 90 \text{ V}, T_J = 125^{\circ}\text{C})$ | I _R | 3 5 | | μA mA |
| (Rated dc Voltage, $T_J = 25^{\circ}C$) (Rated dc Voltage, $T_J = 125^{\circ}C$) | | - 7 | 25 28 | μA mA |

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

1. Pulse Test: Pulse Width = 300 μ s, Duty Cycle \leq 2.0%

TYPICAL CHARACTERISTICS



TYPICAL CHARACTERISTICS

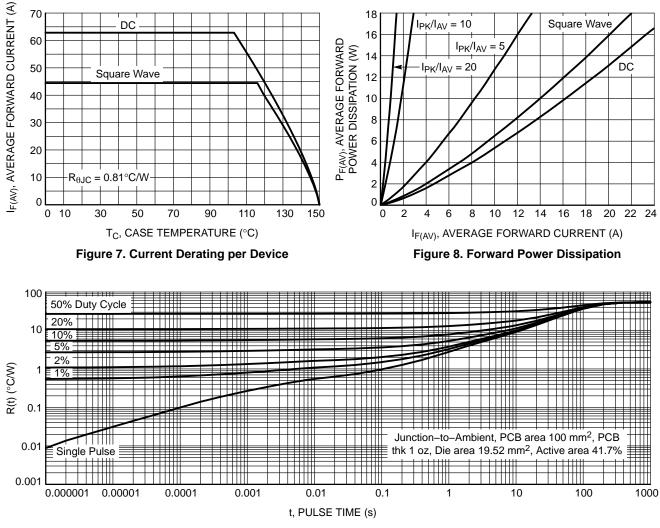


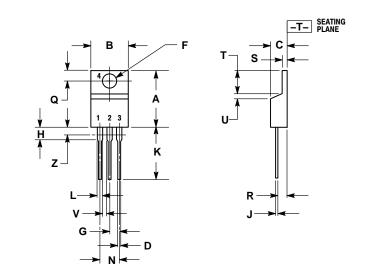
Figure 9. Thermal Characteristics

ORDERING INFORMATION

| Device | Package | Shipping |
|----------------|-----------------------|-----------------|
| NTST40H120ECTG | TO-220AB (Pb-Free) | 50 Units / Rail |

PACKAGE DIMENSIONS

TO-220 CASE 221A-09 ISSUE AF



| | INCHES | | MILLIMETERS | |
|-----|--------|-------|-------------|-------|
| DIM | MIN | MAX | MIN | MAX |
| Α | 0.570 | 0.620 | 14.48 | 15.75 |
| В | 0.380 | 0.405 | 9.66 | 10.28 |
| С | 0.160 | 0.190 | 4.07 | 4.82 |
| D | 0.025 | 0.035 | 0.64 | 0.88 |
| F | 0.142 | 0.161 | 3.61 | 4.09 |
| G | 0.095 | 0.105 | 2.42 | 2.66 |
| Н | 0.110 | 0.155 | 2.80 | 3.93 |
| J | 0.014 | 0.025 | 0.36 | 0.64 |
| Κ | 0.500 | 0.562 | 12.70 | 14.27 |
| L | 0.045 | 0.060 | 1.15 | 1.52 |
| Ν | 0.190 | 0.210 | 4.83 | 5.33 |
| Q | 0.100 | 0.120 | 2.54 | 3.04 |
| R | 0.080 | 0.110 | 2.04 | 2.79 |
| S | 0.045 | 0.055 | 1.15 | 1.39 |
| Т | 0.235 | 0.255 | 5.97 | 6.47 |
| U | 0.000 | 0.050 | 0.00 | 1.27 |
| ٧ | 0.045 | | 1.15 | |
| Ζ | | 0.080 | | 2.04 |

DIMENSIONING AND TOLERANCING PER ANSI

CONTROLLING DIMENSION: INCH. DIMENSION Z DEFINES A ZONE WHERE ALL

STYLE 6: PIN 1. ANODE 2. CATHODE 3. ANODE

NOTES

3.

Y14.5M, 1982.

4. CATHODE

ON Semiconductor and the intervent and the intervent of patients, trademarks of Semiconductor Components Industries, LLC (SCILLC) or its subsidiaries in the United States and/or other countries. SCILLC owns the rights to a number of patents, trademarks, copyrights, trade secrets, and other intellectual property. A listing of SCILLC's product/patent coverage may be accessed at www.onsemic.com/site/pdf/Patent-Marking.pdf. SCILLC reserves the right to make changes without further notice to any products herein. SCILLC makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does SCILLC assume any liability arising out of the application or use of any product or circuit, and specifications can and do vary in different applications special, consequential or incidental damages. "Typical" parameters which may be provided in SCILLC data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. SCILLC does not convey any license under its patent rights of others. SCILLC products are not designed, intended, or authorized for uses a components in systems intended for surgical implant into the body, or other applications intended to support or sustain life, or for any such unintended or unauthorized application, Buyer shall indemnify and hold SCILLC and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expense, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim of personal injury or death associated with such unintended or unauthorized use, even if such claim of personal injury or death associated with such unintended or unauthorized use, even if such claim apersonal injury or death associ

PUBLICATION ORDERING INFORMATION

LITERATURE FULFILLMENT:

Literature Distribution Center for ON Semiconductor P.O. Box 5163, Denver, Colorado 80217 USA Phone: 303–675–2175 or 800–344–3860 Toll Free USA/Canada Fax: 303–675–2176 or 800–344–3867 Toll Free USA/Canada Email: orderlit@onsemi.com N. American Technical Support: 800–282–9855 Toll Free USA/Canada Europe, Middle East and Africa Technical Support: Phone: 421 33 700 2010

Phone: 421 33 790 2910 Japan Customer Focus Center Phone: 81–3–5817–1050 ON Semiconductor Website: www.onsemi.com

Order Literature: http://www.onsemi.com/orderlit

For additional information, please contact your local Sales Representative

Mouser Electronics

Authorized Distributor

Click to View Pricing, Inventory, Delivery & Lifecycle Information:

ON Semiconductor: NTST40H120ECTG