Very Low Forward Voltage Trench-based Schottky Rectifier

Exceptionally Low $V_F = 0.471 \text{ V}$ at $I_F = 5 \text{ A}$

Features

- Fine Lithography Trench-based Schottky Technology for Very Low Forward Voltage and Low Leakage
- Fast Switching with Exceptional Temperature Stability
- Low Power Loss and Lower Operating Temperature
- Higher Efficiency for Achieving Regulatory Compliance
- Low Thermal Resistance
- High Surge Capability
- This Device is Pb–Free, Halogen Free/BFR Free and is RoHS Compliant

Typical Applications

- Switching Power Supplies including Notebook/Netbook Adapters, ATX and Flat Panel Display
- High Frequency and DC-DC Converters
- Freewheeling and OR-ing Diodes
- Reverse Battery Protection
- Instrumentation

Mechanical Characteristics

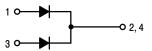
- Case: Epoxy, Molded
- Epoxy Meets Flammability Rating UL 94-0 @ 0.125 in
- Finish: All External Surfaces Corrosion Resistant and Terminal Leads are Readily Solderable
- Lead Temperature for Soldering Purposes: 260°C Maximum for 10 sec



ON Semiconductor®

http://onsemi.com

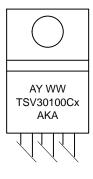
PIN CONNECTIONS





TO-220 CASE 221A STYLE 6

MARKING DIAGRAM



A = Assembly Location

Y = Year
WW = Work Week
AKA = Polarity Designator
x = G or H

G = Pb-Free Package H = Halide-Free Package

ORDERING INFORMATION

Device	Package	Shipping
NTSV30100CTG	TO-220 (Pb-Free)	50 Units / Rail

MAXIMUM RATINGS

Rating		Value	Unit
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage	V _{RRM} V _{RWM} V _R	100	V
Average Rectified Forward Current (Rated V_R , T_C = 115°C) Per Device Per Diode	I _{F(AV)}	30 15	A
Peak Repetitive Forward Current (Rated V _R , Square Wave, 20 kHz, T _C = 110°C) Per Device Per Diode	I _{FRM}	60 30	A
Non-repetitive Peak Surge Current (Surge Applied at Rated Load Conditions Halfwave, Single Phase, 60 Hz)	I _{FSM}	100	А
Operating Junction Temperature	TJ	-40 to +150	°C
Storage Temperature	T _{stg}	-40 to +150	°C
Voltage Rate of Change (Rated V _R)	dv/dt	10,000	V/μs

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

THERMAL CHARACTERISTICS

Rating	Symbol	Value	Unit
Maximum Thermal Resistance Junction-to-Case Junction-to-Ambient	$R_{ heta JC} \ R_{ heta JA}$	2.0 70	°C/W

ELECTRICAL CHARACTERISTICS (Per Leg unless otherwise noted)

Rating	Symbol	Тур	Max	Unit
Maximum Instantaneous Forward Voltage (Note 1) ($I_F = 5 \text{ A}$, $T_J = 25^{\circ}\text{C}$) ($I_F = 7.5 \text{ A}$, $T_J = 25^{\circ}\text{C}$) ($I_F = 15 \text{ A}$, $T_J = 25^{\circ}\text{C}$)	VF	0.509 0.575 0.751	- - 1.05	V
$(I_F = 5 A, T_J = 125^{\circ}C)$ $(I_F = 7.5 A, T_J = 125^{\circ}C)$ $(I_F = 15 A, T_J = 125^{\circ}C)$		0.471 0.539 0.662	- - 0.82	
Maximum Instantaneous Reverse Current (Note 1) $(V_R = 70 \text{ V}, T_J = 25^{\circ}\text{C})$ $(V_R = 70 \text{ V}, T_J = 125^{\circ}\text{C})$	I _R	11.2 7.9		μA mA
(Rated dc Voltage, T _J = 25°C) (Rated dc Voltage, T _J = 125°C)		63 19.1	500 35	μA mA

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

1. Pulse Test: Pulse Width = $300 \mu s$, Duty Cycle $\leq 2.0\%$

TYPICAL CHARACTERISTICS

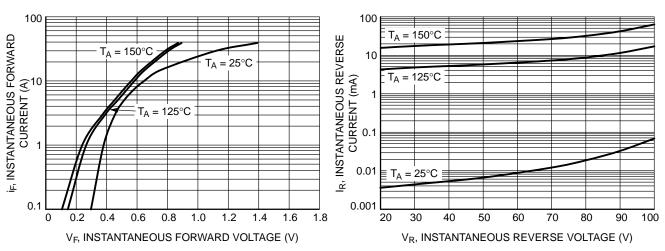


Figure 1. Typical Instantaneous Forward Characteristics

Figure 2. Typical Reverse Characteristics

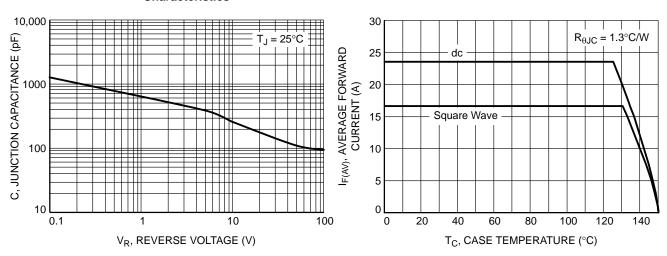


Figure 3. Typical Junction Capacitance

Figure 4. Current Derating per Leg

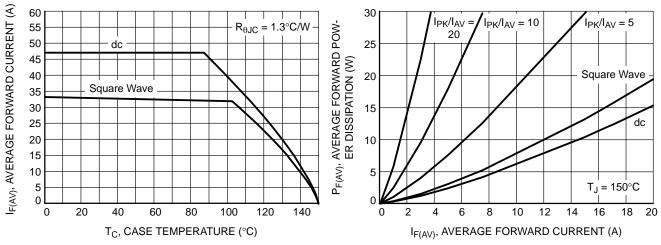


Figure 5. Current Derating

Figure 6. Forward Power Dissipation

TYPICAL CHARACTERISTICS

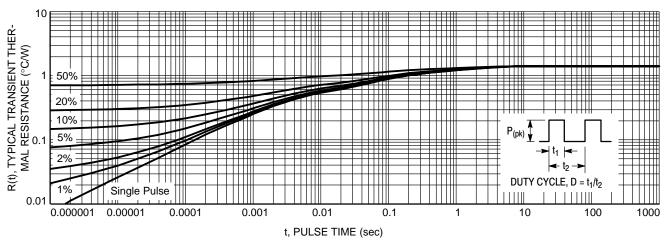
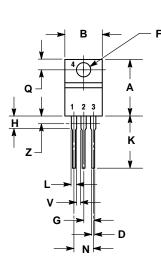
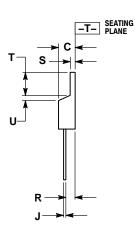


Figure 7. Typical Transient Thermal Response

PACKAGE DIMENSIONS

TO-220 CASE 221A-09 **ISSUE AH**





NOTES:

- DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982. CONTROLLING DIMENSION: INCH.
- DIMENSION Z DEFINES A ZONE WHERE ALL BODY AND LEAD IRREGULARITIES ARE ALLOWED.

	INCHES		MILLIMETERS	
DIM	MIN	MAX	MIN	MAX
Α	0.570	0.620	14.48	15.75
В	0.380	0.415	9.66	10.53
С	0.160	0.190	4.07	4.83
D	0.025	0.038	0.64	0.96
F	0.142	0.161	3.61	4.09
G	0.095	0.105	2.42	2.66
Н	0.110	0.161	2.80	4.10
J	0.014	0.024	0.36	0.61
K	0.500	0.562	12.70	14.27
L	0.045	0.060	1.15	1.52
N	0.190	0.210	4.83	5.33
Q	0.100	0.120	2.54	3.04
R	0.080	0.110	2.04	2.79
S	0.045	0.055	1.15	1.39
T	0.235	0.255	5.97	6.47
U	0.000	0.050	0.00	1.27
٧	0.045		1.15	
Z		0.080		2.04

STYLE 6: PIN 1. ANODE

2. CATHODE

ANODE 3. CATHODE

ON Semiconductor and the 👊 are registered trademarks of Semiconductor Components Industries, LLC (SCILLC) or its subsidiaries in the United States and/or other countries. SCILLC owns the rights to a number of patents, trademarks, copyrights, trade secrets, and other intellectual property. A listing of SCILLC's product/patent coverage may be accessed at www.onsemi.com/site/pdf/Patent-Marking.pdf. SCILLC reserves the right to make changes without further notice to any products herein. SCILLC makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does SCILLC assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. "Typical" parameters which may be provided in SCILLC data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. SCILLC does not convey any licensee under its patent rights of others. SCILLC products are not designed, intended, or other applications in systems in systems intended for surprised for use as components in systems instanced for surprised in systems in systems. or authorized for use as components in systems intended for surgical implant into the body, or other applications intended to support or sustain life, or for any other application in which the failure of the SCILLC product could create a situation where personal injury or death may occur. Should Buyer purchase or use SCILLC products for any such unintended or unauthorized application, Buyer shall indemnify and hold SCILLC and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that SCILLC was negligent regarding the design or manufacture of the part. SCILLC is an Equal Opportunity/Affirmative Action Employer. This literature is subject to all applicable copyright laws and is not for resale in any manner.

PUBLICATION ORDERING INFORMATION

LITERATURE FULFILLMENT:

Literature Distribution Center for ON Semiconductor P.O. Box 5163, Denver, Colorado 80217 USA

Phone: 303-675-2175 or 800-344-3860 Toll Free USA/Canada Fax: 303-675-2176 or 800-344-3867 Toll Free USA/Canada

Email: orderlit@onsemi.com

N. American Technical Support: 800-282-9855 Toll Free USA/Canada

Europe, Middle East and Africa Technical Support: Phone: 421 33 790 2910

Japan Customer Focus Center

Phone: 81-3-5817-1050

ON Semiconductor Website: www.onsemi.com

Order Literature: http://www.onsemi.com/orderlit

For additional information, please contact your local Sales Representative

Mouser Electronics

Authorized Distributor

Click to View Pricing, Inventory, Delivery & Lifecycle Information:

ON Semiconductor: NTSV30100CTG