Very Low Forward Voltage Trench-based Schottky Rectifier

Exceptionally Low $V_F = 0.471 \text{ V}$ at $I_F = 5 \text{ A}$

Features

- Fine Lithography Trench-based Schottky Technology for Very Low Forward Voltage and Low Leakage
- Fast Switching with Exceptional Temperature Stability
- Low Power Loss and Lower Operating Temperature
- Higher Efficiency for Achieving Regulatory Compliance
- Low Thermal Resistance
- High Surge Capability
- These Devices are Pb–Free, Halogen Free/BFR Free and are RoHS Compliant

Typical Applications

- Switching Power Supplies including Notebook / Netbook Adapters, ATX and Flat Panel Display
- High Frequency and DC-DC Converters
- Freewheeling and OR-ing Diodes
- Reverse Battery Protection
- Instrumentation

Mechanical Characteristics

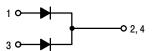
- Case: Epoxy, Molded
- Epoxy Meets Flammability Rating UL 94-0 @ 0.125 in
- Finish: All External Surfaces Corrosion Resistant and Terminal Leads are Readily Solderable
- Lead Temperature for Soldering Purposes: 260°C Maximum for 10 sec



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PIN CONNECTIONS





MARKING DIAGRAM



A = Assembly Location

Y = Year WW = Work Week AKA = Polarity Designator G = Pb-Free Package

ORDERING INFORMATION

See detailed ordering and shipping information on page 2 of this data sheet.

MAXIMUM RATINGS

Rating			Value	Unit
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage		V _{RRM} V _{RWM} V _R	100	٧
Average Rectified Forward Current (Rated V_R , T_C = 116°C) (Rated V_R , T_C = 136°C)	Per device Per diode	I _{F(AV)}	30 15	Α
Peak Repetitive Forward Current (Rated V_R , Square Wave, 20 kHz, T_C = 109°C) (Rated V_R , Square Wave, 20 kHz, T_C = 133°C)	Per device Per diode	I _{FRM}	60 30	Α
Nonrepetitive Peak Surge Current (Surge applied at rated load conditions halfwave, single phase, 60 Hz)		I _{FSM}	125	А
Operating Junction Temperature		TJ	-40 to +175	°C
Storage Temperature		T _{stg}	-40 to +175	°C

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

THERMAL CHARACTERISTICS

Rating		Symbol	Value	Unit
Typical Thermal Resistance	Junction-to-Case Junction-to-Ambient	$R_{ heta JC} \ R_{ heta JA}$	1.4 47	°C/W °C/W

ELECTRICAL CHARACTERISTICS (Per Leg unless otherwise noted)

Rating	Symbol	Тур	Max	Unit
Maximum Instantaneous Forward Voltage (Note 1) $ \begin{array}{l} (I_F=5~A,~T_J=25^\circ C)\\ (I_F=7.5~A,~T_J=25^\circ C)\\ (I_F=15~A,~T_J=25^\circ C) \end{array} $	VF	0.544 0.606 0.761	- - 0.9	V
$(I_F = 5 \text{ A}, T_J = 125^{\circ}\text{C})$ $(I_F = 7.5 \text{ A}, T_J = 125^{\circ}\text{C})$ $(I_F = 15 \text{ A}, T_J = 125^{\circ}\text{C})$		0.471 0.539 0.662	- - 0.73	
Maximum Instantaneous Reverse Current (Note 1) $(V_R = 70 \text{ V}, T_J = 25^{\circ}\text{C})$ $(V_R = 70 \text{ V}, T_J = 125^{\circ}\text{C})$		1.7 2.1		μA mA
(Rated dc Voltage, $T_J = 25^{\circ}\text{C}$) (Rated dc Voltage, $T_J = 125^{\circ}\text{C}$)		_ 5.0	85 16	μA mA

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

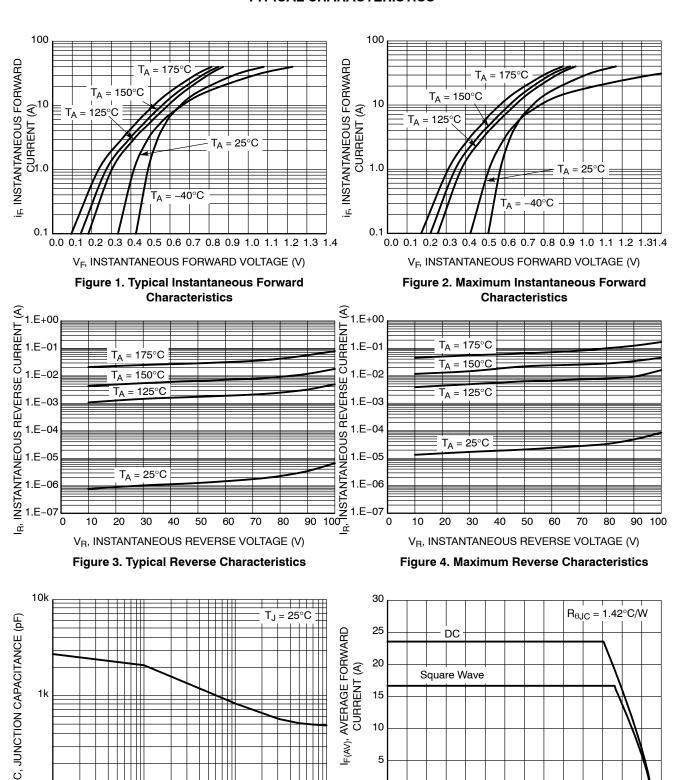
1. Pulse Test: Pulse Width = $300 \mu s$, Duty Cycle $\leq 2.0\%$

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ORDERING INFORMATION

Device	Package	Shipping
NTSV30H100ECTG	TO-220AB (Pb-Free)	50 Units / Rail

TYPICAL CHARACTERISTICS



V_R, REVERSE VOLTAGE (V) Figure 5. Typical Junction Capacitance

10

0.1

T_C, CASE TEMPERATURE (°C) Figure 6. Current Derating per Leg

120

140

180

100

100

5

0

40

60

80

TYPICAL CHARACTERISTICS

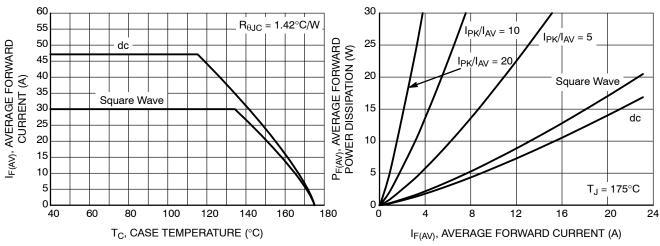


Figure 7. Current Derating per Leg

Figure 8. Forward Power Dissipation

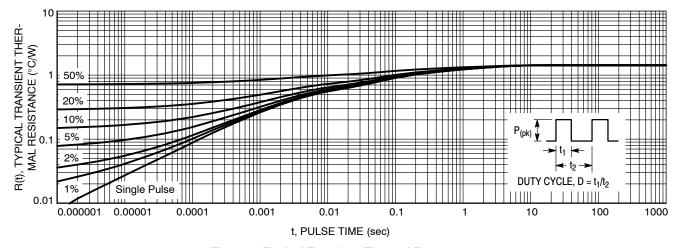
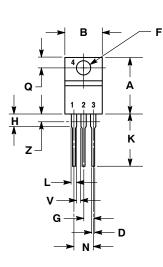
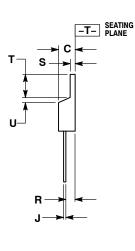


Figure 9. Typical Transient Thermal Response

PACKAGE DIMENSIONS

TO-220 CASE 221A-09 **ISSUE AH**





NOTES:

- DIMENSIONING AND TOLERANCING PER ANSI
- Y14.5M, 1982. CONTROLLING DIMENSION: INCH.
- DIMENSION Z DEFINES A ZONE WHERE ALL BODY AND LEAD IRREGULARITIES ARE ALLOWED.

	INCHES		MILLIN	IETERS
DIM	MIN	MAX	MIN	MAX
Α	0.570	0.620	14.48	15.75
В	0.380	0.415	9.66	10.53
С	0.160	0.190	4.07	4.83
D	0.025	0.038	0.64	0.96
F	0.142	0.161	3.61	4.09
G	0.095	0.105	2.42	2.66
Н	0.110	0.161	2.80	4.10
J	0.014	0.024	0.36	0.61
K	0.500	0.562	12.70	14.27
L	0.045	0.060	1.15	1.52
N	0.190	0.210	4.83	5.33
Q	0.100	0.120	2.54	3.04
R	0.080	0.110	2.04	2.79
S	0.045	0.055	1.15	1.39
T	0.235	0.255	5.97	6.47
U	0.000	0.050	0.00	1.27
٧	0.045		1.15	
Z		0.080		2.04

STYLE 6:

- ANODE PIN 1.
 - CATHODE 3.
 - ANODE CATHODE

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