# **Small Signal MOSFET**

20 V, 220 mA / -200 mA, Complementary, 1.0 x 1.0 mm SOT-963 Package

### Features

- Complementary MOSFET Device
- Offers a Low  $R_{DS(on)}$  Solution in the Ultra Small 1.0x1.0 mm Package
- 1.5 V Gate Voltage Rating
- Ultra Thin Profile (< 0.5 mm) Allows It to Fit Easily into Extremely Thin Environments such as Portable Electronics.
- This is a Pb–Free Device

### Applications

- Load Switch with Level Shift
- Optimized for Power Management in Ultra Portable Equipment

#### **MAXIMUM RATINGS** (T<sub>J</sub> = $25^{\circ}$ C unless otherwise specified)

Para	meter		Symbol	Value	Unit				
Drain-to-Source Voltag	ge		V <sub>DSS</sub>	20	V				
Gate-to-Source Voltag	е		V <sub>GS</sub>	±8	V				
N-Channel	Steady	$T_A = 25^{\circ}C$		220					
Continuous Drain Current (Note 1)	State	$T_A = 85^{\circ}C$		160	mA				
, , , , , , , , , , , , , , , , , , ,	t ≤ 5 s	$T_A = 25^{\circ}C$		280					
P-Channel	Steady	$T_A = 25^{\circ}C$	I <sub>D</sub>	-200					
Continuous Drain Current (Note 1)	State	$T_A = 85^{\circ}C$		-140					
, , , , , , , , , , , , , , , , , , ,	t ≤ 5 s	$T_A = 25^{\circ}C$		-250					
Power Dissipation	Steady			125					
(Note 1)	State	$T_A = 25^{\circ}C$	PD		mW				
	t ≤ 5 s			200					
Pulsed Drain Current	N-Channel	t 10.00	1	800	m۸				
	P-Channel	t <sub>p</sub> = 10 μs	I <sub>DM</sub>	-600	mA				
Operating Junction and	I Storage Tem	perature	TJ,	-55 to	°C				
			T <sub>STG</sub>	150					
Source Current (Body I	Source Current (Body Diode) (Note 2)			200	mA				
Lead Temperature for S (1/8" from case for 1		oses	ΤL	260	°C				

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

 Surface-mounted on FR4 board using the minimum recommended pad size, 1 oz. Cu.

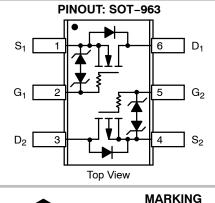
2. Pulse Test: pulse width  $\leq$  300 µs, duty cycle  $\leq$  2%



## **ON Semiconductor®**

#### http://onsemi.com

V <sub>(BR)DSS</sub>	R <sub>DS(on)</sub> Max	I <sub>D</sub> Max
	1.5 Ω @ 4.5 V	
N-Channel	2.0 Ω @ 2.5 V	]
20 V	3.0 Ω @ 1.8 V	0.22 A
	4.5 Ω @ 1.5 V	
	5.0 Ω @ –4.5 V	
P-Channel	6.0 Ω @ –2.5 V	_0.2 A
20 V	7.0 Ω @ –1.8 V	0.2 A
	10 Ω @ –1.5 V	





### **ORDERING INFORMATION**

Device	Package	Shipping <sup>†</sup>
NTUD3169CZT5G	SOT-963 (Pb-Free)	8000 / Tape & Reel

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

### THERMAL RESISTANCE RATINGS

Parameter	Symbol	Мах	Unit
Junction-to-Ambient - Steady State, Minimum Pad (Note 3)	$R_{ hetaJA}$	1000	°C/W
Junction-to-Ambient – t $\leq$ 5 s (Note 3)		600	

3. Surface-mounted on FR4 board using the minimum recommended pad size, 1 oz. Cu.

### **ELECTRICAL CHARACTERISTICS** (T<sub>J</sub> = 25°C unless otherwise specified)

Parameter	Symbol	N/P	Test Condition	on	Min	Тур	Max	Unit
OFF CHARACTERISTICS								
Drain-to-Source Breakdown Voltage	M	Ν		I <sub>D</sub> = 250 μA	20			v
	V <sub>(BR)DSS</sub>	Р	$V_{GS} = 0 V$	I <sub>D</sub> = -250 μA	-20			
Zero Gate Voltage Drain Current		N		$T_J = 25^{\circ}C$			50	nA
		IN	V <sub>GS</sub> = 0 V, V <sub>DS</sub> = 5.0 V	$T_J = 85^{\circ}C$			200	
	IDSS	Р		$T_J = 25^{\circ}C$			-50	
		Р	$V_{GS}$ = 0 V, $V_{DS}$ = -5.0 V	$T_J = 85^{\circ}C$			-200	
Zero Gate Voltage Drain Current		Ν	$V_{GS}$ = 0 V, $V_{DS}$ = 16 V	Τ 05%Ο			100	-
	I <sub>DSS</sub>	Р	$V_{GS}$ = 0 V, $V_{DS}$ = -16 V	T <sub>J</sub> = 25°C			-100	nA
Gate-to-Source Leakage Current	1	Ν					±100	
	I <sub>GSS</sub>	Р	V <sub>DS</sub> = 0 V, V <sub>GS</sub> =	±3.0 v			±100	nA

### ON CHARACTERISTICS (Note 4)

Gate Threshold Voltage		Ν	$V_{GS} = V_{DS}$	I <sub>D</sub> = 250 μA	0.4		1.0	V
	V <sub>GS(TH)</sub>	Р		I <sub>D</sub> = -250 μA	-0.4		-1.0	
Drain-to-Source On Resistance		Ν	$V_{GS}$ = 4.5 V, I <sub>D</sub> = $^{-1}$	100 mA		0.75	1.5	
		Р	V <sub>GS</sub> = -4.5V, I <sub>D</sub> = -	100 mA		2.0	5.0	Ω
		Ν	$V_{GS}$ = 2.5 V, I <sub>D</sub> =	50 mA		1.0	2.0	
		Р	$V_{GS} = -2.5V, I_D = -2.5V$	–50 mA		2.6	6.0	
	Р	Ν	V <sub>GS</sub> = 1.8 V, I <sub>D</sub> =	20 mA		1.4	3.0	
	R <sub>DS(on)</sub>	Р	$V_{GS} = -1.8V, I_D = -1.8V$	–20 mA		3.4	7.0	
		Ν	V <sub>GS</sub> = 1.5 V, I <sub>D</sub> =	10 mA		1.8	4.5	
		Р	$V_{GS} = -1.5 \text{ V}, \text{ I}_{D} = -1.5 \text{ V}$	–10 mA		4.0	10	
		Ν	$V_{GS} = 1.2 \text{ V}, \text{ I}_{D} = 1.2 \text{ V}$	1.0 mA		2.8		
		Р	$V_{GS} = -1.2 \text{ V}, \text{ I}_{D} = -1.2 \text{ V}$	–1.0 mA		6.0		
Forward Transconductance	ñ	Ν	V <sub>DS</sub> = 5.0 V, I <sub>D</sub> = 1	125 mA		0.48		S
	9fs	Р	$V_{DS}$ = -5.0 V, I <sub>D</sub> = -	-125 mA		0.35		3
Source-Drain Diode Voltage	V <sub>SD</sub>	Ν	$V_{GS}$ = 0 V, I <sub>S</sub> = 10 mA	$T_J = 25^{\circ}C$		0.6	1.0	V
		Р	$V_{GS} = 0 \text{ V}, \text{ I}_{S} = -10 \text{ mA}$			-0.6	-1.0	

#### CAPACITANCES

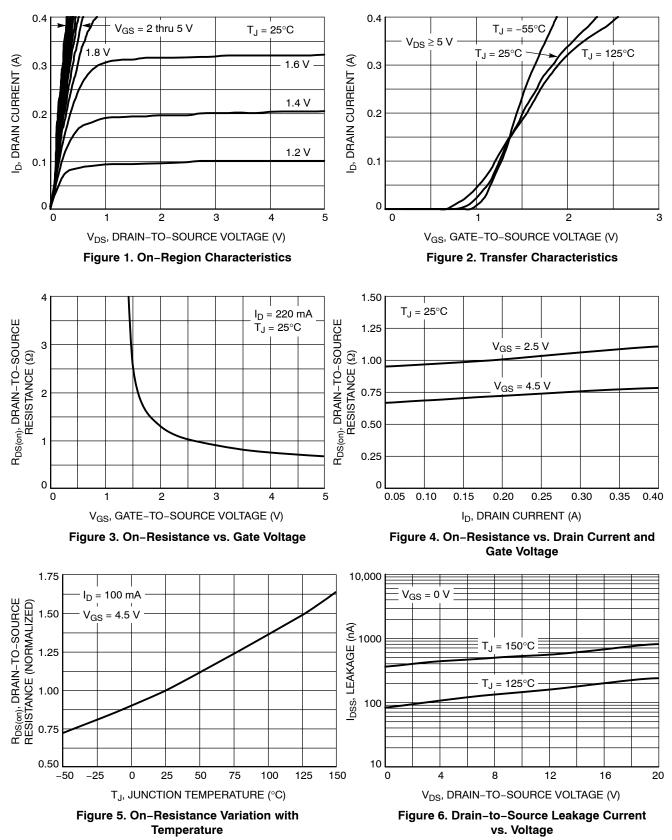
Input Capacitance	C <sub>ISS</sub>			1	12.5	
Output Capacitance	C <sub>OSS</sub>	Ν	f = 1 MHz, V <sub>GS</sub> = 0 V V <sub>DS</sub> = 15 V	:	3.6	
Reverse Transfer Capacitance	C <sub>RSS</sub>	1		:	2.6	
Input Capacitance	C <sub>ISS</sub>			1	13.5	рF
Output Capacitance	C <sub>OSS</sub>	Р	f = 1 MHz, V <sub>GS</sub> = 0 V V <sub>DS</sub> = -15 V	:	3.8	1
Reverse Transfer Capacitance	C <sub>RSS</sub>	1		:	2.0	1

4. Switching characteristics are independent of operating junction temperatures

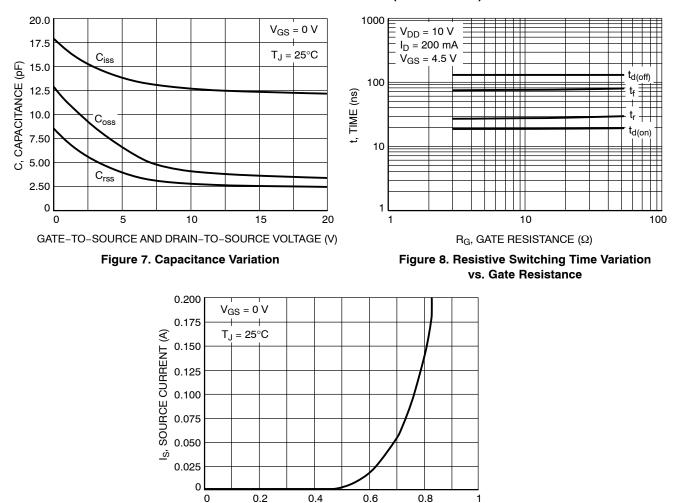
### **ELECTRICAL CHARACTERISTICS** (T<sub>J</sub> = $25^{\circ}$ C unless otherwise specified)

Parameter	Symbol	N/P	Test Condition	Min	Тур	Max	Unit
SWITCHING CHARACTERISTICS, V	<b>GS = 4.5 V</b> (Not	e 4)					
Turn-On Delay Time	t <sub>d(ON)</sub>		V <sub>GS</sub> = 4.5 V, V <sub>DD</sub> = 10 V, I <sub>D</sub> = 200 mA,		16.5		ns
Rise Time	t <sub>r</sub>	N			25.5		
Turn-Off Delay Time	t <sub>d(OFF)</sub>	-	$R_{\rm G}^{\rm c} = 2.0 \ \Omega$		142		
Fall Time	t <sub>f</sub>				80		
Turn-On Delay Time	t <sub>d(ON)</sub>				26		
Rise Time	t <sub>r</sub>		V <sub>GS</sub> = -4.5 V, V <sub>DD</sub> = -15 V,		46		
Turn-Off Delay Time	t <sub>d(OFF)</sub>	P	$I_D = -200 \text{ mA}, R_G = 2.0 \Omega$		196		
Fall Time	t <sub>f</sub>				145		

4. Switching characteristics are independent of operating junction temperatures

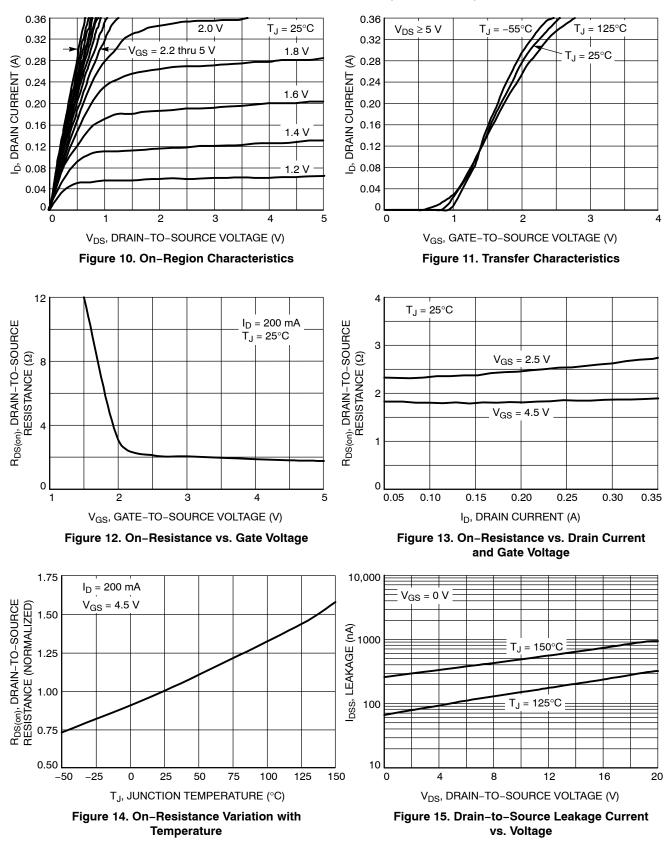


### **TYPICAL CHARACTERISTICS (N-CHANNEL)**



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V<sub>SD</sub>, SOURCE-TO-DRAIN VOLTAGE (V) Figure 9. Diode Forward Voltage vs. Current



#### **TYPICAL CHARACTERISTICS (P-CHANNEL)**

### **TYPICAL CHARACTERISTICS (P-CHANNEL)**

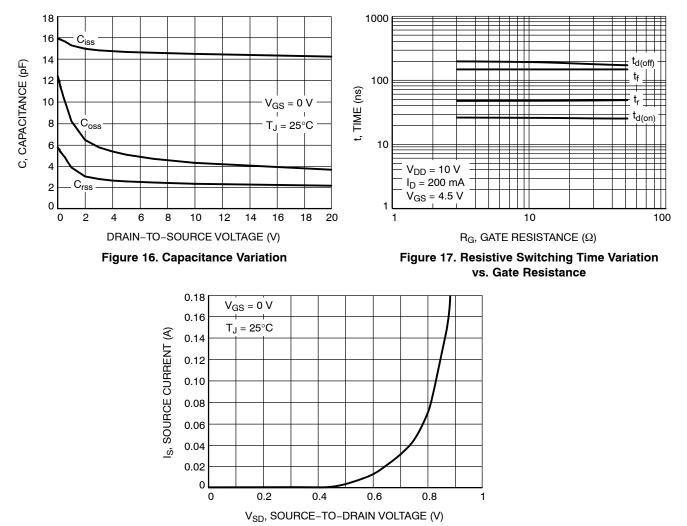
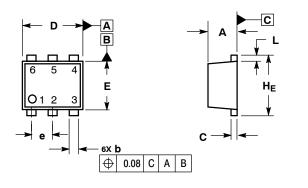


Figure 18. Diode Forward Voltage vs. Current

#### PACKAGE DIMENSIONS

SOT-963 CASE 527AD-01 ISSUE D

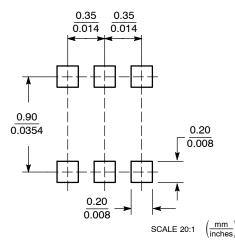


NOTES:

- DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
- TI4-300, 1962. CONTROLLING DIMENSION: MILLIMETERS MAXIMUM LEAD THICKNESS INCLUDES LEAD FINISH THICKNESS, MINIMUM LEAD THICKNESS IS THE MINIMUM THICKNESS OF BASE MATERIAL. 3.

IES	INCHES		RS	MIL		
M MAX	MIN NOM		MAX	NOM	MIN	DIM
			0.40	0.37	0.34	Α
06 0.00	0.006	0.004	0.20	0.15	0.10	b
05 0.00	0.005	0.003	0.17	0.12	0.07	С
39 0.04	0.039	0.037	1.05	1.00	0.95	D
32 0.03	0.03 0.032		0.85	0.80	0.75	ш
BSC	0.014 BSC			0.35 BS		e
04 0.00	0.004	0.002	0.15	0.10	0.05	L
39 0.04	0.039	0.037	1.05	1.00	0.95	ΗE
04 0.0	0.15	0.10	0.05	L		

#### **SOLDERING FOOTPRINT\***



\*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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