SB02-09C

Schottky Barrier Diode 90V, 0.2A, Low IR, Single CP



Applications

• High frequency rectification (switching regulators, converters, choppers)

Features

- Low forward voltage (VF max=0.7V)
- Fast reverse recovery time (t_{rr} max=10ns)

- Low switching noise
- · Low leakage current and high reliability due to highly reliable planar structure

Specifications

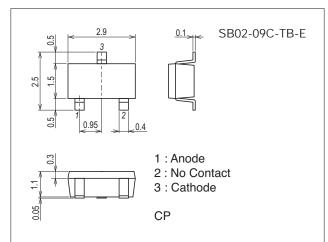
Absolute Maximum Ratings at Ta=25°C

Parameter	Symbol	Conditions	Ratings	Unit
Repetitive Peak Reverse Voltage	V _{RRM}		90	V
Nonrepetitive Peak Reverse Surge Voltage	VRSM		95	V
Average Output Current	IO		200	mA
Surge Forward Current	IFSM	50Hz sine wave, 1 cycle	5	А
Junction Temperature	Tj		-55 to +125	°C
Storage Temperature	Tstg		-55 to +125	°C

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

Package Dimensions

unit : mm (typ) 7013A-004

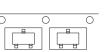


Product & Package Information

- Package
- JEITA, JEDEC
- Minimum Packing Quantity : 3,000 pcs./reel

: CP

Packing Type: TB

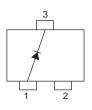




Marking

: SC-59, TO-236, SOT-23, TO-236AB

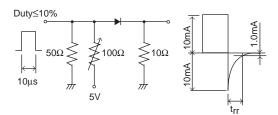
Electrical Connection



Electrical Characteristics at Ta=25°C

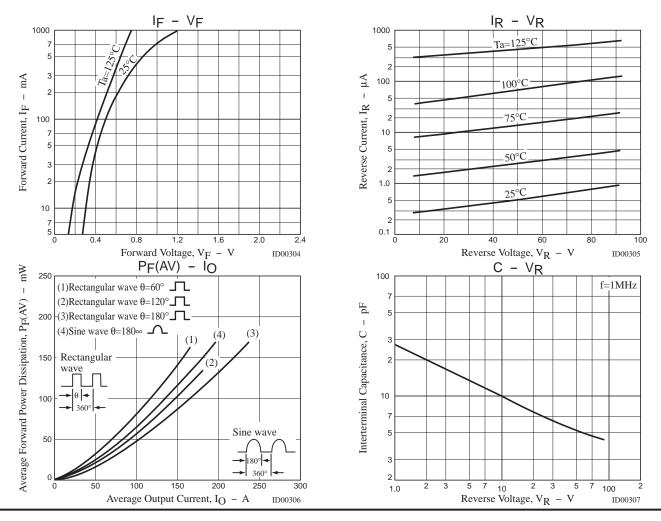
Parameter	Symbol	Conditions	Ratings			Unit
		Conditions	min	typ	max	Unit
Reverse Voltage	VR	I _R =200μA	90			V
Forward Voltage	VF	IF=200mA			0.7	V
Reverse Current	IR	V _R =45V			50	μΑ
Interterminal Capacitance	С	V _R =10V, f=1MHz		10		рF
Reverse Recovery Time	t _{rr}	IF=IR=100mA, See specified Test Circuit.			10	ns
Thermal Resistance	Rth(j-a)1			420		°C / W
	Rth(j-a)2	Mounted in Cu-foiled area of 16mm ² ×0.2mm		330		°C/W
		on glass epoxy board		330		C / W

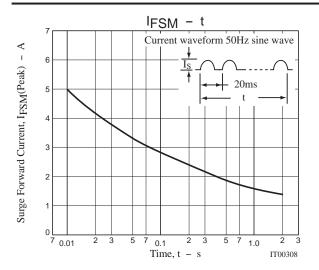
trr Test Circuit



Ordering Information

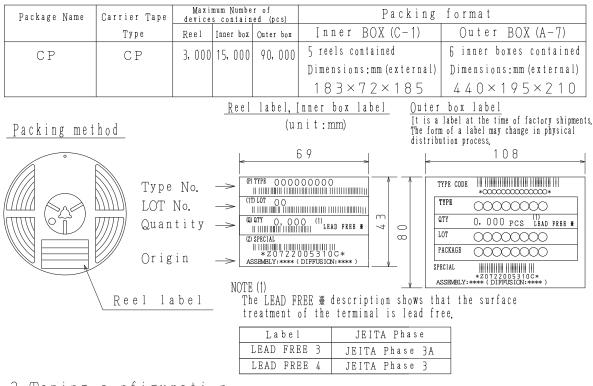
Device	Package	Shipping	memo					
SB02-09C-TB-E	02-09C-TB-E CP		Pb Free					



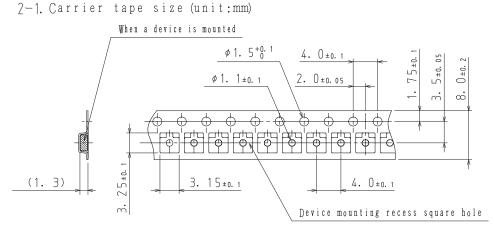


Embossed Taping Specification SB02-09C-TB-E

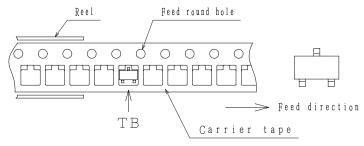
1. Packing Format



2. Taping configuration

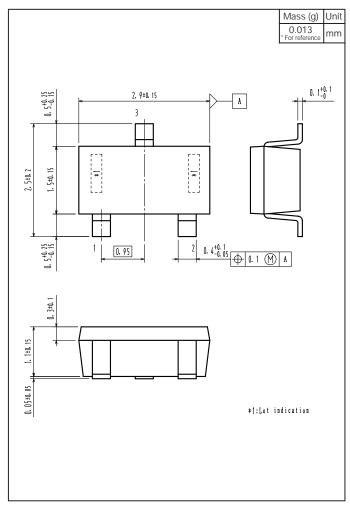


2-2. Device placement direction

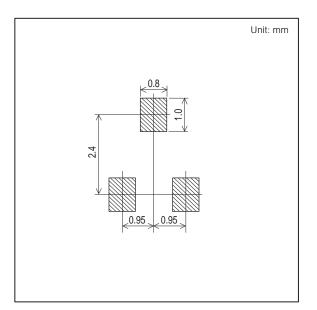


Those with one electrode terminal on the feed hole side TB

Outline Drawing SB02-09C-TB-E



Land Pattern Example



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