Power MOSFET 20V, 64mΩ, 3.5A, Single N-Channel

This low-profile high-power MOSFET is produced using ON Semiconductor's trench technology, which is specifically designed to minimize gate charge and ultra low on resistance. This device is suitable for applications with low gate charge driving or ultra low on resistance requirements.

Features

- Low On-Resistance
- Low Capacitance
- 1.8V drive
- ESD Diode-Protected Gate
- Pb-Free, Halogen Free and RoHS compliance
- Ultra small package SCH6 (1.6mm×1.6mm×0.56mmt)

Typical Applications

Load Switch

SPECIFICATIONS

ABSOLUTE MAXIMUM RATING at Ta = 25°C (Note 1)

| Parameter | Symbol | Value | Unit |
|--------------------------------------------------------------------|--------|-------------|----------|
| Drain to Source Voltage | VDSS | 20 | V |
| Gate to Source Voltage | VGSS | ±10 | ٧ |
| Drain Current (DC) | ID | 3.5 | Α |
| Drain Current (Pulse) PW ≤ 10µs, duty cycle ≤ 1% | IDP | 14 | Α |
| Power Dissipation When mounted on ceramic substrate (900mm²×0.8mm) | PD | 0.8 | W |
| Junction Temperature | Tj | 150 | °C |
| Storage Temperature | Tstg | -55 to +150 | °C |

Note 1: Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

THERMAL RESISTANCE RATINGS

| Parameter | Symbol | Value | Unit | | | |
|----------------------------------------------------------------------|-----------------|-------|------|--|--|--|
| Junction to Ambient When mounted on ceramic substrate (900mm²×0.8mm) | $R_{\theta JA}$ | 156.2 | °C/W | | | |

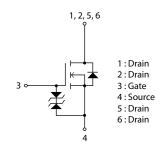


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| VDSS | R _{DS} (on) Max | ID Max |
|------|--------------------------|--------|
| 20V | 64mΩ@ 4.5V | |
| | 95mΩ@ 2.5V | 3.5A |
| | 149mΩ@ 1.8V | |

ELECTRICAL CONNECTION N-Channel



PACKING TYPE: TL

TL TL



ORDERING INFORMATION

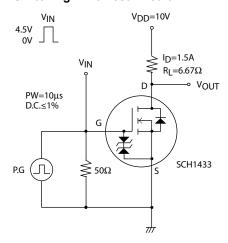
See detailed ordering and shipping information on page 5 of this data sheet.

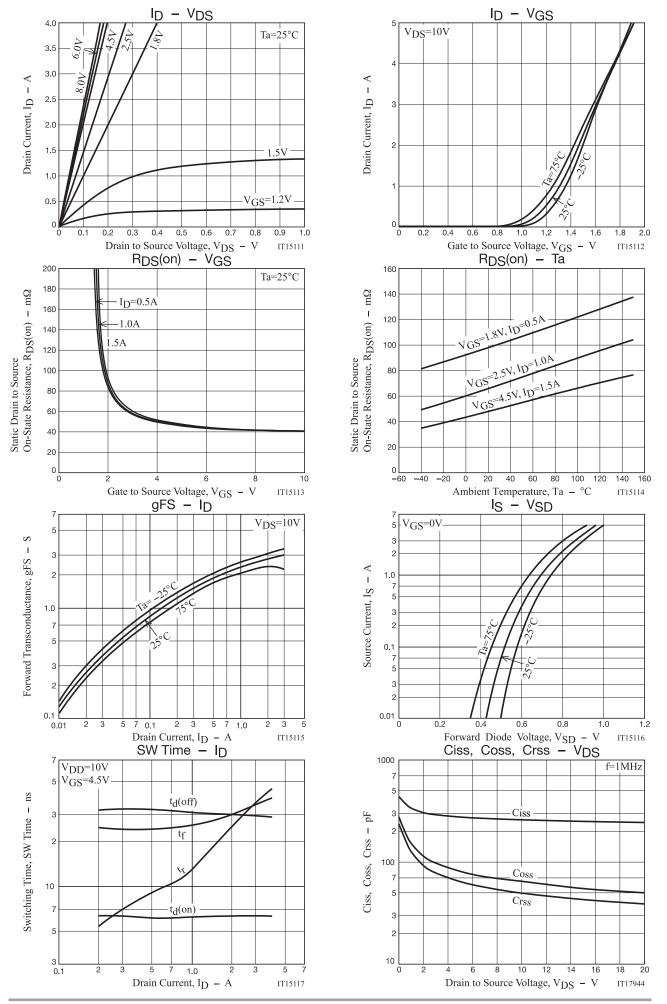
ELECTRICAL CHARACTERISTICS at Ta = 25°C (Note 2)

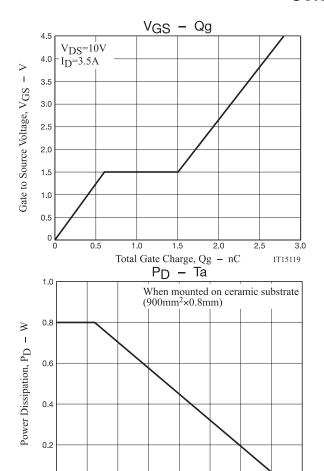
| Parameter | Symbol | Conditions | Value | | | Unit | |
|--------------------------------------------|-----------------------|-------------------------------------------------------------------|-------|------|-----|-----------|--|
| Parameter Symbol Conditions | | Conditions | min | typ | max | Offic | |
| Drain to Source Breakdown Voltage | V(BR)DSS | ID=1mA, VGS=0V | 20 | | | V | |
| Zero-Gate Voltage Drain Current | IDSS | V _{DS} =20V, V _{GS} =0V | | | 1 | μΑ | |
| Gate to Source Leakage Current | IGSS | V _{GS} =±8V, V _{DS} =0V | | | ±10 | μΑ | |
| Gate Threshold Voltage | V _G S(th) | V _{DS} =10V, I _D =1mA | 0.4 | | 1.3 | V | |
| Forward Transconductance | gFS | V _{DS} =10V, I _D =1.5A 1.6 | | 2.8 | | S | |
| | R _{DS} (on)1 | I _D =1.5A, V _{GS} =4.5V | | 49 | 64 | $m\Omega$ | |
| Static Drain to Source On-State Resistance | R _{DS} (on)2 | I _D =1A, V _{GS} =2.5V | | 68 | 95 | $m\Omega$ | |
| Resistance | R _{DS} (on)3 | I _D =0.5A, V _{GS} =1.8V | | 99 | 149 | $m\Omega$ | |
| Input Capacitance | Ciss | | | 260 | | pF | |
| Output Capacitance | Coss | V _{DS} =10V, f=1MHz | | 65 | | pF | |
| Reverse Transfer Capacitance | Crss | | | 50 | | pF | |
| Turn-ON Delay Time | t _d (on) | | | 6.2 | | ns | |
| Rise Time | tr | Can appoint Toot Circuit | | 19 | | ns | |
| Turn-OFF Delay Time | t _d (off) | See specified Test Circuit | | 30 | | ns | |
| Fall Time | tf | | | 28 | | ns | |
| Total Gate Charge | Qg | | | 2.8 | | nC | |
| Gate to Source Charge | Qgs | V _{DS} =15V, V _{GS} =4.5V, I _D =3.5A | | 0.6 | | nC | |
| Gate to Drain "Miller" Charge | Qgd | | | 0.9 | | nC | |
| Forward Diode Voltage | V _{SD} | IS=3.5A, VGS=0V | | 0.85 | 1.2 | V | |

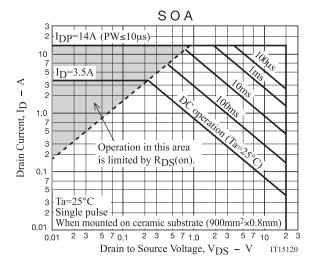
Note 2 : Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

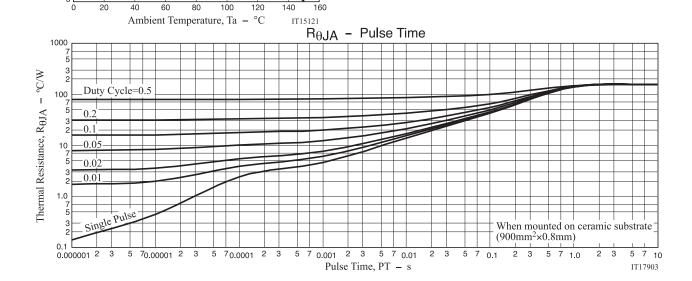
Switching Time Test Circuit





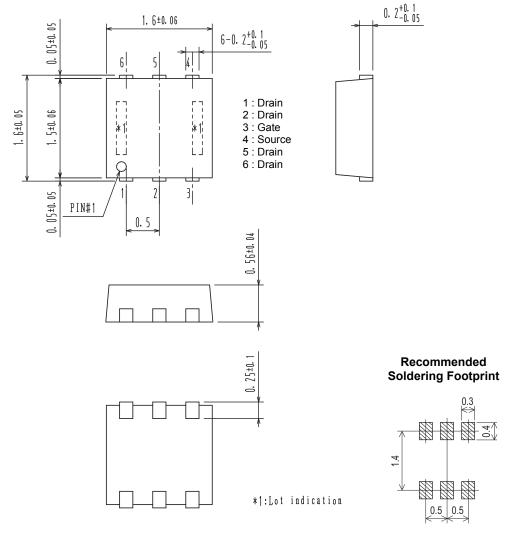






PACKAGE DIMENSIONS

unit: mm SOT-563 / SCH6 CASE 463AB ISSUE O



ORDERING INFORMATION

| Device | Marking | Package | Shipping (Qty / Packing) | |
|--------------|-------------|----------------|--------------------------|--|
| SCH1433-TL-H | 71 | SOT-563 / SCH6 | 5 000 / Tana & Daal | |
| SCH1433-TL-W | CH1433-TL-W | | 5,000 / Tape & Reel | |

[†] For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D. http://www.onsemi.com/pub_link/Collateral/BRD8011-D.PDF

Note on usage: Since the SCH1433 is a MOSFET product, please avoid using this device in the vicinity of highly charged objects.

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