# BAW56WT1G, SBAW56WT1G

# Dual Switching Diode, Common Anode

### Features

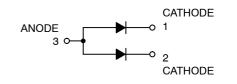
- AEC-Q101 Qualified and PPAP Capable
- S Prefix for Automotive and Other Applications Requiring Unique Site and Control Change Requirements
- These Devices are Pb–Free, Halogen Free/BFR Free and are RoHS Compliant\*



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## **MAXIMUM RATINGS** ( $T_A = 25^{\circ}C$ )

Rating	Symbol	Мах	Unit
Reverse Voltage	V <sub>R</sub>	70	V
Forward Current	١ <sub>F</sub>	200	mA
Peak Forward Surge Current	I <sub>FM(surge)</sub>	500	mA

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

#### THERMAL CHARACTERISTICS (T<sub>A</sub> = 25°C)

Characteristic	Symbol	Мах	Unit
Total Device Dissipation FR-5 Board (Note 1) $T_{A} = 25^{\circ}C$	P <sub>D</sub>	200	mW
Derate above 25°C		1.6	mW/°C
Thermal Resistance, Junction-to-Ambient	$R_{\thetaJA}$	625	°C/W
Total Device Dissipation Alumina Substrate (Note 2) $T_A = 25^{\circ}C$	PD	300	mW
Derate above 25°C		2.4	mW/°C
Thermal Resistance, Junction-to-Ambient	$R_{\theta JA}$	417	°C/W
Junction and Storage Temperature	T <sub>J</sub> , T <sub>stg</sub>	– 55 to +150	°C

1. FR–5 = 1.0  $\times$  0.75  $\times$  0.062 in.

2. Alumina = 0.4  $\times$  0.3  $\times$  0.024 in. 99.5% alumina.

### MARKING DIAGRAM



A1 = Device Code M = Date Code\*

.

= Pb-Free Package

(Note: Microdot may be in either location)

\*Date Code orientation may vary depending upon manufacturing location.

#### **ORDERING INFORMATION**

Device	Package	Shipping <sup>†</sup>
BAW56WT1G	SC–70 (Pb–Free)	3,000 / Tape & Reel
SBAW56WT1G	SC–70 (Pb–Free)	3,000 / Tape & Reel

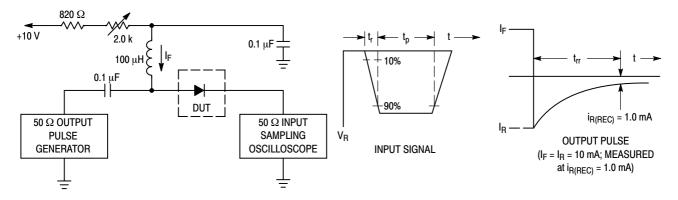
†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

\*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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#### **ELECTRICAL CHARACTERISTICS** (T<sub>A</sub> = 25°C unless otherwise noted)

Characteristic	Symbol	Min	Max	Unit	
OFF CHARACTERISTICS					
Reverse Breakdown Voltage $(I_{(BR)} = 100 \ \mu A)$	V <sub>(BR)</sub>	70	-	V	
Reverse Voltage Leakage Current ( $V_R = 25 \text{ V}, T_J = 150^{\circ}\text{C}$ ) ( $V_R = 70 \text{ V}$ ) ( $V_R = 70 \text{ V}, T_J = 150^{\circ}\text{C}$ )	I <sub>R</sub>	- - -	30 2.5 50	μΑ	
Diode Capacitance (V <sub>R</sub> = 0, f = 1.0 MHz)	C <sub>D</sub>	_	2.0	pF	
Forward Voltage $(I_F = 1.0 \text{ mA})$ $(I_F = 10 \text{ mA})$ $(I_F = 60 \text{ mA})$ $(I_F = 150 \text{ mA})$	VF	- - -	715 855 1000 1250	mV	
Reverse Recovery Time (I <sub>F</sub> = I <sub>R</sub> = 10 mA, R <sub>L</sub> = 100 $\Omega$ , I <sub>R(REC)</sub> = 1.0 mA) (Figure 1)	t <sub>rr</sub>	_	6.0	ns	



Notes: 1. A 2.0 k $\Omega$  variable resistor adjusted for a Forward Current (I<sub>F</sub>) of 10 mA.

2. Input pulse is adjusted so  $I_{R(peak)}$  is equal to 10 mA.

3. t<sub>p</sub> » t<sub>rr</sub>

Figure 1. Recovery Time Equivalent Test Circuit

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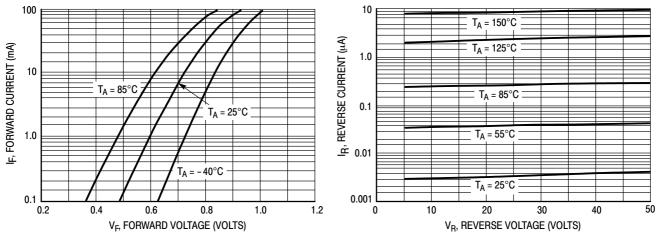


Figure 2. Forward Voltage

Figure 3. Leakage Current

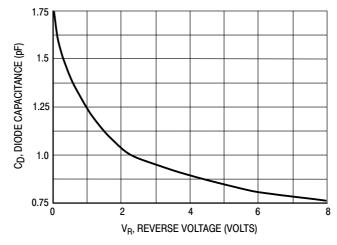
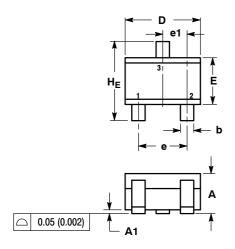


Figure 4. Capacitance

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#### PACKAGE DIMENSIONS

SC-70 (SOT-323) CASE 419-04 ISSUE N



NOTES: 1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.

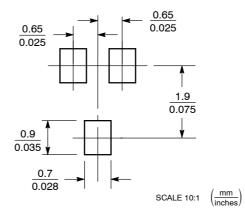
DIMENSIONING AND TOLERANCING PER ANSI 114.5M, 1982.
CONTROLLING DIMENSION: INCH.

	MILLIMETERS			INCHES		
DIM	MIN	NOM	MAX	MIN	NOM	MAX
Α	0.80	0.90	1.00	0.032	0.035	0.040
A1	0.00	0.05	0.10	0.000	0.002	0.004
A2	0.70 REF			0.028 REF		
b	0.30	0.35	0.40	0.012	0.014	0.016
c	0.10	0.18	0.25	0.004	0.007	0.010
D	1.80	2.10	2.20	0.071	0.083	0.087
Е	1.15	1.24	1.35	0.045	0.049	0.053
е	1.20	1.30	1.40	0.047	0.051	0.055
e1	0.65 BSC			0.026 BSC		
L	0.20	0.38	0.56	0.008	0.015	0.022
HE	2.00	2.10	2.40	0.079	0.083	0.095

STYLE 4: PIN 1. CATHODE 2. CATHODE

3. ANODE

#### **SOLDERING FOOTPRINT\***



\*For additional information on our Pb–Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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